# SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

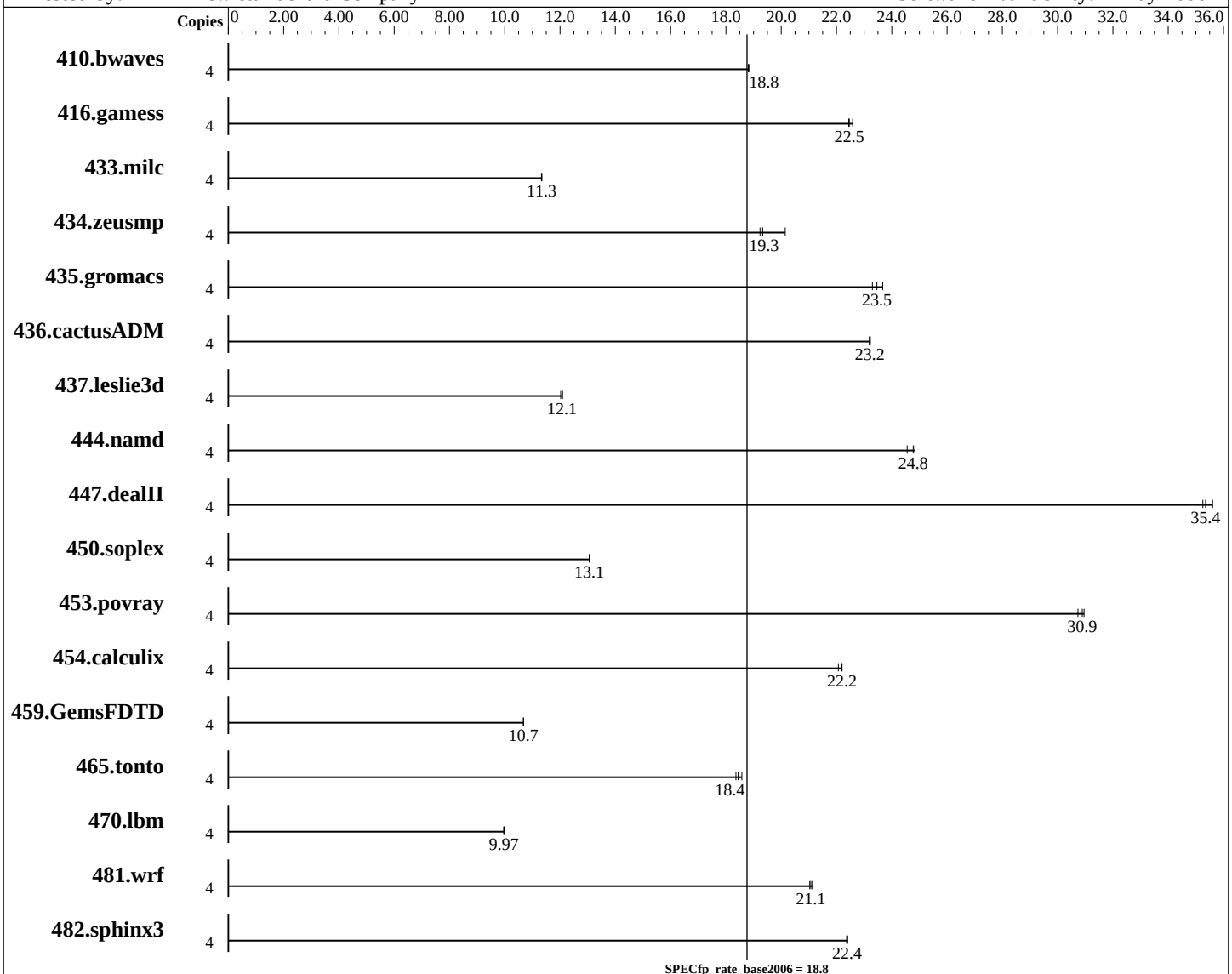
ProLiant DL380 G4 (3.8GHz, Intel Xeon Processor)

SPECfp®_rate2006 = --

SPECfp_rate_base2006 = 18.8

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Apr-2006
**Hardware Availability:** Sep-2005
**Software Availability:** May-2006




### Hardware

| | |
|---|---|
| CPU Name: | Intel Xeon |
| CPU Characteristics: | 3.8GHz, 2MB L2, 800MHz bus |
| CPU MHz: | 3800 |
| FPU: | Integrated |
| CPU(s) enabled: | 2 cores, 2 chips, 1 core/chip, 2 threads/core |
| CPU(s) orderable: | 1,2 chips |
| Primary Cache: | 12 K micro-ops I + 16 KB D on chip per chip |
| Secondary Cache: | 2 MB I+D on chip per chip |

Continued on next page

### Software

| | |
|---|---|
| Operating System: | Windows Server 2003 Ent, SP1 |
| Compiler: | Intel C++ Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_CC_P_9.1.020<br>Intel Visual Fortran Compiler 9.1 for 32-bit apps, Build 20060323Z Package ID: W_FC_P_9.1.020<br>Microsoft Visual Studio .NET 2003 (for libraries)<br>MicroQuill SmartHeap Library 7.4 |
| Auto Parallel: | No |
| File System: | NTFS |
| System State: | Default |

Continued on next page

# SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

## Hewlett-Packard Company

ProLiant DL380 G4 (3.8GHz, Intel Xeon Processor)

SPECfp_rate2006 = --

SPECfp_rate_base2006 = 18.8

**CPU2006 license:** 3
**Test sponsor:** Hewlett-Packard Company
**Tested by:** Hewlett-Packard Company

**Test date:** Apr-2006
**Hardware Availability:** Sep-2005
**Software Availability:** May-2006

L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-3200)
Disk Subsystem: 146GB 10k U320 SCSI
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: None

## Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---|---|---|---|---|---|---|---|---|---|---|---|---|---|---|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 4 | 2888 | 18.8 | 2889 | 18.8 | **2889** | **18.8** | | | | | | | |
| 416.gamess | 4 | 3490 | 22.4 | 3468 | 22.6 | **3487** | **22.5** | | | | | | | |
| 433.milc | 4 | 3238 | 11.3 | 3242 | 11.3 | **3239** | **11.3** | | | | | | | |
| 434.zeusmp | 4 | 1893 | 19.2 | **1883** | **19.3** | 1807 | 20.1 | | | | | | | |
| 435.gromacs | 4 | 1207 | 23.7 | **1217** | **23.5** | 1226 | 23.3 | | | | | | | |
| 436.cactusADM | 4 | 2058 | 23.2 | 2062 | 23.2 | **2059** | **23.2** | | | | | | | |
| 437.leslie3d | 4 | **3113** | **12.1** | 3111 | 12.1 | 3127 | 12.0 | | | | | | | |
| 444.namd | 4 | 1292 | 24.8 | 1306 | 24.6 | **1295** | **24.8** | | | | | | | |
| 447.dealII | 4 | **1294** | **35.4** | 1285 | 35.6 | 1298 | 35.3 | | | | | | | |
| 450.soplex | 4 | **2554** | **13.1** | 2552 | 13.1 | 2554 | 13.1 | | | | | | | |
| 453.povray | 4 | 687 | 31.0 | 692 | 30.7 | **689** | **30.9** | | | | | | | |
| 454.calculix | 4 | 1487 | 22.2 | 1495 | 22.1 | **1487** | **22.2** | | | | | | | |
| 459.GemsFDTD | 4 | 3994 | 10.6 | **3976** | **10.7** | 3975 | 10.7 | | | | | | | |
| 465.tonto | 4 | 2119 | 18.6 | 2144 | 18.4 | **2134** | **18.4** | | | | | | | |
| 470.lbm | 4 | 5512 | 9.97 | 5515 | 9.97 | **5512** | **9.97** | | | | | | | |
| 481.wrf | 4 | 2115 | 21.1 | 2124 | 21.0 | **2120** | **21.1** | | | | | | | |
| 482.sphinx3 | 4 | **3482** | **22.4** | 3486 | 22.4 | 3480 | 22.4 | | | | | | | |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

## Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

## Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.20090715.01.xml


SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.


For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v90.
Report generated on Tue Jul 22 09:57:01 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 August 2006.