SPEC® CINT2006 Result

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Hewlett-Packard Company

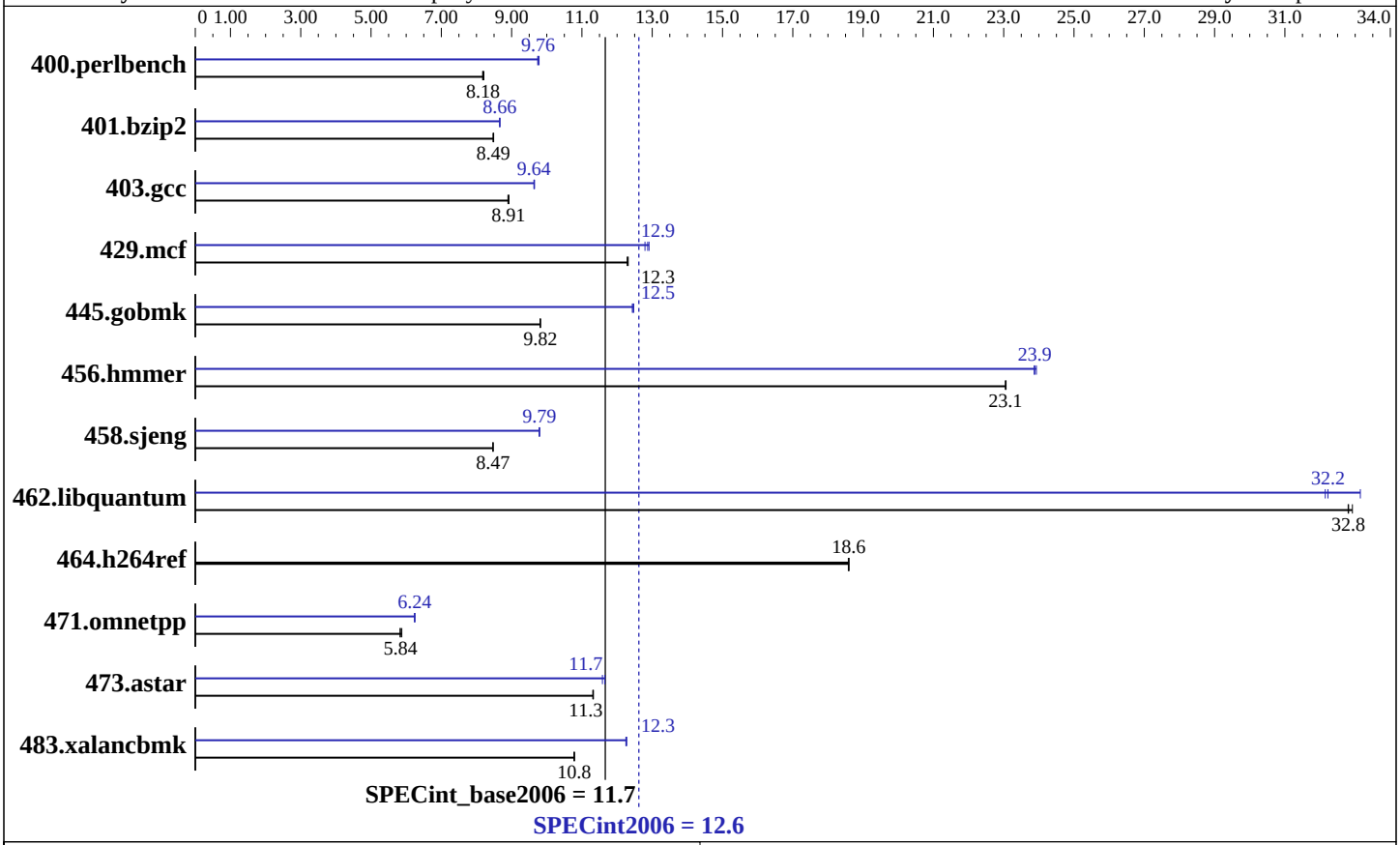
HP Integrity rx2620
(1.4GHz/12MB Dual-Core Intel Itanium 2)

SPECint®2006 = 12.6

SPECint_base2006 = 11.7

CPU2006 license: 03
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Oct-2006
Hardware Availability: Sep-2006
Software Availability: Sep-2006



Hardware

CPU Name:	Dual-Core Intel Itanium 2 9015
CPU Characteristics:	1.4GHz/12MB, 400MHz FSB
CPU MHz:	1400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	16 KB I + 16 KB D on chip per core
Secondary Cache:	1 MB I + 256 KB D on chip per core
L3 Cache:	6 MB I+D on chip per core
Other Cache:	None
Memory:	24 GB (12x2GB DIMMs)
Disk Subsystem:	146GB 10K RPM SCSI
Other Hardware:	None

Software

Operating System:	HPUX11i-TCOE B.11.23.0609
Compiler:	HP C/aC++ Developer's Bundle C.11.23.12
Auto Parallel:	No
File System:	vxfs
System State:	Multi-user
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill Smartheap 8.0

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1195	8.18	**1194**	**8.18**	1190	8.21	999	9.78	**1001**	**9.76**	1003	9.74
401.bzip2	1139	8.47	1137	8.49	**1137**	**8.49**	1114	8.66	1114	8.66	**1114**	**8.66**
403.gcc	902	8.92	**903**	**8.91**	905	8.89	834	9.65	**835**	**9.64**	835	9.64
429.mcf	742	12.3	**741**	**12.3**	740	12.3	**709**	**12.9**	706	12.9	713	12.8
445.gobmk	**1068**	**9.82**	1068	9.82	1069	9.81	**842**	**12.5**	841	12.5	844	12.4
456.hmmer	405	23.1	404	23.1	**405**	**23.1**	**391**	**23.9**	390	23.9	391	23.9
458.sjeng	**1428**	**8.47**	1429	8.47	1428	8.47	**1235**	**9.79**	1235	9.80	1237	9.78
462.libquantum	632	32.8	**631**	**32.8**	629	32.9	**643**	**32.2**	625	33.1	644	32.1
464.h264ref	1190	18.6	**1190**	**18.6**	1191	18.6	1190	18.6	**1190**	**18.6**	1191	18.6
471.omnetpp	**1071**	**5.84**	1073	5.83	1064	5.88	**1002**	**6.24**	1000	6.25	1002	6.24
473.astar	620	11.3	**620**	**11.3**	621	11.3	**603**	**11.7**	602	11.7	606	11.6
483.xalancbmk	**640**	**10.8**	639	10.8	640	10.8	563	12.3	562	12.3	**563**	**12.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
The system had the September 2006 HP-UX 11i v2 Technical Computing
Operating Environment (TCOE) and compilers installed, along with
the following patches:

PHSS_34858  linker + fdp cumulative patch
PHSS_34853  Math Library Cumulative Patch
PHSS_34854  Integrity Unwind Library
PHSS_34855  HP C Compiler (A.06.12)
PHSS_34856  aC++ Compiler (A.06.12)
PHSS_34857  u2comp/be/plugin library patch
PHSS_34395  FORTRAN I/O Library [libIO77]
PHSS_34397  FORTRAN Intrinsics [libF90 B.11.23.17]
PHSS_34399  Fortran Product Patch, v3.1 to v3.1.1
PHKL_34020  Perfmon enhancements and Itanium Dual-Core

The following kernel tunables were set, in addition to the defaults
set by the Technical Computing OE:

dbc_max_pct=20
dbc_min_pct=20
maxdsiz=3221225472
maxssiz=401604608
```

Platform Notes

```
The "cpuconfig" EFI command was used prior to booting
  to deconfigure processors.

Although two cores were enabled during testing,
  the SPEC CPU2006 benchmarks used only one core.
```

Base Compiler Invocation

C benchmarks:
```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:
```
/opt/aCC/bin/aCC -Aa
```

Base Portability Flags

400.perlbench: `-DSPEC_CPU_HPUX_IA64`
403.gcc: `-DSPEC_CPU_HPUX`
462.libquantum: `-DSPEC_CPU_HPUX`
483.xalancbmk: `-DSPEC_CPU_HPUX_IA64`

Base Optimization Flags

C benchmarks:
```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
```

C++ benchmarks:
```
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared -Wl,+pd,64M
-Wl,+pi,64M -Wl,-N
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a
```

Peak Compiler Invocation

C benchmarks:
```
/opt/ansic/bin/cc -Ae
```

C++ benchmarks:
```
/opt/aCC/bin/aCC -Aa
```

Peak Portability Flags

400.perlbench: -DSPEC_CPU_HPUX_IA64
403.gcc: -DSPEC_CPU_HPUX
462.libquantum: -DSPEC_CPU_HPUX
483.xalancbmk: -DSPEC_CPU_HPUX_IA64

Peak Optimization Flags

C benchmarks:

400.perlbench: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M -Wl,-N

401.bzip2: Same as 400.perlbench

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M +Odataprefetch=direct

456.hmmer: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M

458.sjeng: Same as 445.gobmk

462.libquantum: Same as 456.hmmer

464.h264ref: basepeak = yes

C++ benchmarks:

471.omnetpp: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

473.astar: +Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

483.xalancbmk: +Oprofile=collect:all(pass 1) +Oprofile=use(pass 2)
+Ofaster +Otype_safety=ansi -Wl,-a,archive_shared
-Wl,+pd,64M -Wl,+pi,64M +Onoparmsoverlap
/usr/lib/hpux32/libCsup.a /opt/smartheap/SmartHeap_8/lib/libsmartheap.a

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.11.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.11.xml

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For other inquiries, please contact webmaster@spec.org.

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