SPEC® CINT2006 Result

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Fujitsu Siemens Computers

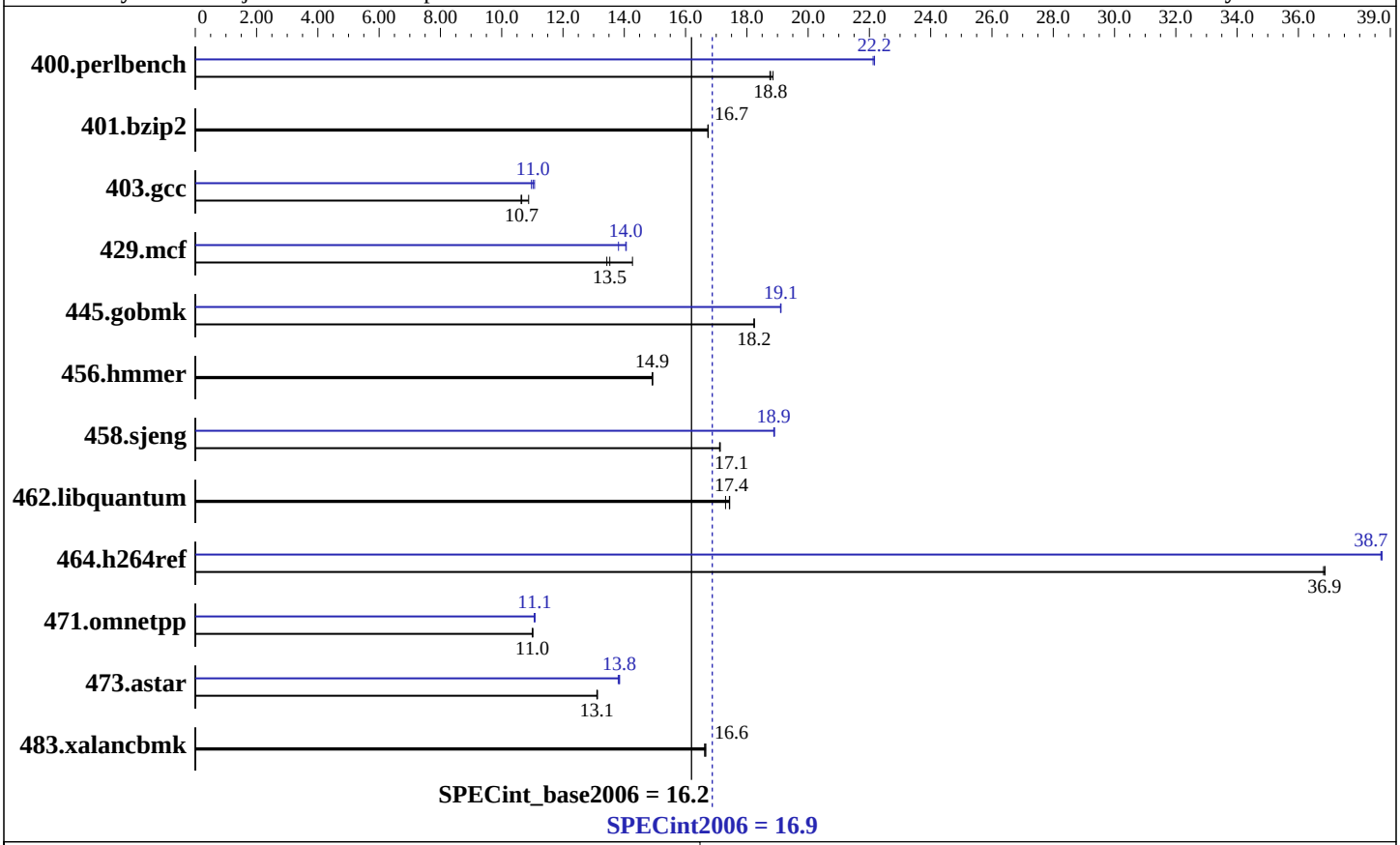
CELSIUS R640, Intel Xeon 5160 processor

SPECint®2006 = 16.9

SPECint_base2006 = 16.2

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Nov-2006
Hardware Availability: Sep-2006
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	Dual Core, 3.0 GHz
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	SATA II 7200 rpm
Other Hardware:	None

Software

Operating System:	Windows XP Professional x64 Edition
Compiler:	Intel C++ Compiler for EM64T version 9.1 - Build 20061104, Package-ID W_CC_C_9.1.033 Microsoft Visual Studio 2005 (libr. & linker)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	64-bit
Peak Pointers:	64-bit
Other Software:	Smart Heap Library, Version 8

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	521	18.8	518	18.9	**520**	**18.8**	**441**	**22.2**	441	22.2	442	22.1
401.bzip2	576	16.7	577	16.7	**577**	**16.7**	576	16.7	577	16.7	**577**	**16.7**
403.gcc	740	10.9	**756**	**10.7**	757	10.6	734	11.0	**730**	**11.0**	727	11.1
429.mcf	679	13.4	639	14.3	**674**	**13.5**	660	13.8	648	14.1	**649**	**14.0**
445.gobmk	575	18.2	**575**	**18.2**	575	18.2	549	19.1	**549**	**19.1**	549	19.1
456.hmmer	**625**	**14.9**	625	14.9	625	14.9	**625**	**14.9**	625	14.9	625	14.9
458.sjeng	707	17.1	706	17.1	**707**	**17.1**	641	18.9	640	18.9	**641**	**18.9**
462.libquantum	**1189**	**17.4**	1188	17.4	1197	17.3	**1189**	**17.4**	1188	17.4	1197	17.3
464.h264ref	**600**	**36.9**	601	36.8	600	36.9	572	38.7	571	38.7	**571**	**38.7**
471.omnetpp	**568**	**11.0**	567	11.0	568	11.0	**565**	**11.1**	565	11.1	564	11.1
473.astar	536	13.1	535	13.1	**536**	**13.1**	509	13.8	**507**	**13.8**	507	13.8
483.xalancbmk	**415**	**16.6**	415	16.6	414	16.7	**415**	**16.6**	415	16.6	414	16.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

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For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
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Base Compiler Invocation

C benchmarks:

```
icl -Qvc8 -Qc99
```

C++ benchmarks:

```
icl -Qvc8
```

Base Portability Flags

400.perlbench: `-D_Complex= -DSPEC_CPU_P64 -DSPEC_CPU_NO_NEED_VA_COPY -DSPEC_CPU_WIN64_X64`
401.bzip2: `-D_Complex= -DSPEC_CPU_P64`
403.gcc: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64`
429.mcf: `-D_Complex= -DSPEC_CPU_P64`
445.gobmk: `-D_Complex= -DSPEC_CPU_P64`
456.hmmer: `-D_Complex= -DSPEC_CPU_P64`
458.sjeng: `-DSPEC_CPU_P64`
462.libquantum: `-DSPEC_CPU_P64`

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Base Portability Flags (Continued)

464.h264ref: `-D_Complex= -DSPEC_CPU_P64 -DWIN32 -DSPEC_CPU_NO_INTTYPES`
471.omnetpp: `-DSPEC_CPU_P64 -DSPEC_CPU_WIN64`
473.astar: `-DSPEC_CPU_P64`
483.xalancbmk: `-DSPEC_CPU_P64 -Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
`-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
`icl -Qvc8 -Qc99`

C++ benchmarks:
`icl -Qvc8`

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -F512000000 shlW32M.lib -link -FORCE:MULTIPLE`

401.bzip2: `basepeak = yes`

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Peak Optimization Flags (Continued)

403.gcc: Same as 400.perlbench

429.mcf: Same as 400.perlbench

445.gobmk: Same as 400.perlbench

456.hmmer: basepeak = yes

458.sjeng: Same as 400.perlbench

462.libquantum: basepeak = yes

464.h264ref: Same as 400.perlbench

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F512000000 shlW32M.lib -link -FORCE:MULTIPLE

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090715.12.xml

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:09:40 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 13 December 2006.