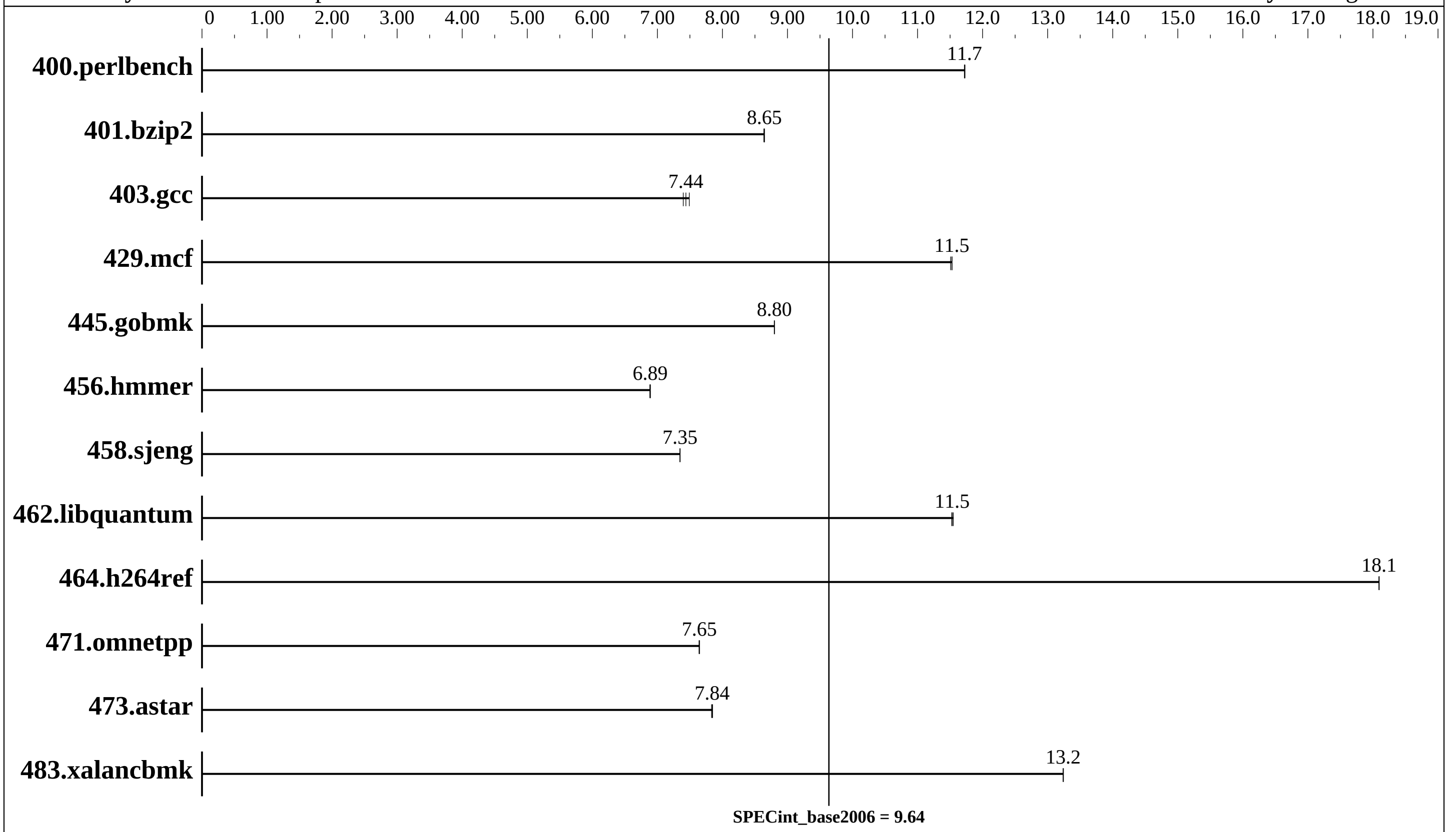
SPEC® CINT2006 Result

IBM Corporation
IBM System x3800

SPECint®2006 = Not Run

SPECint_base2006 = 9.64

CPU2006 license: 11		**Test date:**	Dec-2006
Test sponsor:	IBM Corporation	**Hardware Availability:**	Oct-2006
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon 7130N
CPU Characteristics:	667 MHz bus
CPU MHz:	3133
FPU:	Integrated
CPU(s) enabled:	8 cores, 4 chips, 2 cores/chip, 2 threads/core
CPU(s) orderable:	1,2,4 chips
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	1 MB I+D on chip per core
L3 Cache:	8 MB I+D on chip per chip
Other Cache:	None
Memory:	32 GB (16 x 2048 MB ECC PC2-3200)
Disk Subsystem:	73 GB SAS, 10k RPM
Other Hardware:	None

Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	Not Applicable
Other Software:	Smart Heap Library, Version 8

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Results Table

	Base						Peak					
Benchmark	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**	**Seconds**	**Ratio**
400.perlbench	833	11.7	834	11.7	**833**	**11.7**						
401.bzip2	1117	8.64	1116	8.65	**1116**	**8.65**						
403.gcc	**1082**	**7.44**	1088	7.40	1075	7.49						
429.mcf	791	11.5	792	11.5	**791**	**11.5**						
445.gobmk	1192	8.80	**1192**	**8.80**	1192	8.80						
456.hmmer	**1354**	**6.89**	1355	6.88	1354	6.89						
458.sjeng	**1647**	**7.35**	1647	7.35	1647	7.35						
462.libquantum	1794	11.5	**1797**	**11.5**	1798	11.5						
464.h264ref	1223	18.1	**1223**	**18.1**	1223	18.1						
471.omnetpp	**817**	**7.65**	818	7.64	817	7.65						
473.astar	**895**	**7.84**	894	7.85	896	7.83						
483.xalancbmk	**521**	**13.2**	521	13.2	521	13.2						

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Bios Settings
  Hardware Prefetch enabled
  Memory Array set to High-Performance Memory Array
```

Base Compiler Invocation

C benchmarks:
`icl -Qvc7.1 -Qc99`

C++ benchmarks:
`icl -Qvc7.1`

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`

Base Optimization Flags

C benchmarks:
`-fast /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

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Base Optimization Flags (Continued)

C++ benchmarks:

```
-fast -Qcxx_features /F512000000 shlw32m.lib
          -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090715.01.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
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