SPEC® CFP2006 Result

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Hewlett-Packard Company

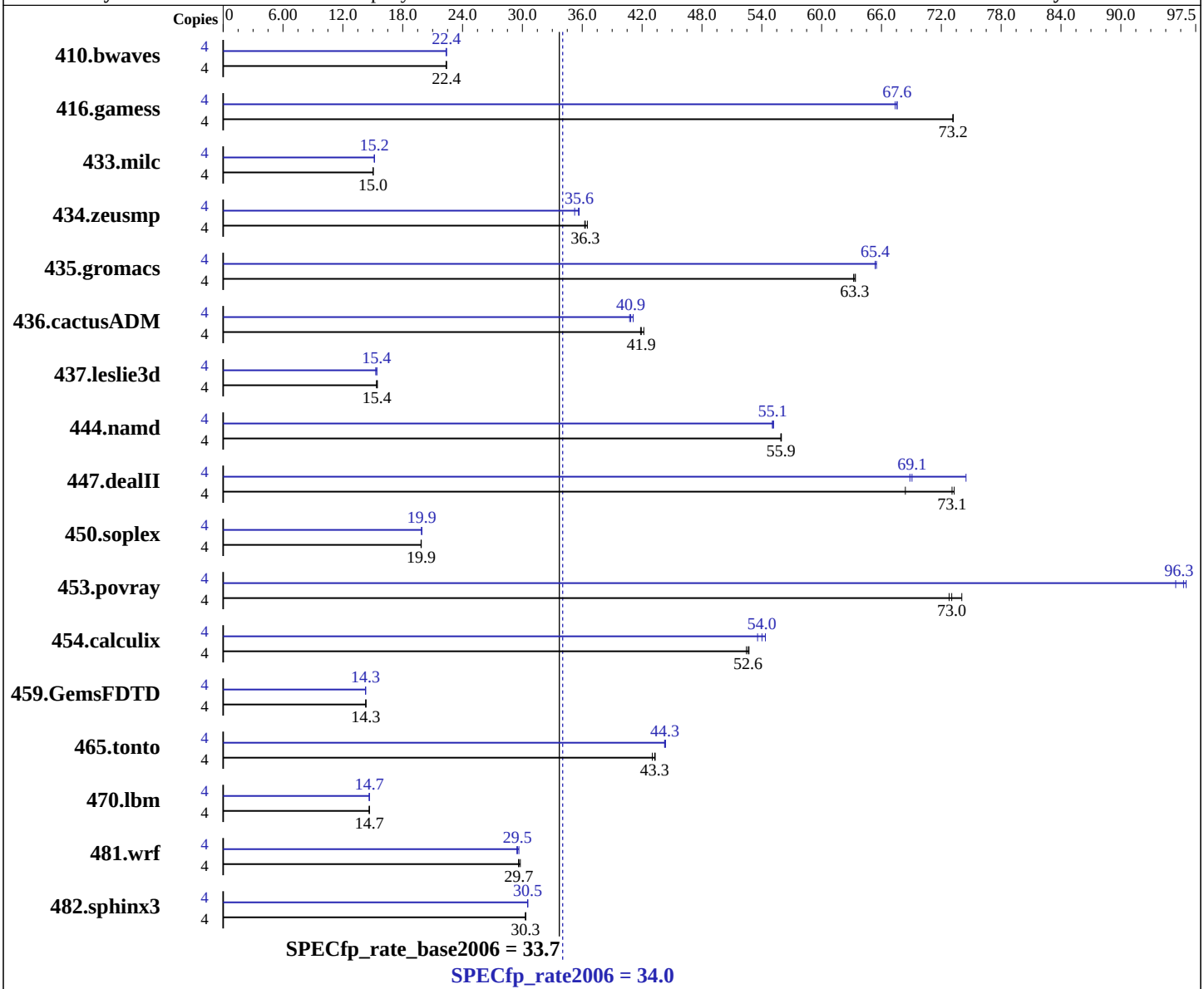
ProLiant BL460c
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 34.0

SPECfp_rate_base2006 = 33.7

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Jan-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB SAS, 10 K RPM	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	**2428**	**22.4**	2433	22.3	2428	22.4	4	**2428**	**22.4**	2428	22.4	2433	22.3
416.gamess	4	1070	73.2	1071	73.2	**1070**	**73.2**	4	**1159**	**67.6**	1159	67.6	1162	67.4
433.milc	4	**2442**	**15.0**	2443	15.0	2441	15.0	4	2421	15.2	2422	15.2	**2421**	**15.2**
434.zeusmp	4	**1002**	**36.3**	997	36.5	1004	36.2	4	1033	35.2	1020	35.7	**1022**	**35.6**
435.gromacs	4	**451**	**63.3**	452	63.2	451	63.4	4	**437**	**65.4**	436	65.5	437	65.4
436.cactusADM	4	**1140**	**41.9**	1133	42.2	1142	41.8	4	**1170**	**40.9**	1163	41.1	1173	40.8
437.leslie3d	4	2431	15.5	**2438**	**15.4**	2447	15.4	4	2441	15.4	**2442**	**15.4**	2459	15.3
444.namd	4	**573**	**55.9**	574	55.9	573	56.0	4	581	55.2	583	55.1	**582**	**55.1**
447.dealII	4	669	68.4	624	73.3	**626**	**73.1**	4	614	74.5	665	68.9	**663**	**69.1**
450.soplex	4	1682	19.8	**1680**	**19.9**	1680	19.9	4	1675	19.9	**1676**	**19.9**	1677	19.9
453.povray	4	287	74.1	**291**	**73.0**	292	72.8	4	**221**	**96.3**	223	95.5	220	96.5
454.calculix	4	629	52.5	**627**	**52.6**	626	52.7	4	607	54.4	616	53.6	**611**	**54.0**
459.GemsFDTD	4	**2966**	**14.3**	2968	14.3	2966	14.3	4	2974	14.3	**2972**	**14.3**	2971	14.3
465.tonto	4	915	43.0	**910**	**43.3**	909	43.3	4	889	44.3	887	44.4	**889**	**44.3**
470.lbm	4	3753	14.6	3750	14.7	**3750**	**14.7**	4	3752	14.6	**3751**	**14.7**	3750	14.7
481.wrf	4	**1507**	**29.7**	1500	29.8	1509	29.6	4	**1514**	**29.5**	1507	29.7	1517	29.4
482.sphinx3	4	2570	30.3	**2571**	**30.3**	2573	30.3	4	2552	30.6	2555	30.5	**2554**	**30.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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