SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

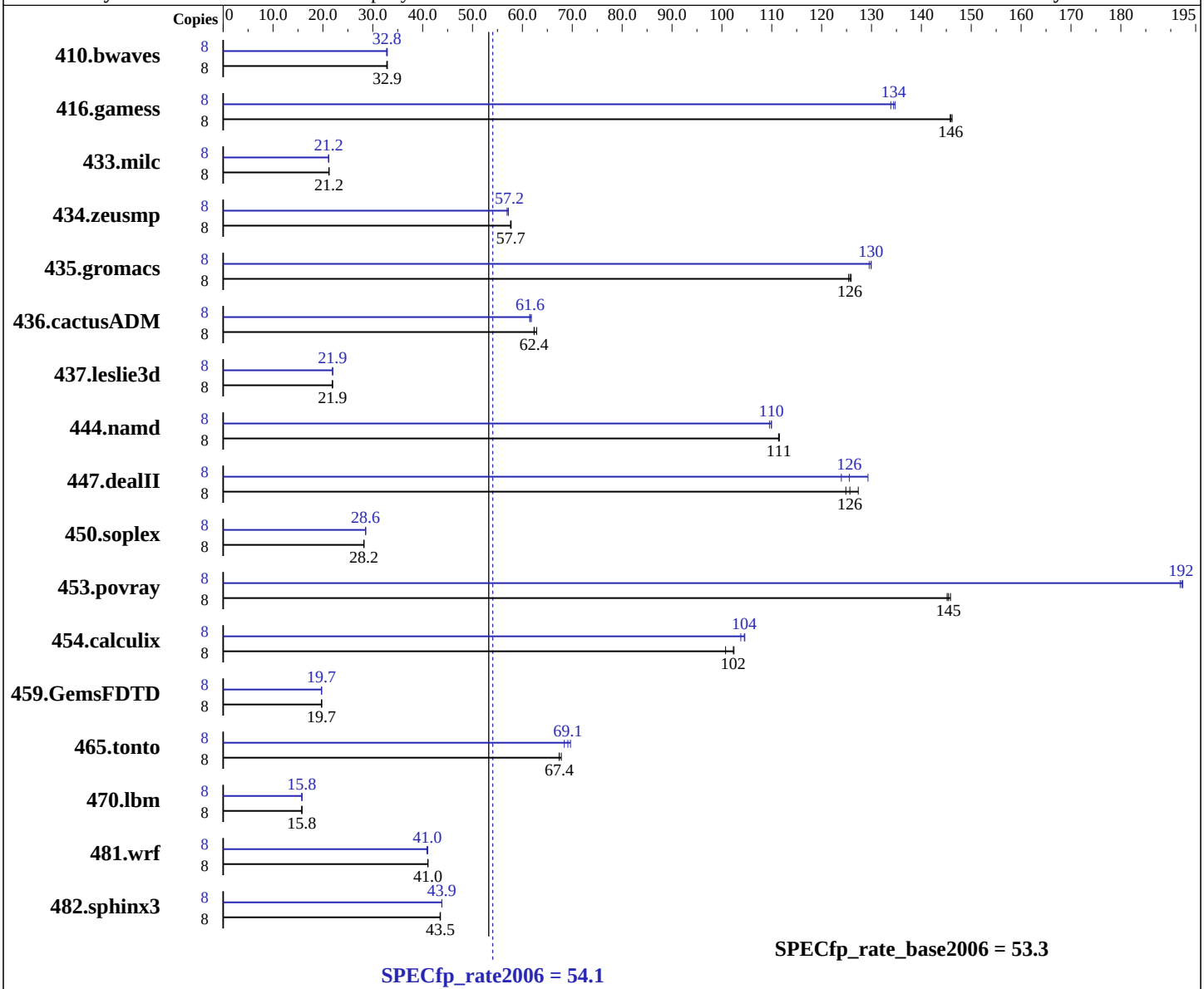
ProLiant ML350 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 54.1

SPECfp_rate_base2006 = 53.3

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 EM64T kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant ML350 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp_rate2006 = 54.1

SPECfp_rate_base2006 = 53.3

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	8 GB (4x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	4x36 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3303	32.9	3315	32.8	**3305**	**32.9**	8	**3312**	**32.8**	3302	32.9	3316	32.8
416.gamess	8	1072	146	**1073**	**146**	1075	146	8	1162	135	1170	134	**1165**	**134**
433.milc	8	**3462**	**21.2**	3455	21.3	3467	21.2	8	**3472**	**21.2**	3468	21.2	3475	21.1
434.zeusmp	8	1263	57.7	**1263**	**57.7**	1261	57.8	8	1273	57.2	**1274**	**57.2**	1279	56.9
435.gromacs	8	**454**	**126**	454	126	455	125	8	440	130	441	130	**440**	**130**
436.cactusADM	8	1533	62.3	1521	62.9	**1533**	**62.4**	8	**1552**	**61.6**	1555	61.5	1547	61.8
437.leslie3d	8	3426	22.0	**3431**	**21.9**	3440	21.9	8	3426	22.0	**3426**	**21.9**	3427	21.9
444.namd	8	575	112	**576**	**111**	576	111	8	584	110	586	110	**584**	**110**
447.dealII	8	719	127	733	125	**728**	**126**	8	738	124	**729**	**126**	708	129
450.soplex	8	2361	28.3	**2364**	**28.2**	2367	28.2	8	2335	28.6	**2335**	**28.6**	2338	28.5
453.povray	8	292	146	**293**	**145**	293	145	8	**221**	**192**	222	192	221	192
454.calculix	8	**645**	**102**	655	101	644	102	8	636	104	631	105	**632**	**104**
459.GemsFDTD	8	4300	19.7	4302	19.7	**4300**	**19.7**	8	4304	19.7	**4303**	**19.7**	4302	19.7
465.tonto	8	1161	67.8	**1168**	**67.4**	1169	67.4	8	1130	69.6	1151	68.4	**1139**	**69.1**
470.lbm	8	6969	15.8	**6962**	**15.8**	6962	15.8	8	6956	15.8	**6956**	**15.8**	6972	15.8
481.wrf	8	2178	41.0	**2177**	**41.0**	2175	41.1	8	**2182**	**41.0**	2188	40.8	2178	41.0
482.sphinx3	8	3585	43.5	**3581**	**43.5**	3578	43.6	8	3556	43.9	**3556**	**43.9**	3554	43.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:27:12 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 February 2007.