SPEC® CFP2006 Result

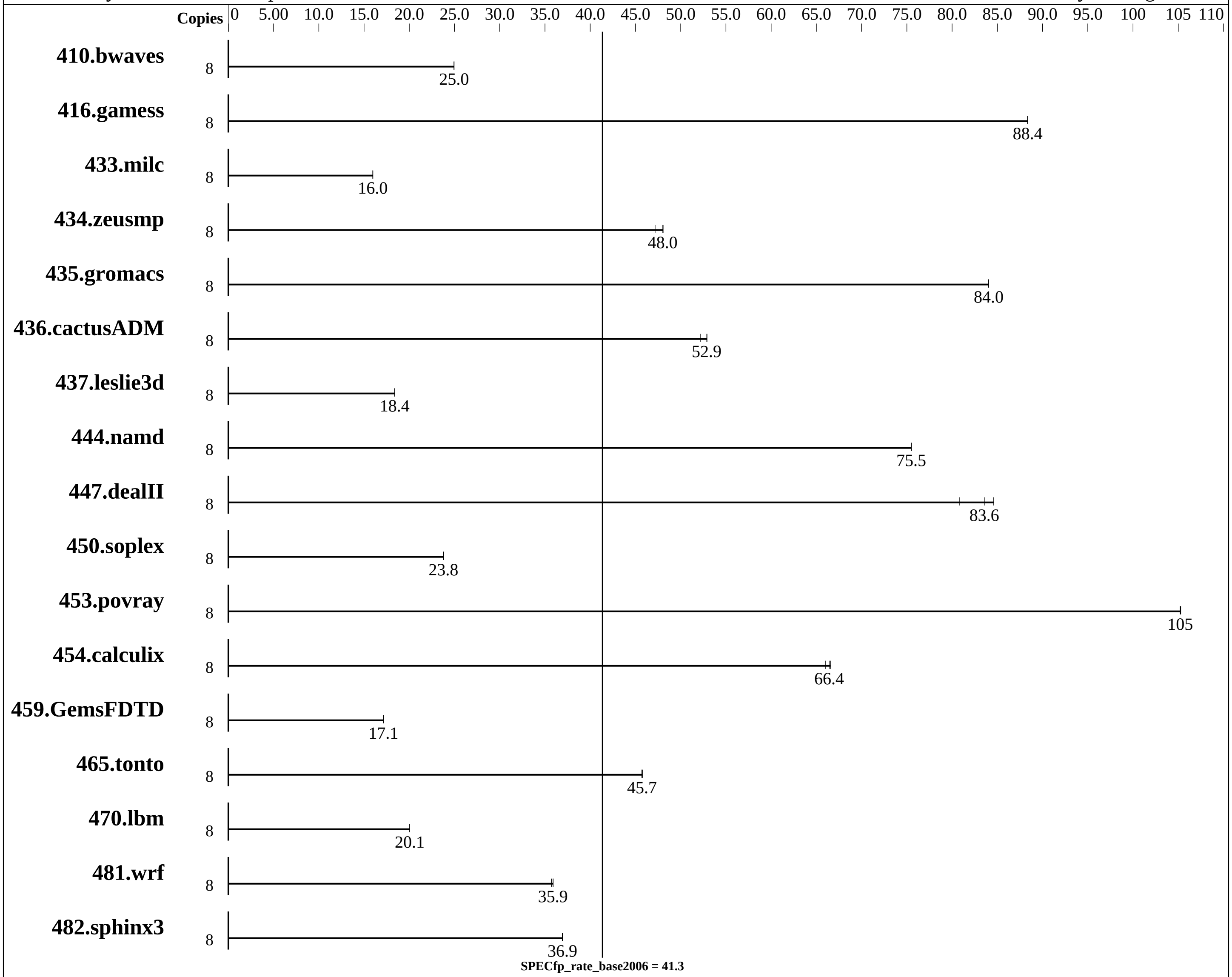
IBM Corporation

IBM BladeCenter HS21 XM (Intel Xeon E5320)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 41.3

CPU2006 license:	11	**Test date:**	Jan-2007
Test sponsor:	IBM Corporation	**Hardware Availability:**	Feb-2007
Tested by:	IBM Corporation	**Software Availability:**	Aug-2006



Hardware

CPU Name:	Intel Xeon E5320
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Microsoft Windows Server 2003 Enterprise x64 Edition + SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2GB DDR2-5300F ECC)
Disk Subsystem: 1 x 74 GB SAS, 1000 RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: Smart Heap Library, Version 8

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**4357**	**25.0**	4359	24.9	4357	25.0							
416.gamess	8	1773	88.3	1773	88.4	**1773**	**88.4**							
433.milc	8	4598	16.0	**4598**	**16.0**	4597	16.0							
434.zeusmp	8	1515	48.1	1543	47.2	**1516**	**48.0**							
435.gromacs	8	680	84.0	**680**	**84.0**	680	84.1							
436.cactusADM	8	1833	52.2	**1808**	**52.9**	1807	52.9							
437.leslie3d	8	4086	18.4	**4088**	**18.4**	4089	18.4							
444.namd	8	850	75.5	**850**	**75.5**	850	75.5							
447.dealII	8	1133	80.8	**1095**	**83.6**	1082	84.6							
450.soplex	8	**2806**	**23.8**	2805	23.8	2808	23.8							
453.povray	8	**404**	**105**	404	105	404	105							
454.calculix	8	992	66.6	1000	66.0	**994**	**66.4**							
459.GemsFDTD	8	**4951**	**17.1**	4954	17.1	4946	17.2							
465.tonto	8	1722	45.7	1720	45.8	**1721**	**45.7**							
470.lbm	8	5482	20.1	**5482**	**20.1**	5482	20.1							
481.wrf	8	2489	35.9	2501	35.7	**2490**	**35.9**							
482.sphinx3	8	**4221**	**36.9**	4218	37.0	4225	36.9							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-flags.20090714.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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