SPEC® CFP2006 Result

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Itautec

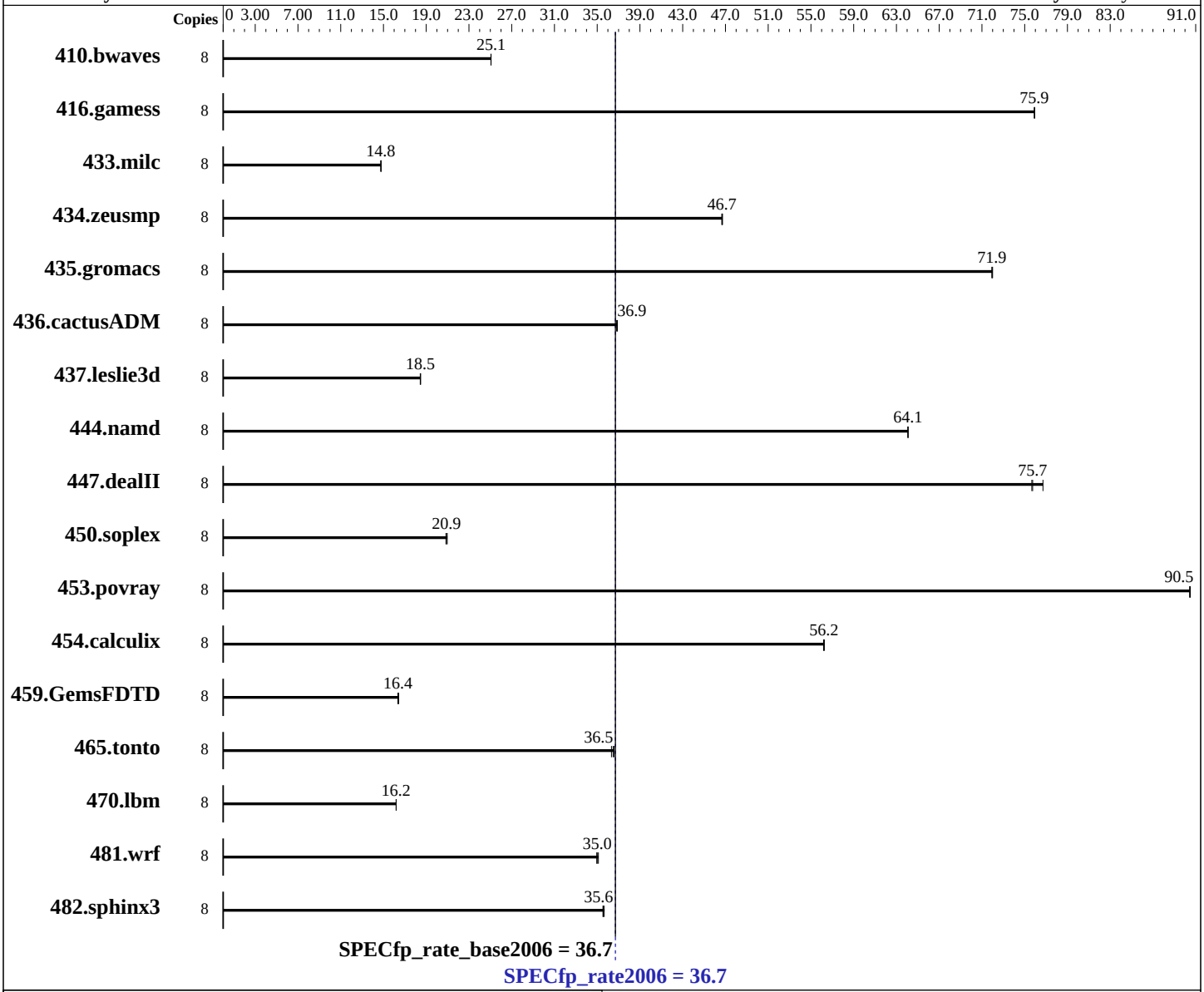
Servidor Itautec LX201 (Intel Xeon E5310)

SPECfp®_rate2006 = 36.7

SPECfp_rate_base2006 = 36.7

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon E5310
CPU Characteristics:	1066MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 80 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	4339	25.1	**4337**	**25.1**	4337	25.1	8	4339	25.1	**4337**	**25.1**	4337	25.1
416.gamess	8	2063	75.9	2064	75.9	**2063**	**75.9**	8	2063	75.9	2064	75.9	**2063**	**75.9**
433.milc	8	4978	14.8	**4977**	**14.8**	4965	14.8	8	4978	14.8	**4977**	**14.8**	4965	14.8
434.zeusmp	8	**1559**	**46.7**	1558	46.7	1560	46.7	8	**1559**	**46.7**	1558	46.7	1560	46.7
435.gromacs	8	**794**	**71.9**	794	71.9	793	72.0	8	**794**	**71.9**	794	71.9	793	72.0
436.cactusADM	8	2596	36.8	2592	36.9	**2594**	**36.9**	8	2596	36.8	2592	36.9	**2594**	**36.9**
437.leslie3d	8	4072	18.5	**4072**	**18.5**	4078	18.4	8	4072	18.5	**4072**	**18.5**	4078	18.4
444.namd	8	1001	64.1	**1001**	**64.1**	1001	64.1	8	1001	64.1	**1001**	**64.1**	1001	64.1
447.dealII	8	1193	76.7	**1208**	**75.7**	1210	75.7	8	1193	76.7	**1208**	**75.7**	1210	75.7
450.soplex	8	3200	20.8	**3193**	**20.9**	3182	21.0	8	3200	20.8	**3193**	**20.9**	3182	21.0
453.povray	8	470	90.5	**471**	**90.5**	471	90.4	8	470	90.5	**471**	**90.5**	471	90.4
454.calculix	8	**1174**	**56.2**	1175	56.2	1173	56.3	8	**1174**	**56.2**	1175	56.2	1173	56.3
459.GemsFDTD	8	**5188**	**16.4**	5167	16.4	5188	16.4	8	**5188**	**16.4**	5167	16.4	5188	16.4
465.tonto	8	**2158**	**36.5**	2167	36.3	2152	36.6	8	**2158**	**36.5**	2167	36.3	2152	36.6
470.lbm	8	6791	16.2	**6790**	**16.2**	6789	16.2	8	6791	16.2	**6790**	**16.2**	6789	16.2
481.wrf	8	2556	35.0	**2553**	**35.0**	2545	35.1	8	2556	35.0	**2553**	**35.0**	2545	35.1
482.sphinx3	8	4387	35.5	4373	35.7	**4384**	**35.6**	8	4387	35.5	4373	35.7	**4384**	**35.6**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`
470.lbm: `basepeak = yes`
482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`
447.dealII: `basepeak = yes`
450.soplex: `basepeak = yes`
453.povray: `basepeak = yes`

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes

436.cactusADM: basepeak = yes

454.calculix: basepeak = yes

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

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