



SPEC® CFP2006 Result

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Itautec

SPECfp®_rate2006 = 50.7

Servidor Itautec LX201 (Intel Xeon X5355)

SPECfp_rate_base2006 = 50.7

CPU2006 license: 9001

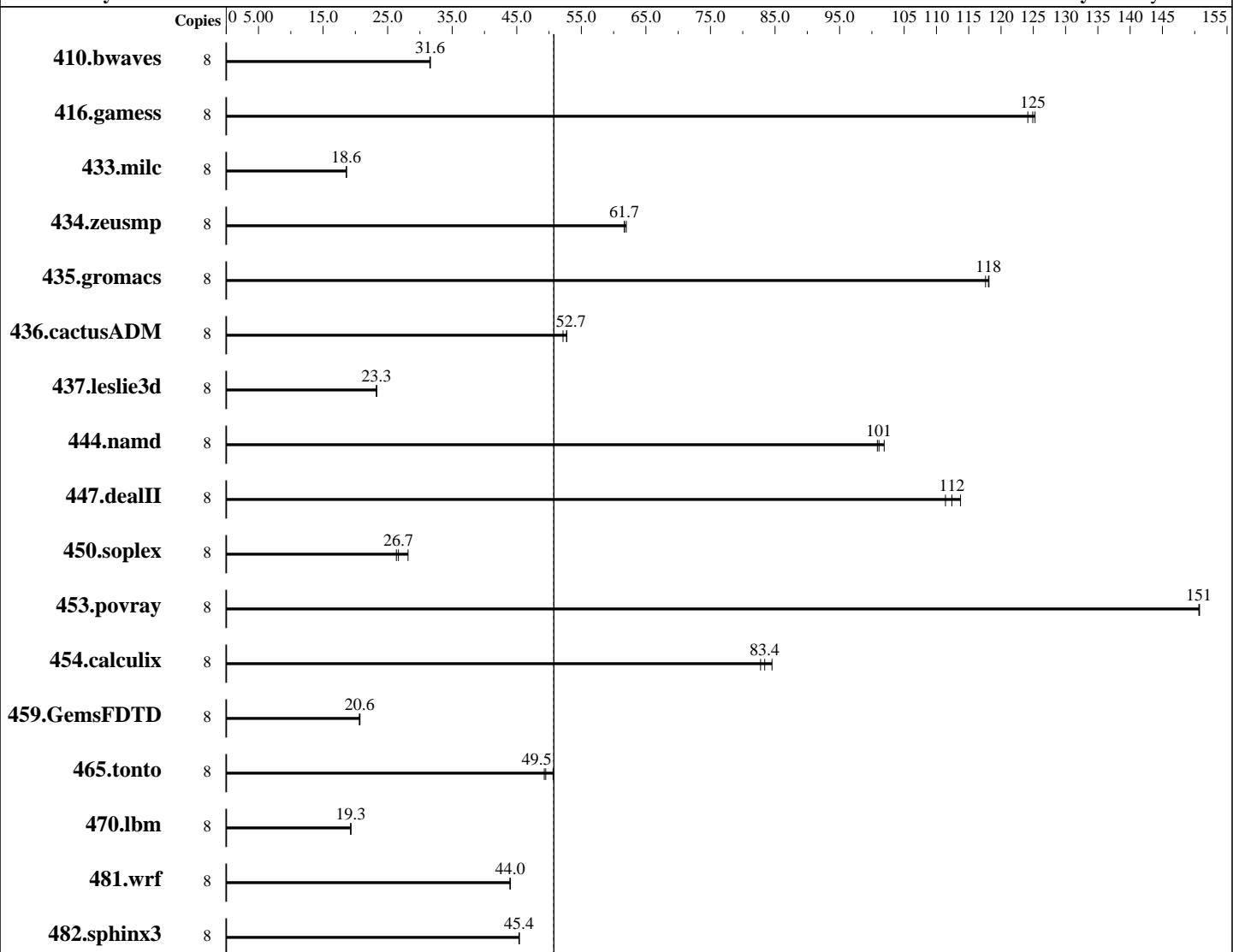
Test date: Feb-2007

Test sponsor: Itautec

Hardware Availability: Feb-2007

Tested by: Itautec

Software Availability: May-2006



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Hardware

CPU Name: Intel Xeon X5355
CPU Characteristics: 1333MHz system bus
CPU MHz: 2660
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores

Software

Operating System: Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.025 Build no 20060519Z
Intel Fortran Compiler for IA32 version 9.1
Package ID W_FC_C_9.1.025 Build no 20060519Z
Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel: No
File System: NTFS

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L3 Cache: None
 Other Cache: None
 Memory: 8 GB (8x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
 Disk Subsystem: 120 GB SATA, 7200RPM
 Other Hardware: None

System State: Default
 Base Pointers: 32-bit
 Peak Pointers: 32-bit
 Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3447	31.5	3437	31.6	3443	31.6	8	3447	31.5	3437	31.6	3443	31.6
416.gamess	8	1254	125	1262	124	1250	125	8	1254	125	1262	124	1250	125
433.milc	8	3948	18.6	3944	18.6	3947	18.6	8	3948	18.6	3944	18.6	3947	18.6
434.zeusmp	8	1181	61.7	1180	61.7	1175	62.0	8	1181	61.7	1180	61.7	1175	62.0
435.gromacs	8	486	118	484	118	484	118	8	486	118	484	118	484	118
436.cactusADM	8	1814	52.7	1813	52.7	1833	52.2	8	1814	52.7	1813	52.7	1833	52.2
437.leslie3d	8	3231	23.3	3231	23.3	3227	23.3	8	3231	23.3	3231	23.3	3227	23.3
444.namd	8	630	102	636	101	635	101	8	630	102	636	101	635	101
447.dealII	8	805	114	814	112	822	111	8	805	114	814	112	822	111
450.soplex	8	2532	26.4	2502	26.7	2369	28.2	8	2532	26.4	2502	26.7	2369	28.2
453.povray	8	282	151	282	151	283	151	8	282	151	282	151	283	151
454.calculix	8	791	83.4	798	82.8	781	84.5	8	791	83.4	798	82.8	781	84.5
459.GemsFDTD	8	4111	20.6	4114	20.6	4096	20.7	8	4111	20.6	4114	20.6	4096	20.7
465.tonto	8	1555	50.6	1591	49.5	1598	49.3	8	1555	50.6	1591	49.5	1598	49.3
470.lbm	8	5699	19.3	5696	19.3	5700	19.3	8	5699	19.3	5696	19.3	5700	19.3
481.wrf	8	2030	44.0	2031	44.0	2034	43.9	8	2030	44.0	2031	44.0	2034	43.9
482.sphinx3	8	3436	45.4	3437	45.4	3435	45.4	8	3436	45.4	3437	45.4	3435	45.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:

 icl -Qvc7.1 -Qc99

C++ benchmarks:

 icl -Qvc7.1

Fortran benchmarks:

 ifort

Benchmarks using both Fortran and C:

 icl -Qvc7.1 -Qc99 ifort



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Base Portability Flags

```
436.cactusADM: -Qlowercase /assume:underscore  
444.namd: -TP  
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG  
             -DBOOST_NO_INTRINSIC_WCHAR_T  
453.povray: -DSPEC_CPU_WINDOWS_ICL  
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase  
481.wrf: -DSPEC_CPU_WINDOWS_ICL
```

Base Optimization Flags

C benchmarks:

```
-fast /F950000000 sh1SMPMt.lib           -link /FORCE:MULTIPLE
```

C++ benchmarks:

```
-fast -Qcxx_features /F950000000 sh1SMPMt.lib  
      -link /FORCE:MULTIPLE
```

Fortran benchmarks:

```
-fast /F950000000           -link /FORCE:MULTIPLE
```

Benchmarks using both Fortran and C:

```
-fast /F950000000           -link /FORCE:MULTIPLE
```

Peak Optimization Flags

C benchmarks:

```
433.milc: basepeak = yes  
470.lbm: basepeak = yes  
482.sphinx3: basepeak = yes
```

C++ benchmarks:

```
444.namd: basepeak = yes  
447.dealII: basepeak = yes  
450.soplex: basepeak = yes  
453.povray: basepeak = yes
```

Fortran benchmarks:

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Peak Optimization Flags (Continued)

410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: basepeak = yes

Benchmarks using both Fortran and C:

435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html>

You can also download the XML flags source by saving the following link:
<http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml>

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For other inquiries, please contact webmaster@spec.org.

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