SPEC® CFP2006 Result

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Hewlett-Packard Company

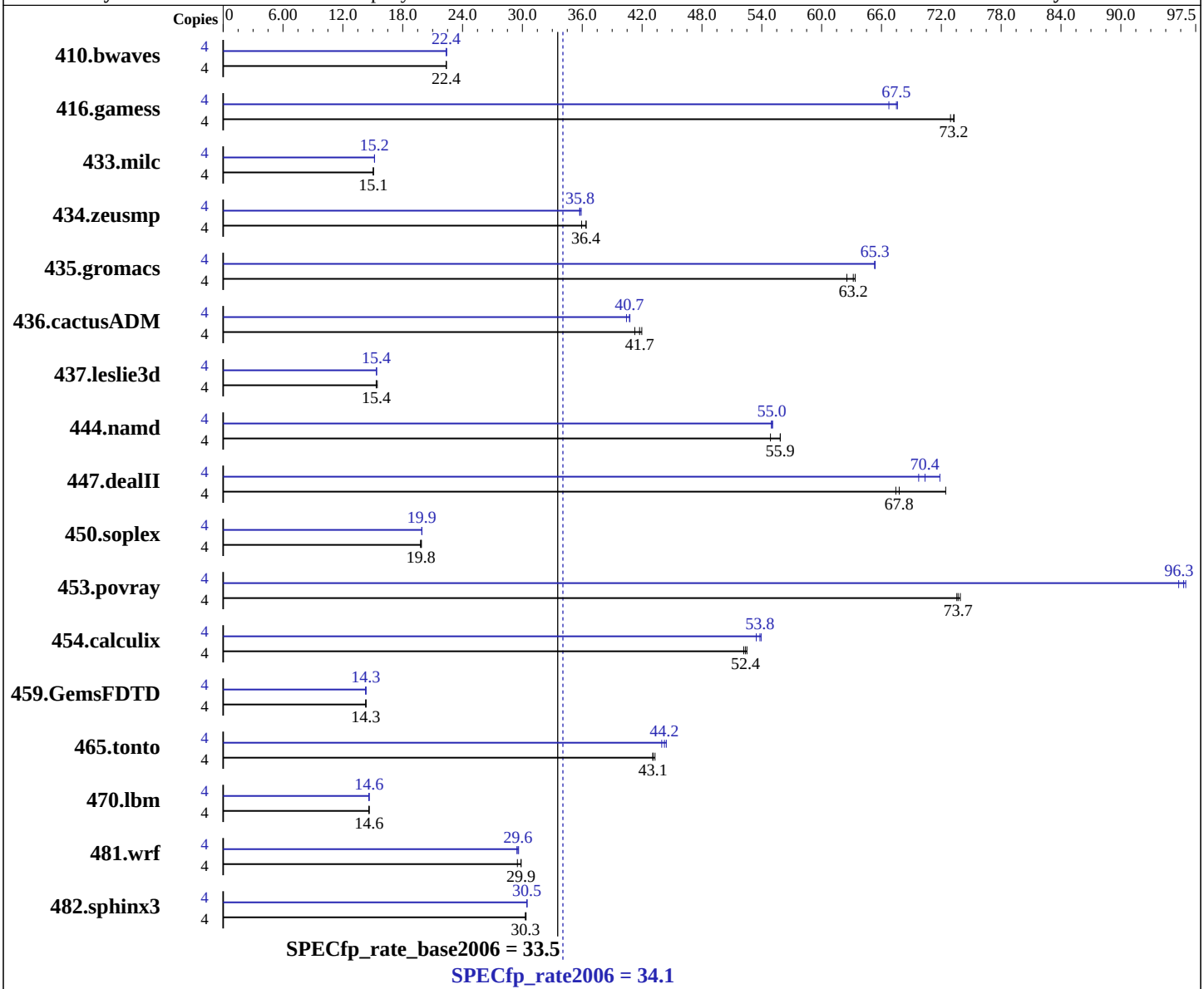
ProLiant ML370 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 34.1

SPECfp_rate_base2006 = 33.5

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2428	22.4	2432	22.4	**2431**	**22.4**	4	**2431**	**22.4**	2432	22.4	2425	22.4
416.gamess	4	1074	72.9	1069	73.3	**1069**	**73.2**	4	1173	66.7	**1160**	**67.5**	1159	67.6
433.milc	4	**2439**	**15.1**	2438	15.1	2440	15.0	4	2421	15.2	2419	15.2	**2419**	**15.2**
434.zeusmp	4	**1001**	**36.4**	1013	35.9	1000	36.4	4	1019	35.7	1014	35.9	**1018**	**35.8**
435.gromacs	4	451	63.4	457	62.5	**452**	**63.2**	4	437	65.3	437	65.4	**437**	**65.3**
436.cactusADM	4	1139	42.0	**1145**	**41.7**	1158	41.3	4	1182	40.4	1172	40.8	**1173**	**40.7**
437.leslie3d	4	2433	15.5	**2449**	**15.4**	2451	15.3	4	**2446**	**15.4**	2446	15.4	2446	15.4
444.namd	4	574	55.9	**574**	**55.9**	585	54.9	4	**583**	**55.0**	582	55.1	584	55.0
447.dealII	4	679	67.4	**675**	**67.8**	632	72.4	4	656	69.7	**650**	**70.4**	637	71.9
450.soplex	4	1678	19.9	**1682**	**19.8**	1686	19.8	4	1674	19.9	**1675**	**19.9**	1675	19.9
453.povray	4	**289**	**73.7**	289	73.6	288	73.9	4	221	96.5	**221**	**96.3**	222	95.8
454.calculix	4	628	52.5	**630**	**52.4**	632	52.2	4	612	53.9	**613**	**53.8**	617	53.4
459.GemsFDTD	4	2969	14.3	**2965**	**14.3**	2963	14.3	4	**2965**	**14.3**	2972	14.3	2964	14.3
465.tonto	4	909	43.3	914	43.1	**913**	**43.1**	4	886	44.4	**890**	**44.2**	895	44.0
470.lbm	4	**3755**	**14.6**	3765	14.6	3754	14.6	4	**3754**	**14.6**	3765	14.6	3753	14.6
481.wrf	4	1515	29.5	1496	29.9	**1496**	**29.9**	4	1517	29.4	1508	29.6	**1512**	**29.6**
482.sphinx3	4	2567	30.4	2573	30.3	**2571**	**30.3**	4	2560	30.4	2559	30.5	**2559**	**30.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"start /b /wait /affinity" used to bind processes to CPU(s).
"ulimit -s unlimited" set
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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