SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

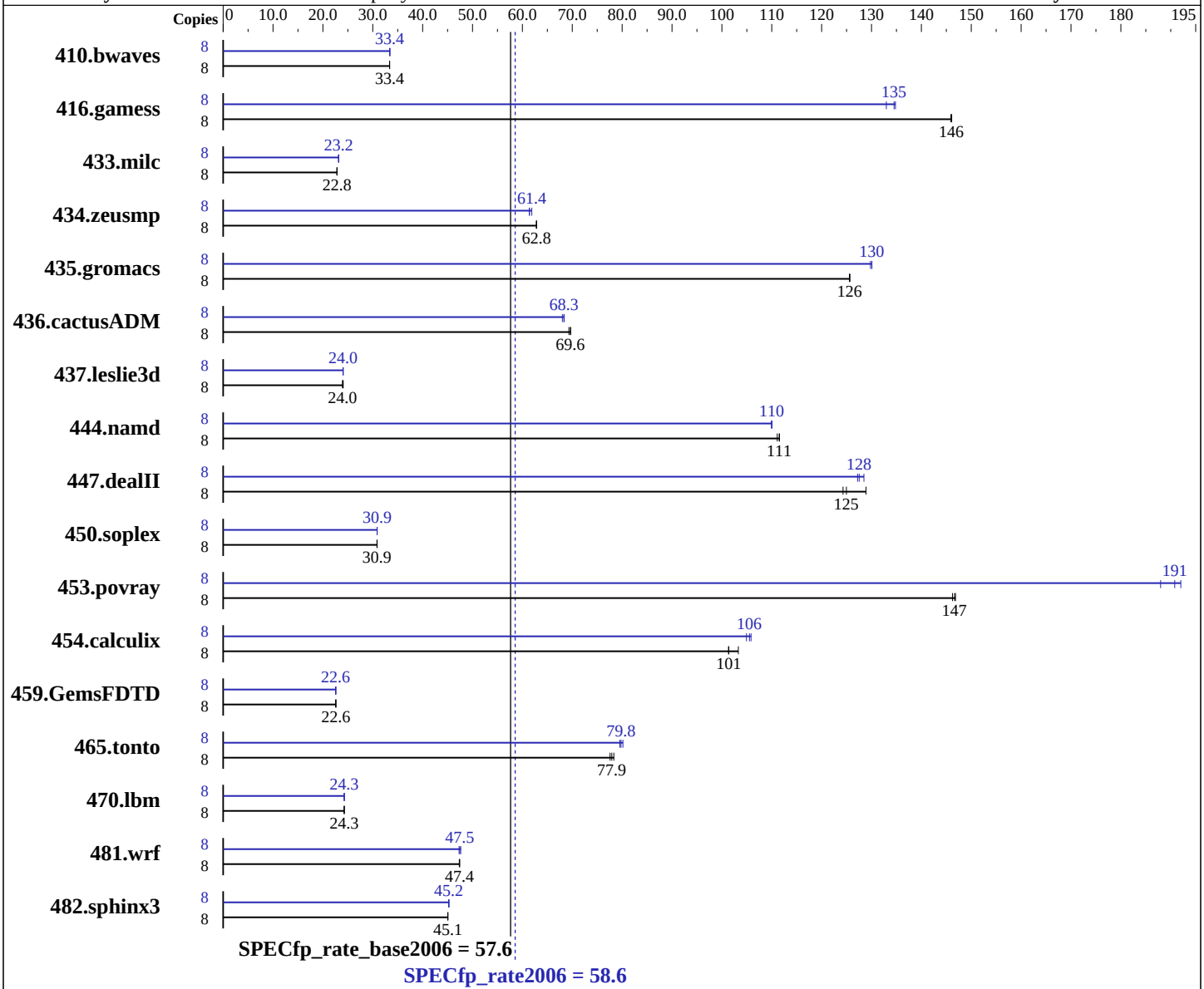
ProLiant DL360 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 58.6

SPECfp_rate_base2006 = 57.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Hewlett-Packard Company

ProLiant DL360 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp_rate2006 = 58.6

SPECfp_rate_base2006 = 57.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006

L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3258**	**33.4**	3257	33.4	3258	33.4	8	3253	33.4	3254	33.4	**3254**	**33.4**
416.gamess	8	1072	146	**1073**	**146**	1073	146	8	**1164**	**135**	1178	133	1162	135
433.milc	8	3215	22.8	**3217**	**22.8**	3218	22.8	8	**3171**	**23.2**	3173	23.1	3171	23.2
434.zeusmp	8	1159	62.8	1158	62.9	**1159**	**62.8**	8	1176	61.9	**1185**	**61.4**	1186	61.4
435.gromacs	8	455	126	455	126	**455**	**126**	8	439	130	**439**	**130**	440	130
436.cactusADM	8	**1373**	**69.6**	1379	69.3	1372	69.7	8	1398	68.4	1406	68.0	**1399**	**68.3**
437.leslie3d	8	3124	24.1	3140	24.0	**3139**	**24.0**	8	3129	24.0	3124	24.1	**3128**	**24.0**
444.namd	8	575	112	**576**	**111**	577	111	8	583	110	583	110	**583**	**110**
447.dealII	8	710	129	**732**	**125**	736	124	8	**718**	**128**	719	127	712	128
450.soplex	8	2161	30.9	2163	30.8	**2163**	**30.9**	8	**2161**	**30.9**	2160	30.9	2162	30.9
453.povray	8	290	147	**290**	**147**	291	146	8	226	188	**223**	**191**	222	192
454.calculix	8	**651**	**101**	639	103	651	101	8	**625**	**106**	629	105	624	106
459.GemsFDTD	8	3762	22.6	3755	22.6	**3756**	**22.6**	8	**3760**	**22.6**	3756	22.6	3762	22.6
465.tonto	8	1005	78.3	**1010**	**77.9**	1015	77.6	8	**987**	**79.8**	990	79.5	982	80.2
470.lbm	8	4529	24.3	**4528**	**24.3**	4528	24.3	8	**4527**	**24.3**	4527	24.3	4527	24.3
481.wrf	8	1887	47.3	1884	47.4	**1886**	**47.4**	8	1889	47.3	1876	47.6	**1883**	**47.5**
482.sphinx3	8	**3461**	**45.1**	3462	45.0	3459	45.1	8	3441	45.3	**3449**	**45.2**	3449	45.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:33:08 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 March 2007.