SPEC® CFP2006 Result

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Hewlett-Packard Company

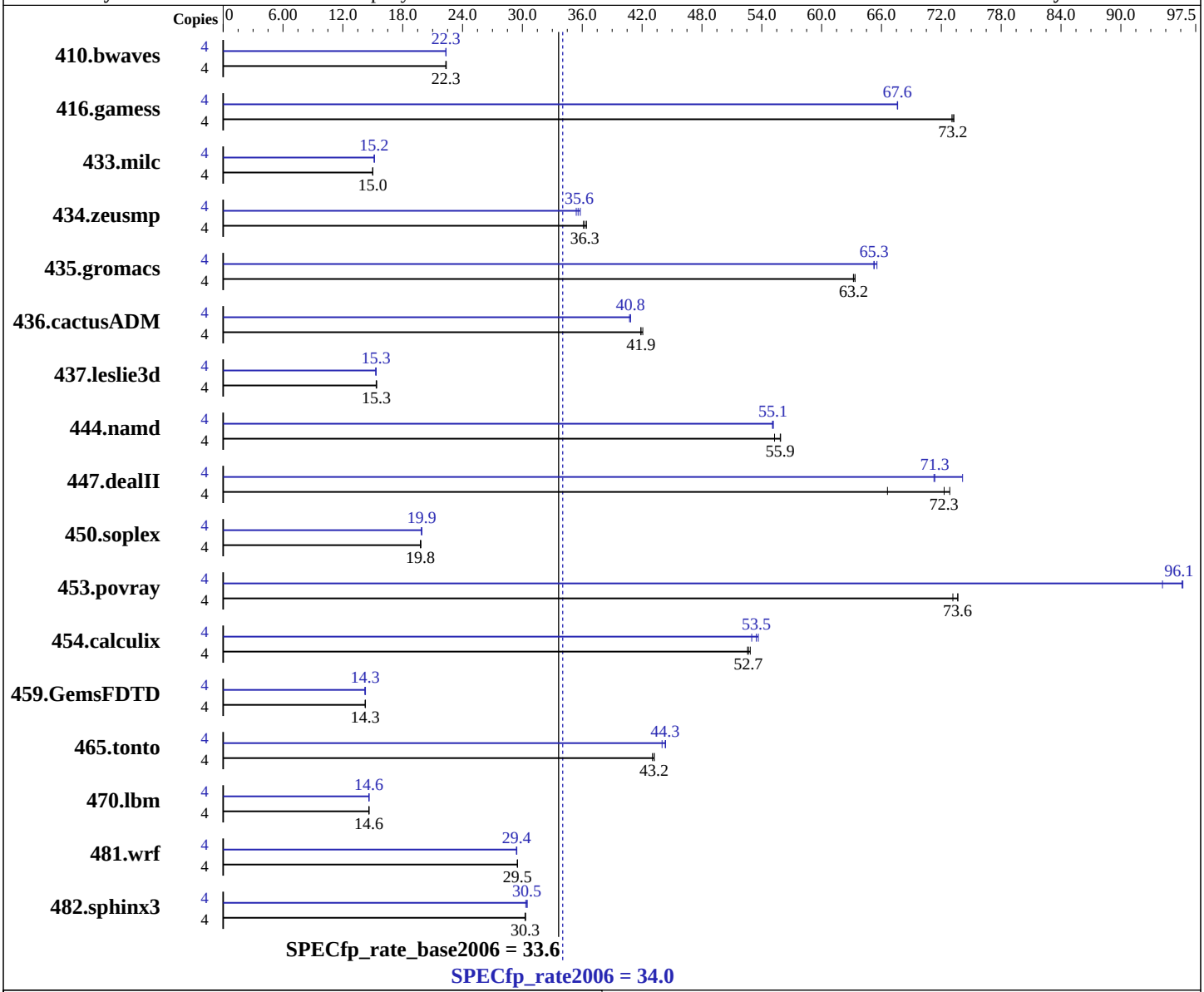
ProLiant DL360 G5
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 34.0

SPECfp_rate_base2006 = 33.6

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66GHz, 2x4 MB L2 shared, 1333 MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300 CL5)	Base Pointers:	64-bit
Disk Subsystem:	2x72 GB 10 K SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	**2434**	**22.3**	2430	22.4	2435	22.3	4	**2434**	**22.3**	2432	22.4	2434	22.3
416.gamess	4	1069	73.3	1072	73.1	**1070**	**73.2**	4	1158	67.6	1159	67.6	**1159**	**67.6**
433.milc	4	2446	15.0	2450	15.0	**2448**	**15.0**	4	**2424**	**15.2**	2424	15.1	2423	15.2
434.zeusmp	4	**1004**	**36.3**	1008	36.1	999	36.4	4	1028	35.4	1017	35.8	**1023**	**35.6**
435.gromacs	4	451	63.4	**452**	**63.2**	452	63.2	4	436	65.5	**438**	**65.3**	438	65.2
436.cactusADM	4	1141	41.9	**1141**	**41.9**	1136	42.1	4	**1170**	**40.8**	1170	40.8	1173	40.7
437.leslie3d	4	2442	15.4	**2450**	**15.3**	2451	15.3	4	**2453**	**15.3**	2451	15.3	2459	15.3
444.namd	4	574	55.9	**574**	**55.9**	580	55.3	4	**582**	**55.1**	582	55.2	583	55.1
447.dealII	4	687	66.6	**633**	**72.3**	628	72.8	4	642	71.3	617	74.1	**641**	**71.3**
450.soplex	4	1687	19.8	1683	19.8	**1687**	**19.8**	4	1675	19.9	**1675**	**19.9**	1679	19.9
453.povray	4	**289**	**73.6**	291	73.2	289	73.7	4	**221**	**96.1**	226	94.2	221	96.2
454.calculix	4	624	52.8	628	52.6	**627**	**52.7**	4	615	53.7	623	53.0	**617**	**53.5**
459.GemsFDTD	4	2975	14.3	**2976**	**14.3**	2983	14.2	4	**2977**	**14.3**	2989	14.2	2974	14.3
465.tonto	4	911	43.2	915	43.0	**911**	**43.2**	4	**888**	**44.3**	894	44.0	887	44.4
470.lbm	4	3760	14.6	**3760**	**14.6**	3760	14.6	4	3760	14.6	**3759**	**14.6**	3759	14.6
481.wrf	4	**1514**	**29.5**	1515	29.5	1514	29.5	4	**1518**	**29.4**	1520	29.4	1517	29.4
482.sphinx3	4	**2574**	**30.3**	2571	30.3	2576	30.3	4	2558	30.5	2570	30.3	**2560**	**30.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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