SPEC® CFP2006 Result

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Itautec

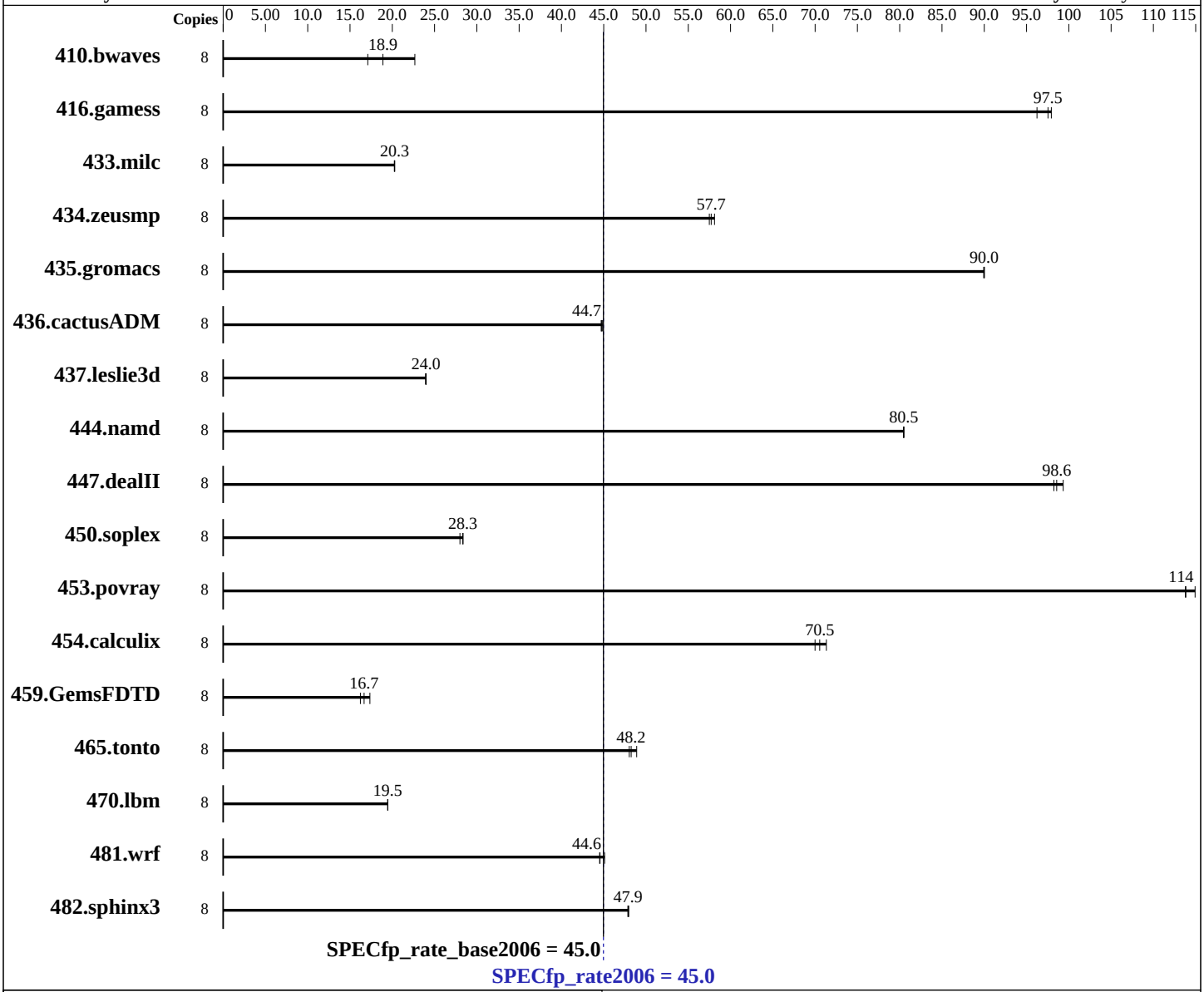
Servidor Itautec LX211 (Intel Xeon X5355)

SPECfp®_rate2006 = 45.0

SPECfp_rate_base2006 = 45.0

CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	1333MHz system bus
CPU MHz:	2660
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1-2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	Windows Server 2003 Enterprise Edition + SP1 (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 9.1 Package ID W_CC_C_9.1.025 Build no 20060519Z Intel Fortran Compiler for IA32 version 9.1 Package ID W_FC_C_9.1.025 Build no 20060519Z Microsoft Visual Studio .NET 2003 7.1.3088 (for libraries)
Auto Parallel:	No
File System:	NTFS

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L3 Cache: None
Other Cache: None
Memory: 6 GB (6x1GB DDR2-RAM PC2-5300F CAS 5-5-5)
Disk Subsystem: 120 GB SATA, 7200RPM
Other Hardware: None

System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**5755**	**18.9**	6357	17.1	4796	22.7	8	**5755**	**18.9**	6357	17.1	4796	22.7
416.gamess	8	**1606**	**97.5**	1600	97.9	1628	96.2	8	**1606**	**97.5**	1600	97.9	1628	96.2
433.milc	8	3628	20.2	**3621**	**20.3**	3621	20.3	8	3628	20.2	**3621**	**20.3**	3621	20.3
434.zeusmp	8	**1261**	**57.7**	1266	57.5	1253	58.1	8	**1261**	**57.7**	1266	57.5	1253	58.1
435.gromacs	8	635	89.9	635	90.0	**635**	**90.0**	8	635	89.9	635	90.0	**635**	**90.0**
436.cactusADM	8	2138	44.7	**2137**	**44.7**	2131	44.9	8	2138	44.7	**2137**	**44.7**	2131	44.9
437.leslie3d	8	3132	24.0	3143	23.9	**3133**	**24.0**	8	3132	24.0	3143	23.9	**3133**	**24.0**
444.namd	8	797	80.5	**797**	**80.5**	798	80.4	8	797	80.5	**797**	**80.5**	798	80.4
447.dealII	8	921	99.3	**929**	**98.6**	932	98.2	8	921	99.3	**929**	**98.6**	932	98.2
450.soplex	8	**2355**	**28.3**	2383	28.0	2352	28.4	8	**2355**	**28.3**	2383	28.0	2352	28.4
453.povray	8	374	114	**374**	**114**	370	115	8	374	114	**374**	**114**	370	115
454.calculix	8	943	70.0	925	71.3	**936**	**70.5**	8	943	70.0	925	71.3	**936**	**70.5**
459.GemsFDTD	8	5230	16.2	4891	17.4	**5093**	**16.7**	8	5230	16.2	4891	17.4	**5093**	**16.7**
465.tonto	8	1639	48.0	1610	48.9	**1632**	**48.2**	8	1639	48.0	1610	48.9	**1632**	**48.2**
470.lbm	8	**5646**	**19.5**	5645	19.5	5648	19.5	8	**5646**	**19.5**	5645	19.5	5648	19.5
481.wrf	8	2007	44.5	**2005**	**44.6**	1981	45.1	8	2007	44.5	**2005**	**44.6**	1981	45.1
482.sphinx3	8	3257	47.9	**3257**	**47.9**	3251	48.0	8	3257	47.9	**3257**	**47.9**	3251	48.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Base Compiler Invocation

C benchmarks:
```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:
```
icl -Qvc7.1
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc7.1 -Qc99 ifort
```

Base Portability Flags

436.cactusADM: `-Qlowercase /assume:underscore`
444.namd: `-TP`
447.dealII: `-DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T`
453.povray: `-DSPEC_CPU_WINDOWS_ICL`
454.calculix: `-DSPEC_CPU_NOZMODIFIER -Qlowercase`
481.wrf: `-DSPEC_CPU_WINDOWS_ICL`

Base Optimization Flags

C benchmarks:
`-fast /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

C++ benchmarks:
`-fast -Qcxx_features /F950000000 shlSMPMt.lib -link /FORCE:MULTIPLE`

Fortran benchmarks:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Benchmarks using both Fortran and C:
`-fast /F950000000 -link /FORCE:MULTIPLE`

Peak Optimization Flags

C benchmarks:

433.milc: `basepeak = yes`
470.lbm: `basepeak = yes`
482.sphinx3: `basepeak = yes`

C++ benchmarks:

444.namd: `basepeak = yes`
447.dealII: `basepeak = yes`
450.soplex: `basepeak = yes`
453.povray: `basepeak = yes`

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `basepeak = yes`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `basepeak = yes`

Benchmarks using both Fortran and C:

435.gromacs: `basepeak = yes`

436.cactusADM: `basepeak = yes`

454.calculix: `basepeak = yes`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml`

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