Itautec
Servidor Itautec MX201 (Intel Xeon E5320)

SPECfp®_rate2006 = 40.5
SPECfp_rate_base2006 = 40.5

SPEC® CFP2006 Result
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CPU2006 license: 9001
Test sponsor: Itautec
Tested by: Itautec

Hardware
CPU Name: Intel Xeon E5320
CPU Characteristics: 1066MHz system bus
CPU MHz: 1860
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1-2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per core, 4 MB shared / 2 cores

Software
Compiler: Intel C++ Compiler for IA32 version 9.1
Package ID W_CC_C_9.1.025 Build no 20060519Z
Intel Fortran Compiler for IA32 version 9.1
Package ID W_FC_C_9.1.025 Build no 20060519Z
Microsoft Visual Studio.NET 2003 7.1.3088 (for libraries)
Auto Parallel: No
File System: NTFS

CPU2006 license: 9001
Test date: Feb-2007
Hardware Availability: Feb-2007
Software Availability: May-2006

410.bwaves 8 25.2 25.2
416.gamess 8 88.7
433.milc 8 15.7
434.zeusmp 8 48.1
435.gromacs 8 83.3
436.cactusADM 8 39.8
437.leslie3d 8 18.6
444.namd 8 75.0
447.dealII 8 85.2
450.soplex 8 23.0
453.povray 8 106
454.calculix 8 64.6
459.GemsFDTD 8 17.5
465.tonto 8 43.5
470.lbm 8 20.3
481.wrf 8 36.0
482.sphinx3 8 37.1

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CPU2006 license: 9001
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System State: Default
Base Pointers: 32-bit
Peak Pointers: 32-bit
Software Availability: May-2006
Other Software: Microquill SmartHeap Library v.8.0 for SMP

Results Table

<table>
<thead>
<tr>
<th>Benchmark</th>
<th>Copies</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
<th>Seconds</th>
<th>Ratio</th>
</tr>
</thead>
<tbody>
<tr>
<td>410.bwaves</td>
<td>8</td>
<td>4310</td>
<td>25.2</td>
<td>4303</td>
<td>25.3</td>
<td>4306</td>
<td>25.2</td>
<td>4303</td>
<td>25.3</td>
</tr>
<tr>
<td>416.gamess</td>
<td>8</td>
<td>1766</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
<td>1765</td>
<td>88.7</td>
</tr>
<tr>
<td>433.mile</td>
<td>8</td>
<td>4665</td>
<td>15.7</td>
<td>4676</td>
<td>15.7</td>
<td>4667</td>
<td>15.7</td>
<td>4667</td>
<td>15.7</td>
</tr>
<tr>
<td>434.zeusmp</td>
<td>8</td>
<td>1517</td>
<td>48.0</td>
<td>1512</td>
<td>48.1</td>
<td>1503</td>
<td>48.4</td>
<td>1503</td>
<td>48.4</td>
</tr>
<tr>
<td>435.gromacs</td>
<td>8</td>
<td>686</td>
<td>83.3</td>
<td>687</td>
<td>83.2</td>
<td>686</td>
<td>83.3</td>
<td>686</td>
<td>83.3</td>
</tr>
<tr>
<td>436.cactusADM</td>
<td>8</td>
<td>2403</td>
<td>39.8</td>
<td>2391</td>
<td>40.0</td>
<td>2430</td>
<td>39.3</td>
<td>2430</td>
<td>39.3</td>
</tr>
<tr>
<td>437.leslie3d</td>
<td>8</td>
<td>4038</td>
<td>18.6</td>
<td>4040</td>
<td>18.6</td>
<td>4042</td>
<td>18.6</td>
<td>4042</td>
<td>18.6</td>
</tr>
<tr>
<td>444.namd</td>
<td>8</td>
<td>856</td>
<td>75.0</td>
<td>856</td>
<td>75.0</td>
<td>856</td>
<td>75.0</td>
<td>856</td>
<td>75.0</td>
</tr>
<tr>
<td>447.dealII</td>
<td>8</td>
<td>1058</td>
<td>86.5</td>
<td>1102</td>
<td>83.1</td>
<td>1074</td>
<td>85.2</td>
<td>1074</td>
<td>85.2</td>
</tr>
<tr>
<td>450.soplex</td>
<td>8</td>
<td>2913</td>
<td>22.9</td>
<td>2905</td>
<td>23.0</td>
<td>2896</td>
<td>23.0</td>
<td>2896</td>
<td>23.0</td>
</tr>
<tr>
<td>453.povray</td>
<td>8</td>
<td>403</td>
<td>106</td>
<td>403</td>
<td>106</td>
<td>402</td>
<td>106</td>
<td>402</td>
<td>106</td>
</tr>
<tr>
<td>454.calculix</td>
<td>8</td>
<td>1021</td>
<td>64.6</td>
<td>1021</td>
<td>64.6</td>
<td>1002</td>
<td>65.8</td>
<td>1002</td>
<td>65.8</td>
</tr>
<tr>
<td>459.GemsFDTD</td>
<td>8</td>
<td>4865</td>
<td>17.4</td>
<td>4861</td>
<td>17.5</td>
<td>4858</td>
<td>17.5</td>
<td>4858</td>
<td>17.5</td>
</tr>
<tr>
<td>465.tonto</td>
<td>8</td>
<td>1810</td>
<td>43.5</td>
<td>1807</td>
<td>43.6</td>
<td>1824</td>
<td>43.2</td>
<td>1824</td>
<td>43.2</td>
</tr>
<tr>
<td>470.lbm</td>
<td>8</td>
<td>5410</td>
<td>20.3</td>
<td>5414</td>
<td>20.3</td>
<td>5414</td>
<td>20.3</td>
<td>5414</td>
<td>20.3</td>
</tr>
<tr>
<td>481.wrf</td>
<td>8</td>
<td>2488</td>
<td>35.9</td>
<td>2479</td>
<td>36.0</td>
<td>2464</td>
<td>36.3</td>
<td>2464</td>
<td>36.3</td>
</tr>
<tr>
<td>482.sphinx3</td>
<td>8</td>
<td>4203</td>
<td>37.1</td>
<td>4207</td>
<td>37.1</td>
<td>4207</td>
<td>37.1</td>
<td>4207</td>
<td>37.1</td>
</tr>
</tbody>
</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes
This result was measured on the Servidor Itautec MX221. The Servidor Itautec MX221 and the Servidor Itautec MX201 are electronically equivalent.

Base Compiler Invocation
C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

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Software Availability: May-2006

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F9500000000 shlSMPMt.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F9500000000 shlSMPMt.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F9500000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F9500000000 -link /FORCE:MULTIPLE

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes
470.lbm: basepeak = yes
482.sphinx3: basepeak = yes

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes
447.dealII: basepeak = yes
450.soplex: basepeak = yes
453.povray: basepeak = yes

Fortran benchmarks:
410.bwaves: basepeak = yes
416.gamess: basepeak = yes
434.zeusmp: basepeak = yes
437.leslie3d: basepeak = yes
459.GemsFDTD: basepeak = yes
465.tonto: basepeak = yes

Benchmarks using both Fortran and C:
435.gromacs: basepeak = yes
436.cactusADM: basepeak = yes
454.calculix: basepeak = yes
481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.html
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Itautec-ic91-flags.20090714.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.