SPEC® CFP2006 Result

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Hewlett-Packard Company

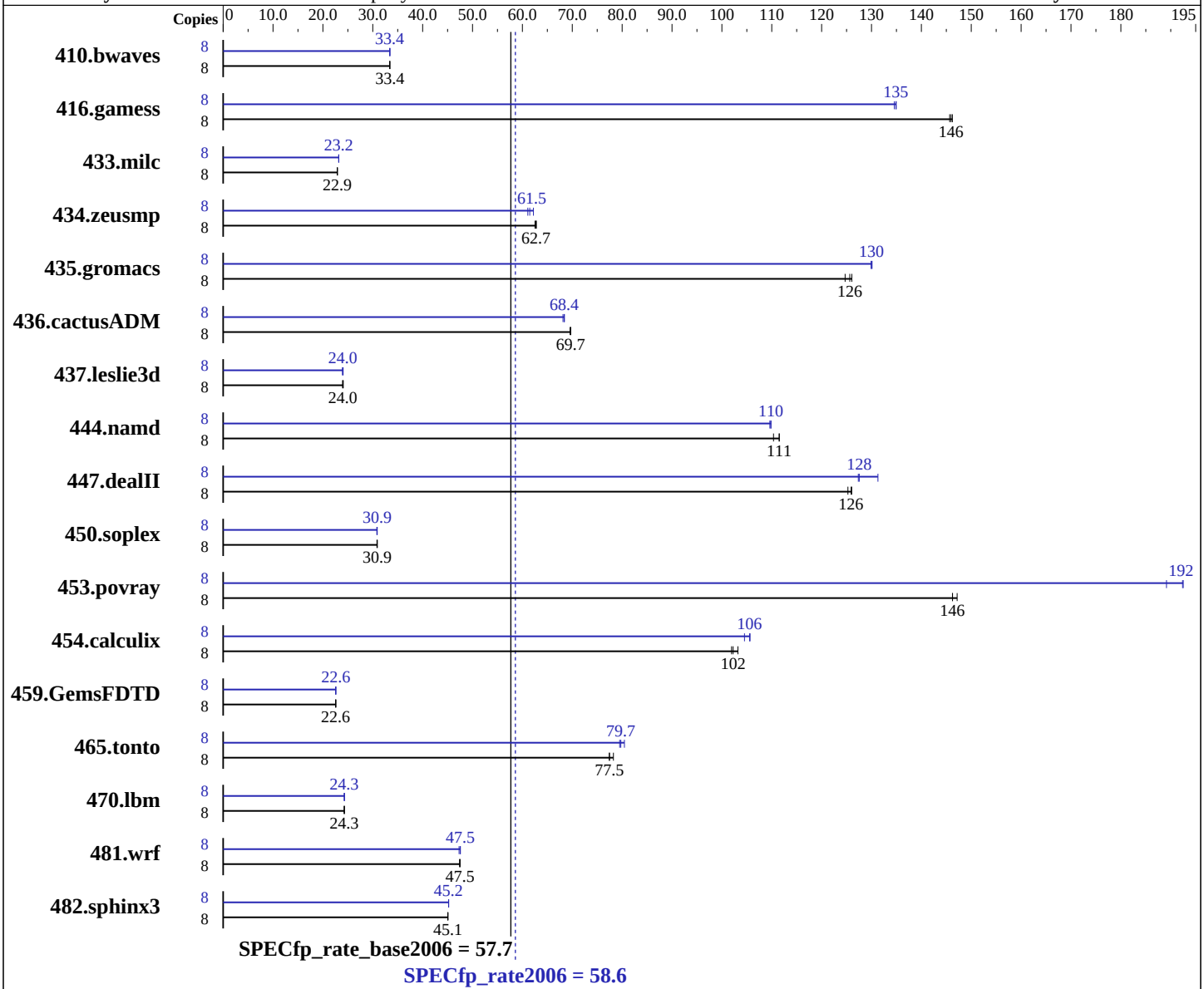
ProLiant BL480c
(2.66 GHz, Intel Xeon processor X5355)

SPECfp®_rate2006 = 58.6

SPECfp_rate_base2006 = 57.7

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Feb-2007
Hardware Availability: Jan-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333MHz system bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_cc_c_9.1.045 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Build 20061101, Package ID: l_fc_c_9.1.040
Auto Parallel:	No

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L3 Cache:	None	File System:	ext2
Other Cache:	None	System State:	Multi-user run level 3
Memory:	16 GB (8x2 GB PC2-5300F CL5)	Base Pointers:	64-bit
Disk Subsystem:	1x72 GB 10k SAS	Peak Pointers:	32/64-bit
Other Hardware:	None	Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3256	33.4	**3253**	**33.4**	3252	33.4	8	3253	33.4	3253	33.4	**3253**	**33.4**
416.gamess	8	1071	146	1075	146	**1073**	**146**	8	**1161**	**135**	1161	135	1164	135
433.milc	8	**3208**	**22.9**	3210	22.9	3206	22.9	8	3169	23.2	**3168**	**23.2**	3167	23.2
434.zeusmp	8	1159	62.8	1164	62.5	**1161**	**62.7**	8	1192	61.1	**1184**	**61.5**	1170	62.2
435.gromacs	8	453	126	458	125	**455**	**126**	8	**439**	**130**	439	130	440	130
436.cactusADM	8	1372	69.7	**1372**	**69.7**	1374	69.6	8	1403	68.1	**1398**	**68.4**	1397	68.4
437.leslie3d	8	3122	24.1	3141	23.9	**3132**	**24.0**	8	**3136**	**24.0**	3143	23.9	3125	24.1
444.namd	8	575	112	**575**	**111**	581	110	8	**584**	**110**	585	110	584	110
447.dealII	8	731	125	**727**	**126**	726	126	8	**718**	**128**	697	131	719	127
450.soplex	8	2159	30.9	**2161**	**30.9**	2164	30.8	8	2165	30.8	2160	30.9	**2161**	**30.9**
453.povray	8	**291**	**146**	289	147	291	146	8	225	189	**221**	**192**	221	193
454.calculix	8	639	103	647	102	**645**	**102**	8	**625**	**106**	625	106	631	105
459.GemsFDTD	8	3763	22.6	3756	22.6	**3761**	**22.6**	8	**3758**	**22.6**	3761	22.6	3757	22.6
465.tonto	8	1006	78.3	**1016**	**77.5**	1017	77.4	8	978	80.5	990	79.5	**987**	**79.7**
470.lbm	8	4526	24.3	4525	24.3	**4525**	**24.3**	8	4523	24.3	**4523**	**24.3**	4523	24.3
481.wrf	8	1882	47.5	1885	47.4	**1882**	**47.5**	8	**1879**	**47.5**	1889	47.3	1879	47.6
482.sphinx3	8	**3460**	**45.1**	3460	45.1	3461	45.1	8	3450	45.2	**3449**	**45.2**	3447	45.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Platform Notes

```
Power Regulator set to Static High Performance Mode in BIOS.
Adjacent Sector Prefetch Disabled in BIOS.
"/usr/bin/taskset" used to bind processes to CPUs.
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

Continued on next page

Peak Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/hp-ic91-flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/hp-ic91-flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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