Bull SAS NovaScale T860 (1.60 GHz, Intel Xeon 5110)

<table>
<thead>
<tr>
<th>SPECint®2006</th>
<th>10.6</th>
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<tbody>
<tr>
<td>SPECint_base2006</td>
<td>10.1</td>
</tr>
</tbody>
</table>

**CPU2006 license:** 20  
**Test sponsor:** Bull SAS  
**Tested by:** Bull SAS  
**Test date:** Mar-2007  
**Hardware Availability:** Sep-2006  
**Software Availability:** Nov-2006

### Hardware

- **CPU Name:** Intel Xeon 5110  
- **CPU Characteristics:** 1.60 GHz, 1066MHz bus  
- **CPU MHz:** 1600  
- **FPU:** Integrated  
- **CPU(s) enabled:** 1 core, 1 chip, 2 cores/chip  
- **CPU(s) orderable:** 1.2 chips  
- **Primary Cache:** 32 KB I + 32 KB D on chip per core  
- **Secondary Cache:** 4 MB I+D on chip per chip  
- **L3 Cache:** None  
- **Memory:** 12 GB (667 MHz ECC CL5 DDR2 FB-DIMM)  
- **Disk Subsystem:** 2x36GB SAS 15000 rpm  
- **Other Hardware:** None

### Software

- **Operating System:** Windows Server 2003 Enterprise X64 Edition  
- **Compiler:** Intel C++ Compiler 9.1 for 32-bit apps, Build 20061105Z Package ID: W_CC_P_9.1.033  
- **Microsoft Visual Studio.NET 2003 (libraries)**  
- **Auto Parallel:** No  
- **File System:** NTFS  
- **System State:** Default  
- **Base Pointers:** 32-bit  
- **Peak Pointers:** 32-bit  
- **Other Software:** MicroQuill SmartHeap Library 8.0 (shlW32M.lib)

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Standard Performance Evaluation Corporation  
info@spec.org  
http://www.spec.org/
SPEC CINT2006 Result

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SPECint_base2006 = 10.1

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Results Table

<table>
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</table>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

/numproc=1 flags was added to boot.ini invoke
uniprocessor environment

Base Compiler Invocation

C benchmarks:
  icl -Qvc7.1 -Qc99

C++ benchmarks:
  icl -Qvc7.1

Base Portability Flags

403.gcc: -DSPEC_CPU_WIN32

Continued on next page
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Base Portability Flags (Continued)

464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Base Optimization Flags

C benchmarks:
  -fast /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

C++ benchmarks:
  -fast -Qcxx_features /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE

Base Other Flags

C benchmarks:
  403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks:
  icl -Qvc7.1 -Qc99

C++ benchmarks:
  icl -Qvc7.1

Peak Portability Flags

403.gcc: -DSPEC_CPU_WIN32
464.h264ref: -DSPEC_CPU_NO_INTTYPES -DWIN32

Peak Optimization Flags

C benchmarks:
  400.perlbench: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast /F512000000 shlw32m.lib
  -link /FORCE:MULTIPLE
  401.bzip2: Same as 400.perlbench

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**Peak Optimization Flags (Continued)**

403.gcc: Same as 400.perlbench

429.mcf: -fast /F512000000 shlw32m.lib
         -link /FORCE:MULTIPLE

445.gobmk: Same as 400.perlbench

456.hmmer: Same as 400.perlbench

458.sjeng: Same as 400.perlbench

462.libquantum: Same as 400.perlbench

464.h264ref: Same as 400.perlbench

C++ benchmarks:
- Oprof_gen(pass 1) -Oprof_use(pass 2) -fast -Qcxx_features
 /F512000000 shlw32m.lib
 -link /FORCE:MULTIPLE

**Peak Other Flags**

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/flags.20090714.00.xml

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For other inquiries, please contact webmaster@spec.org.

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