SPEC® CFP2006 Result

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Bull SAS

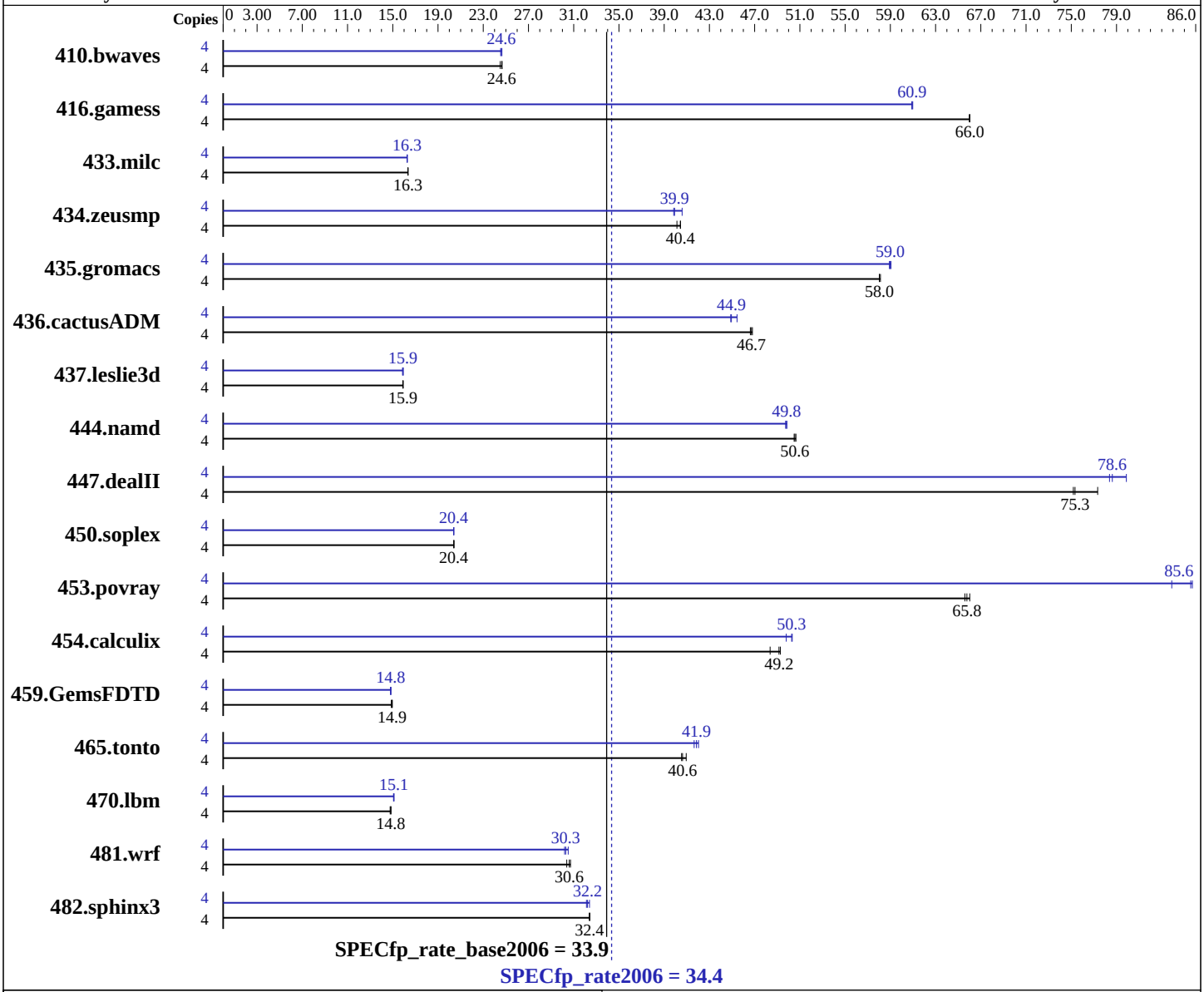
NovaScale T810
(2.4 GHz, Intel Xeon processor X3220)

SPECfp®_rate2006 = 34.4

SPECfp_rate_base2006 = 33.9

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Apr-2007
Hardware Availability: Feb-2007
Software Availability: Nov-2006



Hardware

CPU Name:	Intel Xeon X3220
CPU Characteristics:	2.4GHz, 2x4 MB L2 shared, 1066 MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores

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Software

Operating System:	SuSE Linux Enterprise Server 10 (EM64T) kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_cc_c_9.1.045 Build no 20061101 Intel Fortran Compiler for Intel EM64T-based applications, Version 9.1 Package ID l_fc_c_9.1.040 Build no 20061101
Auto Parallel:	No

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB PC2-5300 CL5)
Disk Subsystem: 2x73 GB 10k SAS
Other Hardware: None

File System: ext2
System State: Multi-user
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2219	24.5	**2206**	**24.6**	2204	24.7	4	2205	24.7	**2210**	**24.6**	2215	24.5
416.gamess	4	1186	66.0	1187	66.0	**1186**	**66.0**	4	**1286**	**60.9**	1284	61.0	1286	60.9
433.milc	4	2245	16.4	2248	16.3	**2247**	**16.3**	4	2255	16.3	**2256**	**16.3**	2259	16.3
434.zeusmp	4	907	40.1	**900**	**40.4**	900	40.4	4	914	39.8	897	40.6	**912**	**39.9**
435.gromacs	4	491	58.1	492	58.0	**492**	**58.0**	4	485	58.9	**484**	**59.0**	484	59.1
436.cactusADM	4	1021	46.8	1025	46.6	**1023**	**46.7**	4	1052	45.4	1065	44.9	**1064**	**44.9**
437.leslie3d	4	2362	15.9	**2364**	**15.9**	2367	15.9	4	2371	15.9	2362	15.9	**2363**	**15.9**
444.namd	4	633	50.7	**635**	**50.6**	635	50.5	4	644	49.8	645	49.7	**644**	**49.8**
447.dealII	4	**607**	**75.3**	592	77.3	609	75.2	4	573	79.9	584	78.4	**582**	**78.6**
450.soplex	4	1635	20.4	1636	20.4	**1635**	**20.4**	4	1636	20.4	1635	20.4	**1636**	**20.4**
453.povray	4	**324**	**65.8**	322	66.0	324	65.6	4	**249**	**85.6**	248	85.7	254	83.9
454.calculix	4	**671**	**49.2**	670	49.3	682	48.4	4	**656**	**50.3**	656	50.3	663	49.8
459.GemsFDTD	4	2837	15.0	**2841**	**14.9**	2855	14.9	4	2861	14.8	**2862**	**14.8**	2868	14.8
465.tonto	4	971	40.5	**969**	**40.6**	961	40.9	4	936	42.0	945	41.6	**940**	**41.9**
470.lbm	4	3722	14.8	**3708**	**14.8**	3703	14.8	4	3649	15.1	3639	15.1	**3639**	**15.1**
481.wrf	4	**1460**	**30.6**	1455	30.7	1470	30.4	4	1480	30.2	1464	30.5	**1475**	**30.3**
482.sphinx3	4	2404	32.4	2408	32.4	**2407**	**32.4**	4	2407	32.4	**2420**	**32.2**	2427	32.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Environment stack size set to 'unlimited'
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

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Base Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

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Peak Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

C++ benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

Fortran benchmarks:
-prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:
-prof_gen(pass 1) -prof_use(pass 2) -fast -auto_ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/EM64T_Intel91_flags.xml

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 11:30:55 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 May 2007.