SPEC® CFP2006 Result

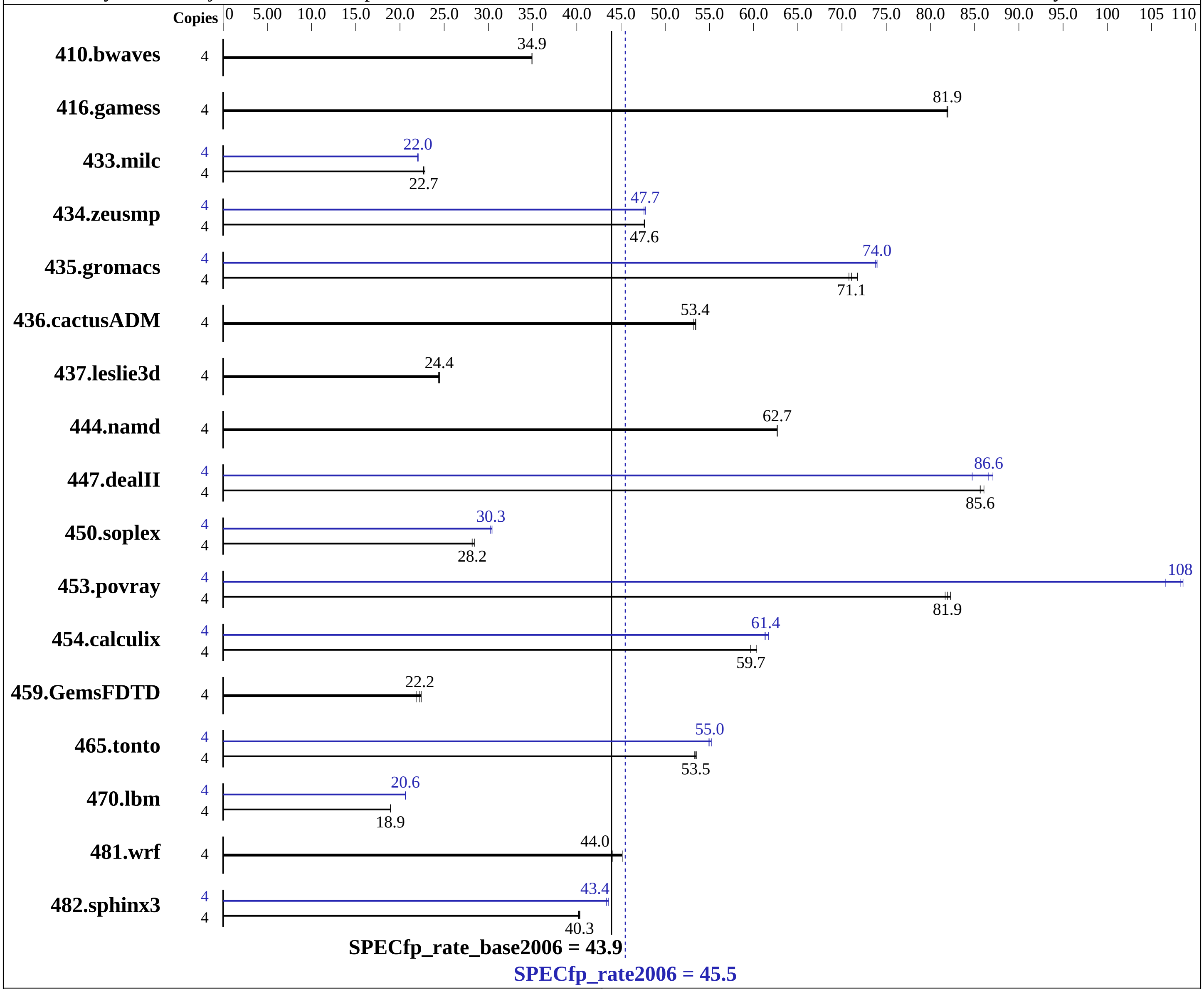
Fujitsu Siemens Computers

PRIMERGY TX300 S3, Intel Xeon processor 5160, 3.0 GHz

SPECfp®_rate2006 = 45.5

SPECfp_rate_base2006 = 43.9

CPU2006 license: 22		**Test date:**	Apr-2007
Test sponsor:	Fujitsu Siemens Computers	**Hardware Availability:**	Jul-2006
Tested by:	Fujitsu Siemens Computers	**Software Availability:**	Feb-2007



Hardware

CPU Name:	Intel Xeon 5160
CPU Characteristics:	1333 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ext2

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L3 Cache:	None
Other Cache:	None
Memory:	8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	SAS (73GB 15400 rpm)
Other Hardware:	None

System State:	Multiuser, Runlevel 3
Base Pointers:	64-bit
Peak Pointers:	32/64-bit
Other Software:	None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1556	34.9	**1556**	**34.9**	1557	34.9	4	1556	34.9	**1556**	**34.9**	1557	34.9
416.gamess	4	957	81.9	955	82.0	**956**	**81.9**	4	957	81.9	955	82.0	**956**	**81.9**
433.milc	4	**1618**	**22.7**	1621	22.7	1609	22.8	4	**1667**	**22.0**	1664	22.1	1670	22.0
434.zeusmp	4	**764**	**47.6**	763	47.7	764	47.6	4	765	47.6	**763**	**47.7**	762	47.8
435.gromacs	4	403	70.8	**402**	**71.1**	398	71.7	4	387	73.8	386	74.0	**386**	**74.0**
436.cactusADM	4	894	53.5	898	53.2	**895**	**53.4**	4	894	53.5	898	53.2	**895**	**53.4**
437.leslie3d	4	**1538**	**24.4**	1537	24.5	1545	24.3	4	**1538**	**24.4**	1537	24.5	1545	24.3
444.namd	4	512	62.7	512	62.7	**512**	**62.7**	4	512	62.7	512	62.7	**512**	**62.7**
447.dealII	4	534	85.6	532	86.1	**534**	**85.6**	4	540	84.7	526	87.1	**528**	**86.6**
450.soplex	4	1174	28.4	1185	28.2	**1184**	**28.2**	4	**1102**	**30.3**	1097	30.4	1103	30.3
453.povray	4	261	81.7	259	82.3	**260**	**81.9**	4	**197**	**108**	196	109	200	107
454.calculix	4	547	60.4	**553**	**59.7**	553	59.7	4	539	61.2	535	61.7	**538**	**61.4**
459.GemsFDTD	4	1894	22.4	**1908**	**22.2**	1944	21.8	4	1894	22.4	**1908**	**22.2**	1944	21.8
465.tonto	4	738	53.3	**736**	**53.5**	735	53.5	4	713	55.2	716	54.9	**715**	**55.0**
470.lbm	4	**2904**	**18.9**	2904	18.9	2902	18.9	4	**2666**	**20.6**	2664	20.6	2669	20.6
481.wrf	4	**1015**	**44.0**	990	45.1	1017	43.9	4	**1015**	**44.0**	990	45.1	1017	43.9
482.sphinx3	4	1940	40.2	1932	40.4	**1935**	**40.3**	4	1788	43.6	**1797**	**43.4**	1800	43.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1333 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
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General Notes (Continued)

```
the PRIMERGY TX300 S3 are electronically equivalent.

For information about Fujitsu Siemens Computers in your country please see:
http://www.fujitsu-siemens.com/countries
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
`-fast`

C++ benchmarks:
`-fast`

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-fast`

Benchmarks using both Fortran and C:
`-fast`

Peak Compiler Invocation

C benchmarks:
`/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/9.1.047/bin/icpc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

Fortran benchmarks (except as noted below):
`ifort`

434.zeusmp: `/opt/intel/fc/9.1.043/bin/ifort -I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

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Peak Optimization Flags

C benchmarks:

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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