SPEC® CFP2006 Result

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Fujitsu Siemens Computers

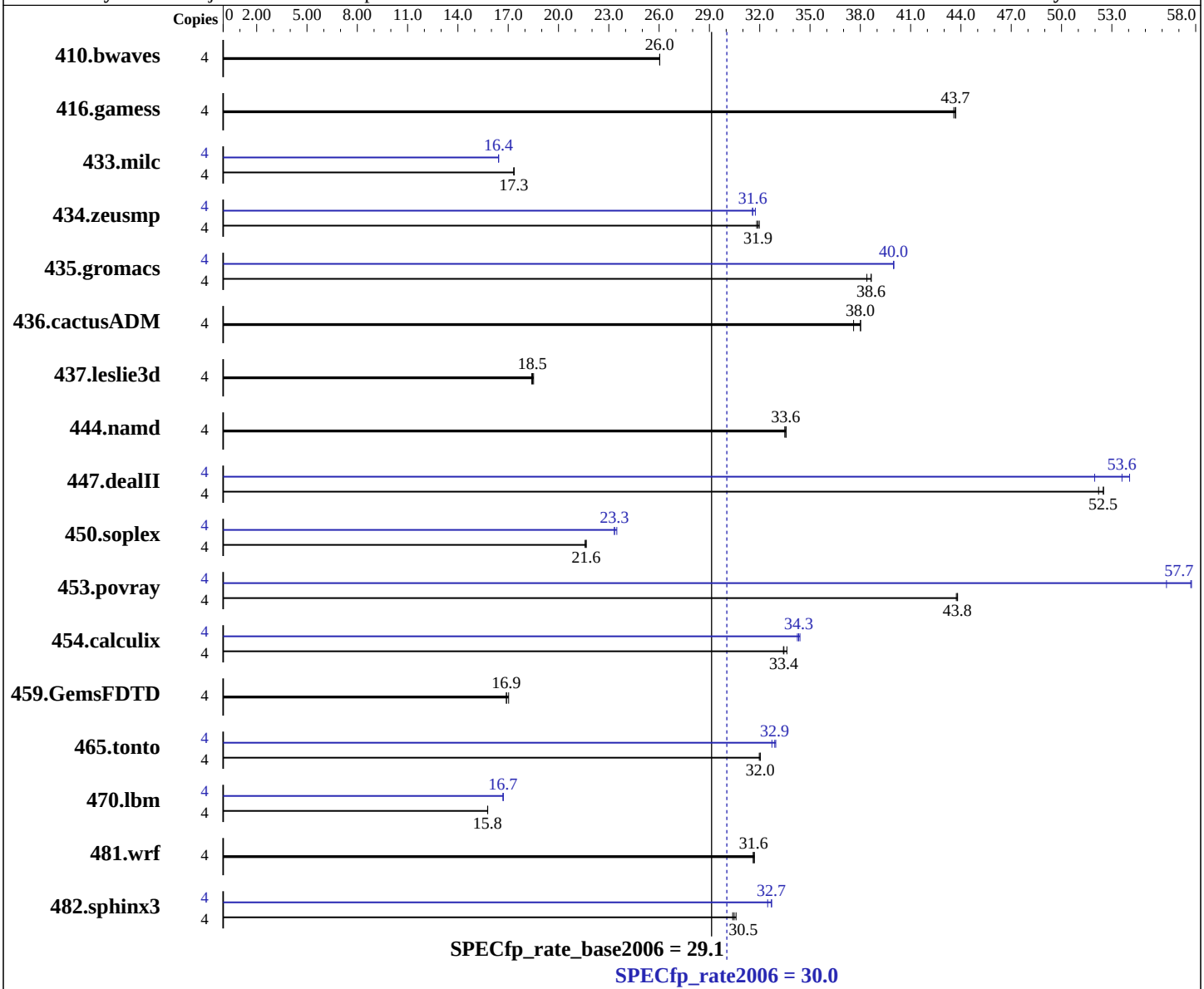
PRIMERGY TX300 S3, Intel Xeon processor 5110, 1.60 GHz

SPECfp®_rate2006 = 30.0

SPECfp_rate_base2006 = 29.1

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Jul-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 5110
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1600
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ext2

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L3 Cache: None
Other Cache: None
Memory: 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem: SAS (73GB 15400 rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2088	26.0	2088	26.0	**2088**	**26.0**	4	2088	26.0	2088	26.0	**2088**	**26.0**
416.gamess	4	1793	43.7	**1794**	**43.7**	1797	43.6	4	1793	43.7	**1794**	**43.7**	1797	43.6
433.milc	4	**2117**	**17.3**	2117	17.3	2118	17.3	4	2236	16.4	**2236**	**16.4**	2235	16.4
434.zeusmp	4	1144	31.8	**1141**	**31.9**	1138	32.0	4	1147	31.7	**1152**	**31.6**	1153	31.6
435.gromacs	4	744	38.4	**739**	**38.6**	739	38.7	4	714	40.0	714	40.0	**714**	**40.0**
436.cactusADM	4	1257	38.0	**1258**	**38.0**	1271	37.6	4	1257	38.0	**1258**	**38.0**	1271	37.6
437.leslie3d	4	2043	18.4	**2038**	**18.5**	2031	18.5	4	2043	18.4	**2038**	**18.5**	2031	18.5
444.namd	4	958	33.5	**956**	**33.6**	955	33.6	4	958	33.5	**956**	**33.6**	955	33.6
447.dealII	4	**872**	**52.5**	871	52.5	876	52.2	4	846	54.1	880	52.0	**854**	**53.6**
450.soplex	4	1541	21.7	**1541**	**21.6**	1546	21.6	4	**1429**	**23.3**	1421	23.5	1430	23.3
453.povray	4	486	43.8	**486**	**43.8**	487	43.7	4	**369**	**57.7**	378	56.3	368	57.8
454.calculix	4	982	33.6	**987**	**33.4**	988	33.4	4	964	34.2	**961**	**34.3**	959	34.4
459.GemsFDTD	4	2493	17.0	2512	16.9	**2511**	**16.9**	4	2493	17.0	2512	16.9	**2511**	**16.9**
465.tonto	4	1231	32.0	1229	32.0	**1229**	**32.0**	4	**1196**	**32.9**	1203	32.7	1194	33.0
470.lbm	4	**3485**	**15.8**	3483	15.8	3485	15.8	4	**3291**	**16.7**	3292	16.7	3291	16.7
481.wrf	4	1413	31.6	1410	31.7	**1413**	**31.6**	4	1413	31.6	1410	31.7	**1413**	**31.6**
482.sphinx3	4	2549	30.6	2565	30.4	**2558**	**30.5**	4	2401	32.5	2383	32.7	**2384**	**32.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
```

Continued on next page

General Notes (Continued)

the PRIMERGY TX300 S3 are electronically equivalent.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/cc/9.1.047/bin/icpc
-I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib

Fortran benchmarks (except as noted below):
ifort

434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
-I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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