**Fujitsu Siemens Computers**

**PRIMERGY TX300 S3, Intel Xeon processor 5110, 1.60 GHz**

<table>
<thead>
<tr>
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<th>Test date: May-2007</th>
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### Hardware

- **CPU Name:** Intel Xeon 5110
- **CPU Characteristics:** 1067 MHz system bus
- **CPU MHz:** 1600
- **FPU:** Integrated
- **CPU(s) enabled:** 4 cores, 2 chips, 2 cores/chip
- **CPU(s) orderable:** 1.2 chips
- **Primary Cache:** 32 KB I + 32 KB D on chip per core
- **Secondary Cache:** 4 MB I+D on chip per chip
- **L3 Cache:** None
- **Other Cache:** None
- **Memory:** 8 GB (8x1 GB DDR2 PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
- **Disk Subsystem:** SAS (73GB 15400 rpm)
- **Other Hardware:** None

### Software

- **Operating System:** 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
- **Compiler:** Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047
- **Auto Parallel:** No
- **File System:** ext2
- **System State:** Multiuser, Runlevel 3
- **Base Pointers:** 32-bit
- **Peak Pointers:** 32/64-bit
- **Other Software:** Smart Heap Library, Version 8.1
SPEC CINT2006 Result

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Results Table

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<th></th>
<th></th>
<th></th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Copies</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Copies</td>
<td>Seconds</td>
<td>Ratio</td>
<td>Seconds</td>
</tr>
<tr>
<td>400.perlbench</td>
<td></td>
<td>899</td>
<td>43.5</td>
<td>899</td>
<td>43.5</td>
<td>900</td>
<td>43.4</td>
<td></td>
<td>823</td>
<td>47.5</td>
<td>819</td>
</tr>
<tr>
<td>401.bzip2</td>
<td>4</td>
<td>1378</td>
<td>28.0</td>
<td>1376</td>
<td>28.0</td>
<td>1382</td>
<td>27.9</td>
<td>4</td>
<td>1307</td>
<td>29.5</td>
<td>1300</td>
</tr>
<tr>
<td>403.gcc</td>
<td>4</td>
<td>879</td>
<td>36.6</td>
<td>872</td>
<td>36.9</td>
<td>873</td>
<td>36.9</td>
<td>4</td>
<td>879</td>
<td>36.6</td>
<td>872</td>
</tr>
<tr>
<td>429.gobmk</td>
<td>4</td>
<td>994</td>
<td>36.7</td>
<td>996</td>
<td>36.6</td>
<td>998</td>
<td>36.6</td>
<td>4</td>
<td>1019</td>
<td>35.8</td>
<td>1017</td>
</tr>
<tr>
<td>445.gcc</td>
<td>4</td>
<td>989</td>
<td>42.4</td>
<td>996</td>
<td>42.1</td>
<td>996</td>
<td>42.1</td>
<td>4</td>
<td>922</td>
<td>45.5</td>
<td>918</td>
</tr>
<tr>
<td>456.hmmer</td>
<td>4</td>
<td>1390</td>
<td>26.9</td>
<td>1387</td>
<td>26.9</td>
<td>1390</td>
<td>26.8</td>
<td>4</td>
<td>1161</td>
<td>32.1</td>
<td>1161</td>
</tr>
<tr>
<td>458.sjeng</td>
<td>4</td>
<td>1215</td>
<td>39.8</td>
<td>1216</td>
<td>39.8</td>
<td>1212</td>
<td>39.9</td>
<td>4</td>
<td>1124</td>
<td>43.1</td>
<td>1121</td>
</tr>
<tr>
<td>462.libquantum</td>
<td>4</td>
<td>3581</td>
<td>23.1</td>
<td>3580</td>
<td>23.2</td>
<td>3573</td>
<td>23.2</td>
<td>4</td>
<td>3515</td>
<td>23.6</td>
<td>3521</td>
</tr>
<tr>
<td>464.h264ref</td>
<td>4</td>
<td>1356</td>
<td>65.3</td>
<td>1355</td>
<td>65.3</td>
<td>1355</td>
<td>65.3</td>
<td>4</td>
<td>1340</td>
<td>66.1</td>
<td>1342</td>
</tr>
<tr>
<td>471.omnetpp</td>
<td>4</td>
<td>913</td>
<td>27.4</td>
<td>906</td>
<td>27.6</td>
<td>905</td>
<td>27.6</td>
<td>4</td>
<td>832</td>
<td>30.0</td>
<td>831</td>
</tr>
<tr>
<td>473.astar</td>
<td>4</td>
<td>1056</td>
<td>26.6</td>
<td>1056</td>
<td>26.6</td>
<td>1055</td>
<td>26.6</td>
<td>4</td>
<td>1041</td>
<td>27.0</td>
<td>1042</td>
</tr>
<tr>
<td>483.xalancbmk</td>
<td>4</td>
<td>589</td>
<td>46.9</td>
<td>589</td>
<td>46.9</td>
<td>590</td>
<td>46.8</td>
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Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1067 MHz

All binaries were built with 32-bit Intel compiler except:
401.bzip2, 456.hmmer and 462.libquantum in peak were built with
64-bit Intel compiler by changing the path for include and library files.

BIOS configuration:
Adjacent Sector Prefetch = Disable

This result was measured on the PRIMERGY RX300 S3. The PRIMERGY RX300 S3 and
the PRIMERGY TX300 S3 are electronically equivalent.

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast
C++ benchmarks:
-xP -O3 -ipo -no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib
456.hmmer: /opt/intel/cce/9.1.047/bin/icc
            -I/opt/intel/cce/9.1.047/include
            -L/opt/intel/cce/9.1.047/lib
462.libquantum: /opt/intel/cce/9.1.047/bin/icc
               -I/opt/intel/cce/9.1.047/include
               -L/opt/intel/cce/9.1.047/lib
C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

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Peak Portability Flags (Continued)

456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof_gen(pass 1) -prof_use(pass 2) -fast
401.bzip2: -fast
403.gcc: basepeak = yes
429.mcf: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap
445.gobmk: Same as 429.mcf
456.hmmer: Same as 400.perlbench
458.sjeng: Same as 429.mcf
462.libquantum: Same as 400.perlbench
464.h264ref: Same as 429.mcf

C++ benchmarks:

471.omnetpp: -prof_gen(pass 1) -prof_use(pass 2) -xP -O3 -ipo
-no-prec-div -L/opt/SmartHeap_8_1/lib -lsmartheap
473.astar: -prof_gen(pass 1) -prof_use(pass 2) -fast
-L/opt/SmartHeap_8_1/lib -lsmartheap
483.xalancbmk: basepeak = yes

The flags file that was used to format this result can be browsed at
You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml
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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.