SPEC® CFP2006 Result

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Fujitsu Siemens Computers

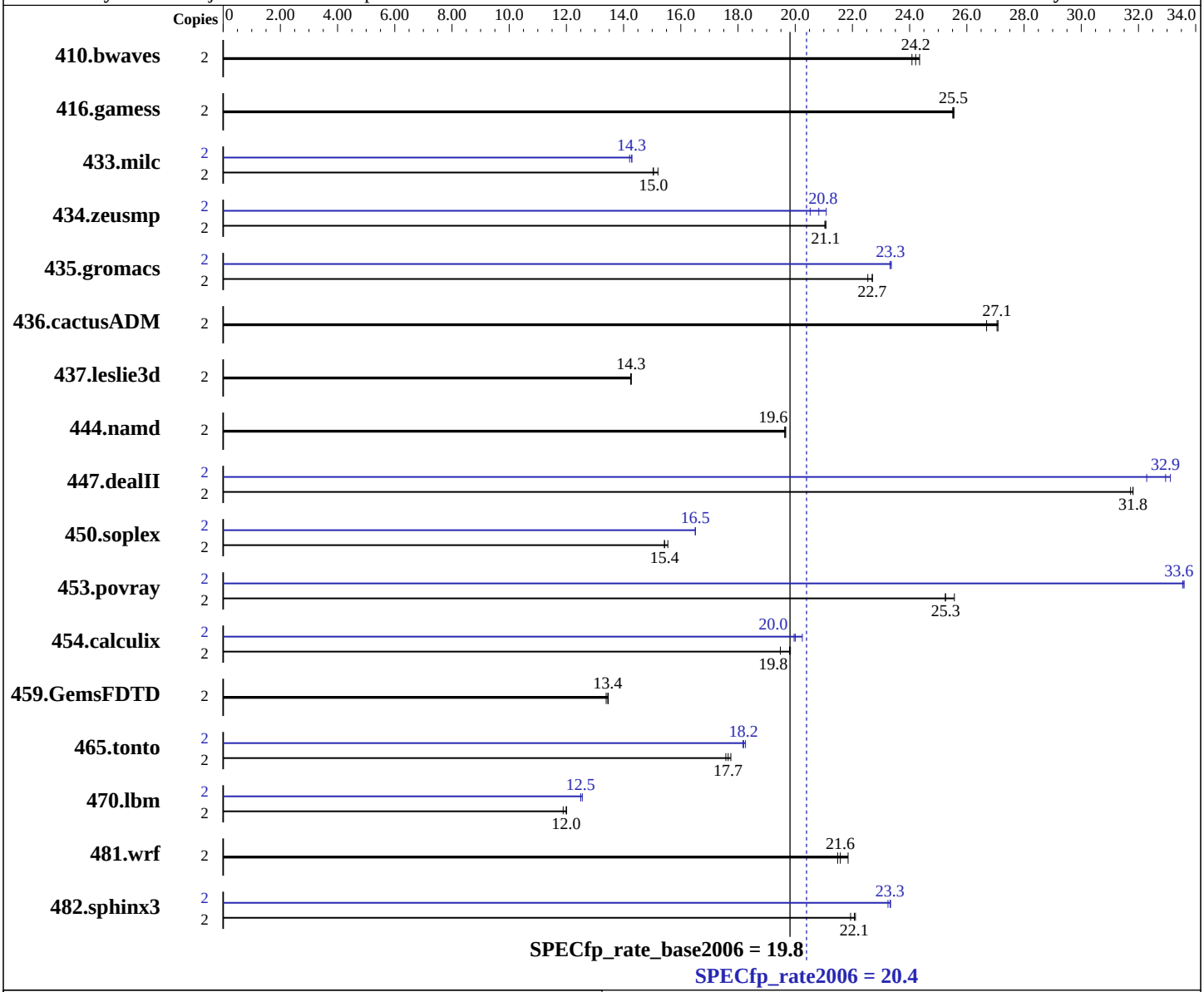
PRIMERGY RX100 S4, Intel Xeon processor 3040, 1.86 GHz

SPECfp®_rate2006 = 20.4

SPECfp_rate_base2006 = 19.8

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Sep-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 3040
CPU Characteristics:	1067 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ReiserFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem: SATA (160 GB, 7200 rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	**1123**	**24.2**	1116	24.3	1129	24.1	2	**1123**	**24.2**	1116	24.3	1129	24.1
416.gamess	2	1533	25.5	1535	25.5	**1534**	**25.5**	2	1533	25.5	1535	25.5	**1534**	**25.5**
433.milc	2	**1220**	**15.0**	1208	15.2	1221	15.0	2	1292	14.2	1285	14.3	**1286**	**14.3**
434.zeusmp	2	864	21.1	**864**	**21.1**	865	21.0	2	863	21.1	**874**	**20.8**	887	20.5
435.gromacs	2	**629**	**22.7**	634	22.5	629	22.7	2	613	23.3	**612**	**23.3**	611	23.4
436.cactusADM	2	895	26.7	**883**	**27.1**	882	27.1	2	895	26.7	**883**	**27.1**	882	27.1
437.leslie3d	2	1320	14.2	**1318**	**14.3**	1318	14.3	2	1320	14.2	**1318**	**14.3**	1318	14.3
444.namd	2	**816**	**19.6**	817	19.6	816	19.7	2	**816**	**19.6**	817	19.6	816	19.7
447.dealII	2	719	31.8	**719**	**31.8**	721	31.7	2	691	33.1	709	32.3	**694**	**32.9**
450.soplex	2	**1081**	**15.4**	1073	15.5	1081	15.4	2	1010	16.5	**1010**	**16.5**	1011	16.5
453.povray	2	421	25.2	**421**	**25.3**	416	25.6	2	317	33.6	**317**	**33.6**	317	33.5
454.calculix	2	832	19.8	**833**	**19.8**	847	19.5	2	**825**	**20.0**	827	20.0	815	20.2
459.GemsFDTD	2	**1578**	**13.4**	1576	13.5	1585	13.4	2	**1578**	**13.4**	1576	13.5	1585	13.4
465.tonto	2	1109	17.7	**1115**	**17.7**	1120	17.6	2	1083	18.2	**1082**	**18.2**	1078	18.3
470.lbm	2	**2292**	**12.0**	2289	12.0	2312	11.9	2	2188	12.6	**2198**	**12.5**	2198	12.5
481.wrf	2	1040	21.5	1023	21.8	**1035**	**21.6**	2	1040	21.5	1023	21.8	**1035**	**21.6**
482.sphinx3	2	1763	22.1	**1767**	**22.1**	1777	21.9	2	1671	23.3	**1672**	**23.3**	1677	23.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see:
```

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General Notes (Continued)

http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Continued on next page

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-fast`

Peak Compiler Invocation

C benchmarks:
`/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/9.1.047/bin/icpc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

Fortran benchmarks (except as noted below):
`ifort`

434.zeusmp: `/opt/intel/fc/9.1.043/bin/ifort -I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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