SPEC® CFP2006 Result

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Fujitsu Siemens Computers

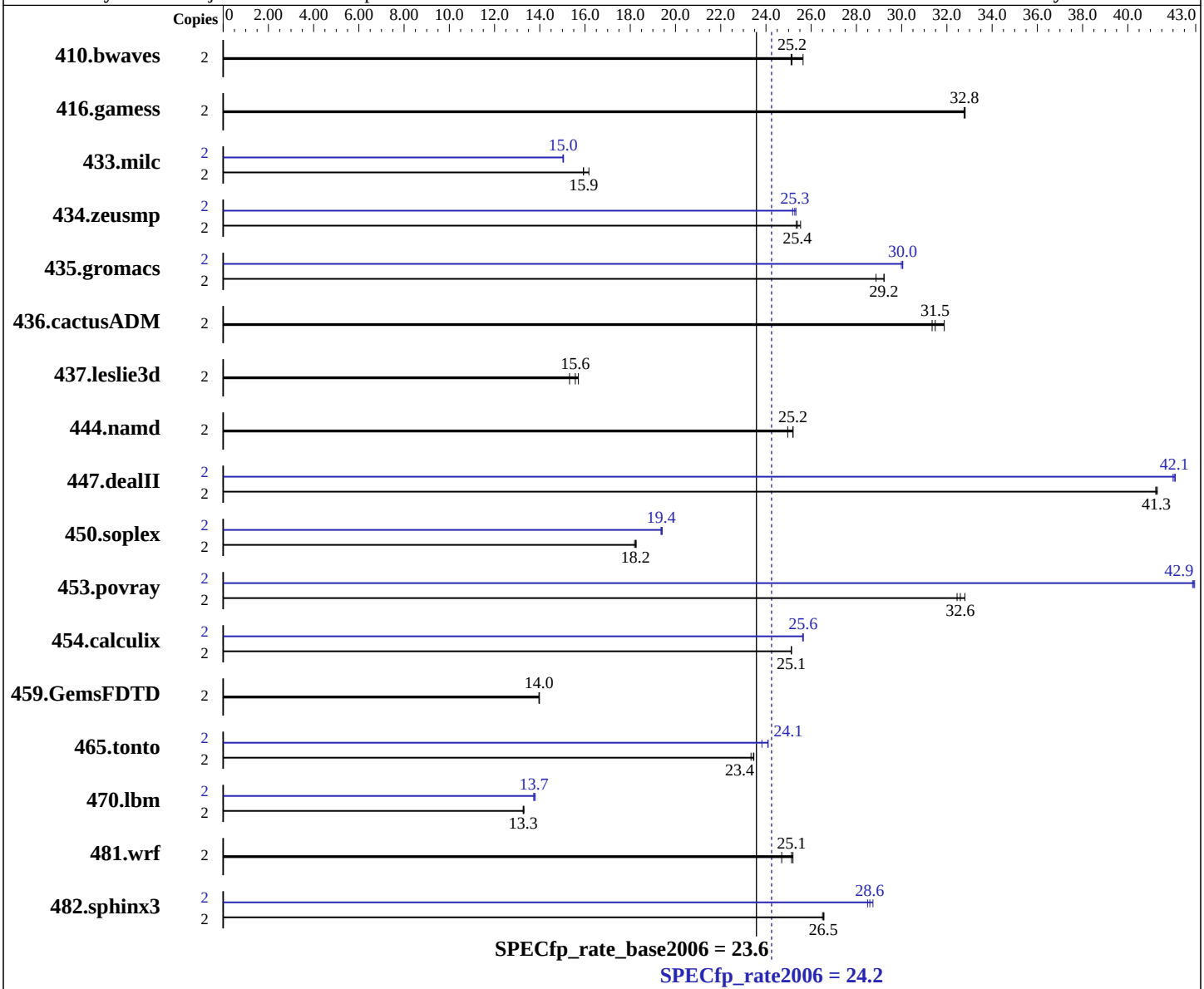
PRIMERGY TX150 S5, Intel Xeon processor 3060, 2.40 GHz

SPECfp®_rate2006 = 24.2

SPECfp_rate_base2006 = 23.6

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: May-2007
Hardware Availability: Sep-2006
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Xeon 3060
CPU Characteristics:	1067 MHz system bus
CPU MHz:	2400
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ReiserFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem: SATA (160 GB 7200 rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1060	25.6	**1081**	**25.2**	1082	25.1	2	1060	25.6	**1081**	**25.2**	1082	25.1
416.gamess	2	1194	32.8	**1195**	**32.8**	1195	32.8	2	1194	32.8	**1195**	**32.8**	1195	32.8
433.milc	2	1135	16.2	**1152**	**15.9**	1153	15.9	2	1220	15.1	**1221**	**15.0**	1222	15.0
434.zeusmp	2	713	25.5	718	25.3	**717**	**25.4**	2	723	25.2	719	25.3	**720**	**25.3**
435.gromacs	2	495	28.9	489	29.2	**489**	**29.2**	2	**475**	**30.0**	476	30.0	475	30.0
436.cactusADM	2	**759**	**31.5**	750	31.9	762	31.3	2	**759**	**31.5**	750	31.9	762	31.3
437.leslie3d	2	1228	15.3	1197	15.7	**1207**	**15.6**	2	1228	15.3	1197	15.7	**1207**	**15.6**
444.namd	2	643	25.0	**637**	**25.2**	636	25.2	2	643	25.0	**637**	**25.2**	636	25.2
447.dealII	2	554	41.3	**555**	**41.3**	555	41.2	2	**544**	**42.1**	545	42.0	543	42.1
450.soplex	2	917	18.2	**915**	**18.2**	913	18.3	2	**861**	**19.4**	859	19.4	862	19.4
453.povray	2	**326**	**32.6**	328	32.5	324	32.8	2	**248**	**42.9**	248	43.0	248	42.9
454.calculix	2	**657**	**25.1**	657	25.1	657	25.1	2	644	25.6	**643**	**25.6**	643	25.7
459.GemsFDTD	2	1517	14.0	1520	14.0	**1519**	**14.0**	2	1517	14.0	1520	14.0	**1519**	**14.0**
465.tonto	2	**839**	**23.4**	839	23.5	843	23.3	2	**817**	**24.1**	826	23.8	817	24.1
470.lbm	2	2066	13.3	**2070**	**13.3**	2072	13.3	2	1991	13.8	**1999**	**13.7**	2001	13.7
481.wrf	2	**889**	**25.1**	905	24.7	887	25.2	2	**889**	**25.1**	905	24.7	887	25.2
482.sphinx3	2	1467	26.6	1471	26.5	**1468**	**26.5**	2	**1363**	**28.6**	1357	28.7	1368	28.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 1067 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers in your country please see:
```

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General Notes (Continued)

http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Continued on next page

Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:
`-fast`

Peak Compiler Invocation

C benchmarks:
`/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/9.1.047/bin/icpc -I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib`

Fortran benchmarks (except as noted below):
`ifort`

434.zeusmp: `/opt/intel/fc/9.1.043/bin/ifort -I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 10:59:56 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 12 June 2007.