SPEC® CFP2006 Result

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Fujitsu Siemens Computers

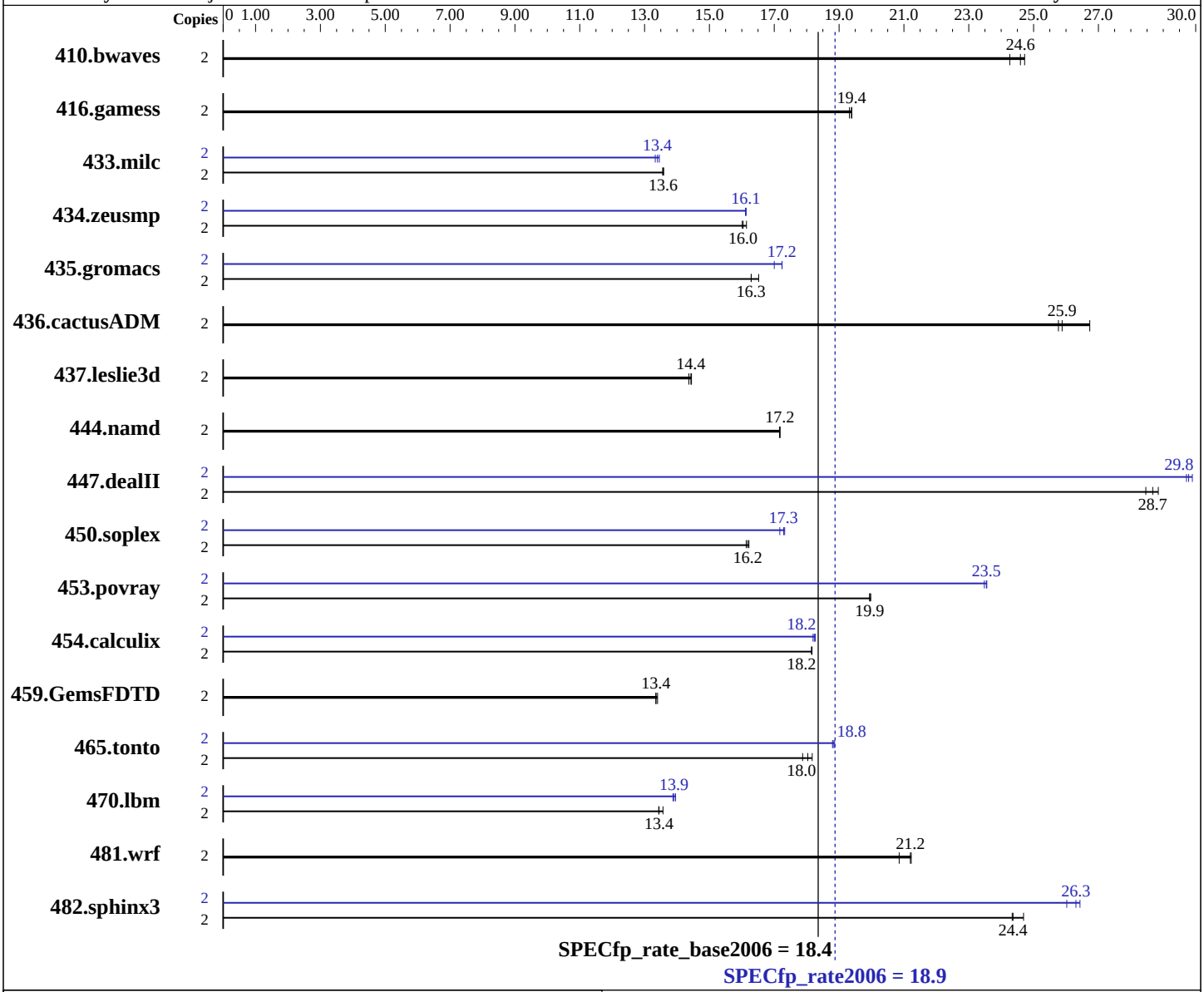
PRIMERGY TX150 S5, Intel Pentium D processor 925, 3.0 GHz

SPECfp®_rate2006 = 18.9

SPECfp_rate_base2006 = 18.4

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Jun-2007
Hardware Availability: Oct-2006
Software Availability: Mar-2007



Hardware

CPU Name:	Intel Pentium D 925
CPU Characteristics:	800 MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	12 K micro-ops I + 16 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per core

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Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp on an x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package-ID: l_cc_p_9.1.047 Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070215, Package ID: l_fc_p_9.1.043
Auto Parallel:	No
File System:	ReiserFS

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 PC2-4200E, 2 rank, CAS 4-4-4, with ECC)
Disk Subsystem: SATA(160GB 7200 rpm)
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1120	24.3	1099	24.7	**1105**	**24.6**	2	1120	24.3	1099	24.7	**1105**	**24.6**
416.gamess	2	2027	19.3	**2021**	**19.4**	2019	19.4	2	2027	19.3	**2021**	**19.4**	2019	19.4
433.milc	2	1354	13.6	**1353**	**13.6**	1351	13.6	2	1377	13.3	1365	13.5	**1370**	**13.4**
434.zeusmp	2	1137	16.0	**1135**	**16.0**	1127	16.1	2	**1129**	**16.1**	1130	16.1	1128	16.1
435.gromacs	2	864	16.5	**877**	**16.3**	877	16.3	2	828	17.2	**828**	**17.2**	840	17.0
436.cactusADM	2	928	25.8	**923**	**25.9**	894	26.7	2	928	25.8	**923**	**25.9**	894	26.7
437.leslie3d	2	1309	14.4	**1303**	**14.4**	1301	14.4	2	1309	14.4	**1303**	**14.4**	1301	14.4
444.namd	2	934	17.2	935	17.2	**934**	**17.2**	2	934	17.2	935	17.2	**934**	**17.2**
447.dealII	2	804	28.5	793	28.8	**798**	**28.7**	2	770	29.7	765	29.9	**768**	**29.8**
450.soplex	2	**1031**	**16.2**	1034	16.1	1028	16.2	2	**965**	**17.3**	971	17.2	963	17.3
453.povray	2	533	20.0	**533**	**19.9**	534	19.9	2	453	23.5	452	23.6	**452**	**23.5**
454.calculix	2	909	18.1	908	18.2	**908**	**18.2**	2	903	18.3	**905**	**18.2**	907	18.2
459.GemsFDTD	2	1584	13.4	**1589**	**13.4**	1590	13.3	2	1584	13.4	**1589**	**13.4**	1590	13.3
465.tonto	2	**1091**	**18.0**	1101	17.9	1083	18.2	2	1043	18.9	1047	18.8	**1046**	**18.8**
470.lbm	2	2025	13.6	2045	13.4	**2044**	**13.4**	2	1969	14.0	1981	13.9	**1975**	**13.9**
481.wrf	2	1071	20.9	**1054**	**21.2**	1053	21.2	2	1071	20.9	**1054**	**21.2**	1053	21.2
482.sphinx3	2	1579	24.7	1602	24.3	**1600**	**24.4**	2	**1482**	**26.3**	1498	26.0	1475	26.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
```

General Notes

```
The system bus runs at 800 MHz

All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.
```

Continued on next page

General Notes (Continued)

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http://www.fujitsu-siemens.com/countries

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Continued on next page

Base Optimization Flags (Continued)

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks:
/opt/intel/cc/9.1.047/bin/icc -I/opt/intel/cc/9.1.047/include
-L/opt/intel/cc/9.1.047/lib

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/cc/9.1.047/bin/icpc
-I/opt/intel/cc/9.1.047/include -L/opt/intel/cc/9.1.047/lib

Fortran benchmarks (except as noted below):
ifort

434.zeusmp: /opt/intel/fc/9.1.043/bin/ifort
-I/opt/intel/fc/9.1.043/include -L/opt/intel/fc/9.1.043/lib

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

470.lbm: `Same as 433.milc`

482.sphinx3: `-fast`

C++ benchmarks:

444.namd: `basepeak = yes`

447.dealII: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

450.soplex: `Same as 447.dealII`

453.povray: `Same as 447.dealII`

Fortran benchmarks:

410.bwaves: `basepeak = yes`

416.gamess: `basepeak = yes`

434.zeusmp: `-fast`

437.leslie3d: `basepeak = yes`

459.GemsFDTD: `basepeak = yes`

465.tonto: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

Benchmarks using both Fortran and C:

435.gromacs: `-prof_gen`(pass 1) `-prof_use`(pass 2) `-fast`

436.cactusADM: `basepeak = yes`

454.calculix: `Same as 435.gromacs`

481.wrf: `basepeak = yes`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/CPU2006_flags.20090714.09.xml`

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