



SPEC® CFP2006 Result

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NEC Corporation

Express5800/120Ri-2
(Intel Xeon processor 5140)

SPECfp®_rate2006 = 37.7

SPECfp_rate_base2006 = 36.5

CPU2006 license: 9006

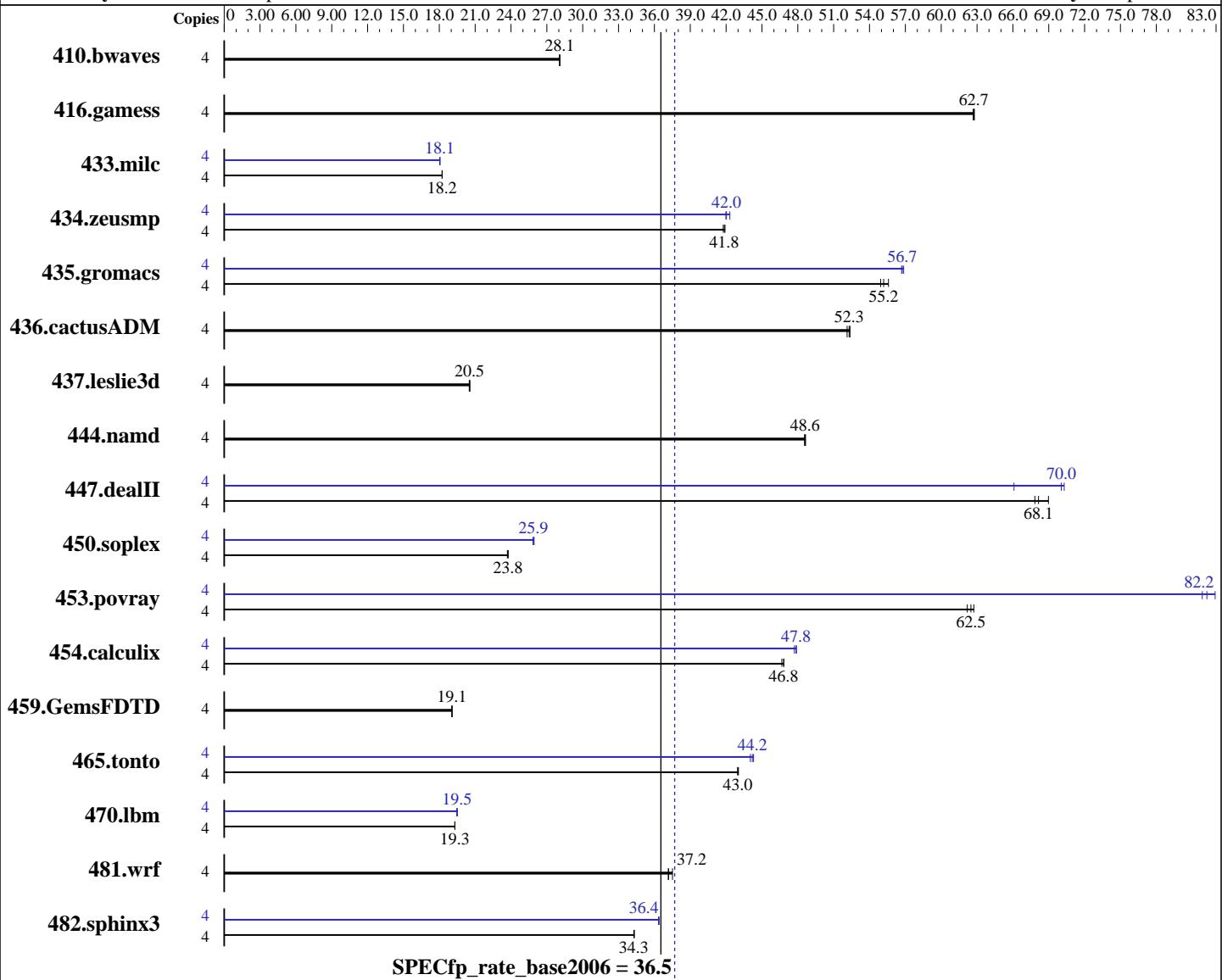
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2007

Hardware Availability: May-2007

Software Availability: Apr-2007



SPECfp_rate_base2006 = 36.5

SPECfp_rate2006 = 37.7

Hardware

CPU Name: Intel Xeon 5140
CPU Characteristics: 2.33 GHz, 4MB L2, 1333MHz bus
CPU MHz: 2333
FPU: Integrated
CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler: Intel C++ Compiler for IA32/EM64T application, Version 9.1 - Build 20070320, Package-ID: l_cc_c_9.1.049
Intel Fortran Compiler for IA32/EM64T application, Version 9.1 - Build 20070320, Package ID: l_fc_c_9.1.045
Auto Parallel: No
File System: ext2

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4x2 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000RPM
Other Hardware: None

System State: Multiuser, Runlevel 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1937	28.1	1937	28.1	1935	28.1	4	1937	28.1	1937	28.1	1935	28.1
416.gamess	4	1250	62.7	1248	62.8	1248	62.7	4	1250	62.7	1248	62.8	1248	62.7
433.milc	4	2014	18.2	2013	18.2	2013	18.2	4	2036	18.0	2034	18.1	2034	18.1
434.zeusmp	4	872	41.8	870	41.8	869	41.9	4	866	42.0	867	42.0	860	42.3
435.gromacs	4	518	55.2	520	54.9	514	55.6	4	504	56.7	502	56.9	503	56.7
436.cactusADM	4	913	52.4	914	52.3	917	52.1	4	913	52.4	914	52.3	917	52.1
437.leslie3d	4	1831	20.5	1829	20.6	1834	20.5	4	1831	20.5	1829	20.6	1834	20.5
444.namd	4	659	48.6	661	48.6	660	48.6	4	659	48.6	661	48.6	660	48.6
447.dealII	4	663	69.0	672	68.1	675	67.8	4	692	66.1	651	70.3	653	70.0
450.soplex	4	1405	23.8	1408	23.7	1404	23.8	4	1289	25.9	1286	25.9	1291	25.8
453.povray	4	342	62.2	341	62.5	339	62.7	4	257	82.9	259	82.2	260	81.8
454.calculix	4	707	46.7	705	46.8	705	46.8	4	689	47.9	690	47.8	692	47.7
459.GemsFDTD	4	2226	19.1	2227	19.1	2226	19.1	4	2226	19.1	2227	19.1	2226	19.1
465.tonto	4	916	43.0	916	43.0	915	43.0	4	889	44.3	891	44.2	894	44.0
470.lbm	4	2848	19.3	2847	19.3	2847	19.3	4	2817	19.5	2821	19.5	2823	19.5
481.wrf	4	1191	37.5	1202	37.2	1201	37.2	4	1191	37.5	1202	37.2	1201	37.2
482.sphinx3	4	2271	34.3	2274	34.3	2274	34.3	4	2144	36.4	2142	36.4	2145	36.3

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

General Notes

The system bus runs at 1333 MHz
All binaries were built with 64-bit Intel compiler except:
433.milc, 434.zeusmp, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with
32-bit Intel compiler by changing the path for include and library files.



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast



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Peak Compiler Invocation

C benchmarks:

```
/opt/intel/cc/9.1.049/bin/icc -I/opt/intel/cc/9.1.049/include  
-L/opt/intel/cc/9.1.049/lib
```

C++ benchmarks (except as noted below):

```
icpc
```

```
450.soplex: /opt/intel/cc/9.1.049/bin/icpc  
-I/opt/intel/cc/9.1.049/include -L/opt/intel/cc/9.1.049/lib
```

Fortran benchmarks (except as noted below):

```
ifort
```

```
434.zeusmp: /opt/intel/fc/9.1.045/bin/ifort  
-I/opt/intel/fc/9.1.045/include -L/opt/intel/fc/9.1.045/lib
```

Benchmarks using both Fortran and C:

```
icc ifort
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof_gen(pass 1) -prof_use(pass 2) -fast
```

```
470.lbm: Same as 433.milc
```

```
482.sphinx3: -fast
```

C++ benchmarks:

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Peak Optimization Flags (Continued)

444.namd: basepeak = yes

447.dealII: -prof_gen(pass 1) -prof_use(pass 2) -fast

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -prof_gen(pass 1) -prof_use(pass 2) -fast

Benchmarks using both Fortran and C:

435.gromacs: -prof_gen(pass 1) -prof_use(pass 2) -fast

436.cactusADM: basepeak = yes

454.calculix: Same as 435.gromacs

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic91-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic91-linux-flags.xml>

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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