



# SPEC® CINT2006 Result

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## NEC Corporation

Express5800/120Bb-6  
(Intel Xeon processor X5355)

**SPECint®2006 = 18.9**

**SPECint\_base2006 = 17.2**

CPU2006 license: 9006

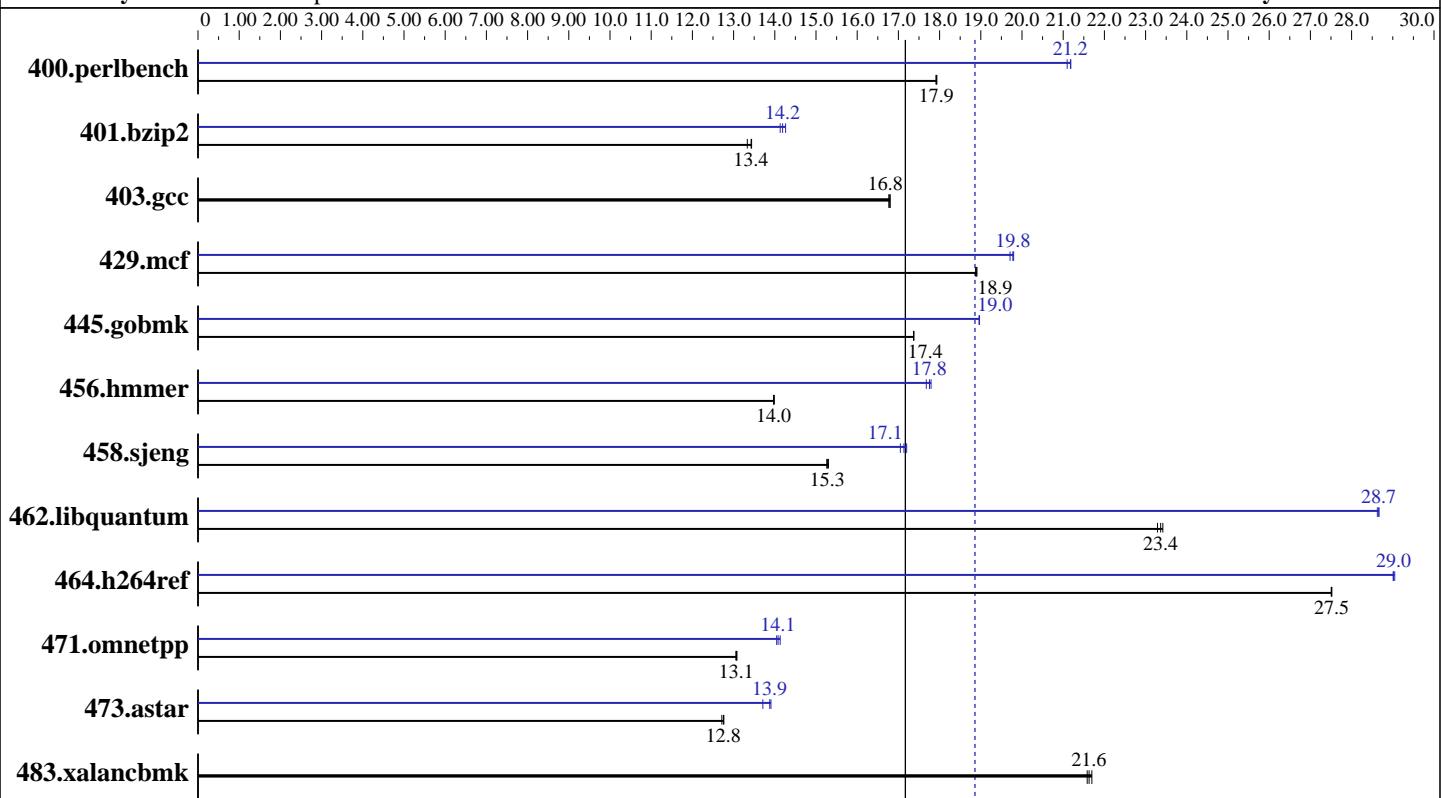
Test sponsor: NEC Corporation

Tested by: NEC Corporation

Test date: Jul-2007

Hardware Availability: Jan-2007

Software Availability: Jun-2007



**SPECint\_base2006 = 17.2**

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### Hardware

CPU Name:	Intel Xeon X5355
CPU Characteristics:	2.66 GHz, 2x4 MB L2 shared, 1333 MHz bus
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB DDR2 5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x146.5 GB SAS, 10000RPM
Other Hardware:	None

### Software

Operating System:	64-Bit SUSE LINUX Enterprise Server 10, Kernel 2.6.16.21-0.8-smp for x86_64
Compiler:	Intel C++ Compiler for IA32/EM64T application, Version 10.0 - Build 20070426 Package ID: l_cc_p_10.0.023
Auto Parallel:	No
File System:	ext2
System State:	Multiuser, Runlevel 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio										
400.perlbench	545	17.9	545	17.9	<b>545</b>	<b>17.9</b>	463	21.1	461	21.2	<b>461</b>	<b>21.2</b>
401.bzip2	718	13.4	<b>719</b>	<b>13.4</b>	724	13.3	<b>680</b>	<b>14.2</b>	677	14.3	683	14.1
403.gcc	<b>479</b>	<b>16.8</b>	480	16.8	479	16.8	<b>479</b>	<b>16.8</b>	480	16.8	479	16.8
429.mcf	483	18.9	<b>483</b>	<b>18.9</b>	482	18.9	<b>461</b>	<b>19.8</b>	463	19.7	461	19.8
445.gobmk	<b>604</b>	<b>17.4</b>	604	17.4	604	17.4	<b>553</b>	<b>19.0</b>	553	19.0	553	19.0
456.hammer	667	14.0	<b>668</b>	<b>14.0</b>	668	14.0	<b>526</b>	<b>17.8</b>	524	17.8	528	17.7
458.sjeng	793	15.3	<b>792</b>	<b>15.3</b>	791	15.3	<b>703</b>	<b>17.2</b>	<b>706</b>	<b>17.1</b>	710	17.0
462.libquantum	890	23.3	<b>887</b>	<b>23.4</b>	885	23.4	<b>723</b>	<b>28.7</b>	724	28.6	<b>723</b>	<b>28.7</b>
464.h264ref	805	27.5	804	27.5	<b>804</b>	<b>27.5</b>	763	29.0	762	29.0	<b>763</b>	<b>29.0</b>
471.omnetpp	478	13.1	479	13.1	<b>478</b>	<b>13.1</b>	445	14.0	442	14.1	<b>444</b>	<b>14.1</b>
473.astar	550	12.8	<b>550</b>	<b>12.8</b>	552	12.7	512	13.7	<b>506</b>	<b>13.9</b>	505	13.9
483.xalancbmk	<b>319</b>	<b>21.6</b>	320	21.6	318	21.7	<b>319</b>	<b>21.6</b>	320	21.6	318	21.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

## General Notes

All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hammer, for peak, are compiled in 64-bit mode

## Base Compiler Invocation

C benchmarks:  
icc

C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32

462.libquantum: -DSPEC\_CPU\_LINUX

483.xalancbmk: -DSPEC\_CPU\_LINUX



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## Base Optimization Flags

C benchmarks:  
-fast

C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

## Base Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /opt/intel/cce/10.0.023/bin/icc

456.hmmr: /opt/intel/cce/10.0.023/bin/icc

C++ benchmarks:

icpc

## Peak Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
401.bzip2: -DSPEC\_CPU\_LP64  
456.hmmr: -DSPEC\_CPU\_LP64  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias  
-prefetch

401.bzip2: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include  
-prof-gen(pass 1) -prof-use(pass 2) -fast

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## Peak Optimization Flags (Continued)

403.gcc: basepeak = yes

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo  
-no-prec\_div -ansi-alias

456.hmmr: -L/opt/intel/cce/10.0.023/lib -I/opt/intel/cce/10.0.023/include  
-prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Obo  
-prefetch -opt-streaming-stores always

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/opt/SmartHeap\_8.1/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/NEC-ic10-linux-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/NEC-ic10-linux-flags.xml>



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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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