SPEC® CFP2006 Result

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Lenovo Group Limited

(Test Sponsor: Intel Corporation)

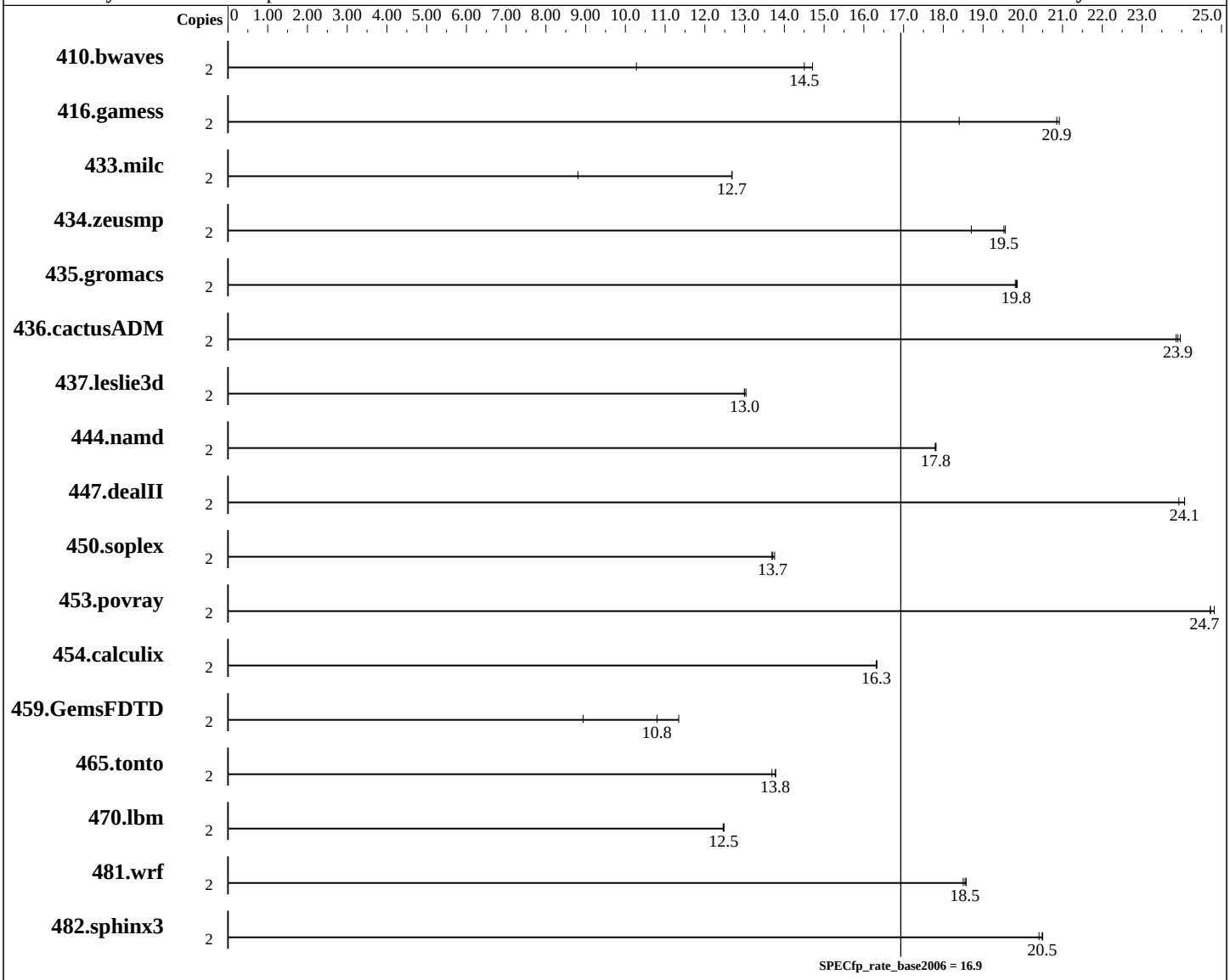
Lenovo ThinkPad T61(Intel Core 2 Duo T7100)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 16.9

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jun-2007
Hardware Availability: Jun-2007
Software Availability: Feb-2007



Hardware

CPU Name:	Intel Core 2 Duo T7100
CPU Characteristics:	1.80 GHz 2MB L2, 800 MHz Bus
CPU MHz:	1800
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	Windows Vista32 Business
Compiler:	Intel C++ Compiler for IA32 version 9.1 Build no 20060816 Intel Fortran Compiler for IA32 version 9.1 Build no 20060816 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda* PC-5300 DDR2-667 5-5-5-12)
Disk Subsystem: Hitachi* HTS721010G9 SATA 100GB 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: Not Applicable
Other Software: SmartHeap library V7.4 from Microquill

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1848	14.7	2644	10.3	**1874**	**14.5**							
416.gamess	2	1872	20.9	**1878**	**20.9**	2128	18.4							
433.milc	2	1448	12.7	**1448**	**12.7**	2085	8.81							
434.zeusmp	2	930	19.6	**932**	**19.5**	973	18.7							
435.gromacs	2	**720**	**19.8**	721	19.8	719	19.9							
436.cactusADM	2	**1000**	**23.9**	997	24.0	1002	23.9							
437.leslie3d	2	**1446**	**13.0**	1447	13.0	1442	13.0							
444.namd	2	901	17.8	**901**	**17.8**	900	17.8							
447.dealII	2	**951**	**24.1**	956	23.9	950	24.1							
450.soplex	2	1213	13.8	**1217**	**13.7**	1219	13.7							
453.povray	2	430	24.7	**430**	**24.7**	429	24.8							
454.calculix	2	1010	16.3	1011	16.3	**1011**	**16.3**							
459.GemsFDTD	2	1871	11.3	**1966**	**10.8**	2375	8.93							
465.tonto	2	**1429**	**13.8**	1438	13.7	1428	13.8							
470.lbm	2	**2202**	**12.5**	2202	12.5	2205	12.5							
481.wrf	2	**1205**	**18.5**	1208	18.5	1202	18.6							
482.sphinx3	2	1910	20.4	**1903**	**20.5**	1901	20.5							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries built on Windows XP Professional w/ SP2
Vista Power Profile set to High Performance
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000 -link /FORCE:MULTIPLE

Benchmarks using both Fortran and C:
-fast /F950000000 -link /FORCE:MULTIPLE

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic91-ia32-flags.20090714.00.xml

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