



# SPEC® CINT2006 Result

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IBM Corporation

SPECint®2006 = 12.5

IBM System x3550 (Intel Xeon 5120)

SPECint\_base2006 = 11.3

CPU2006 license: 11

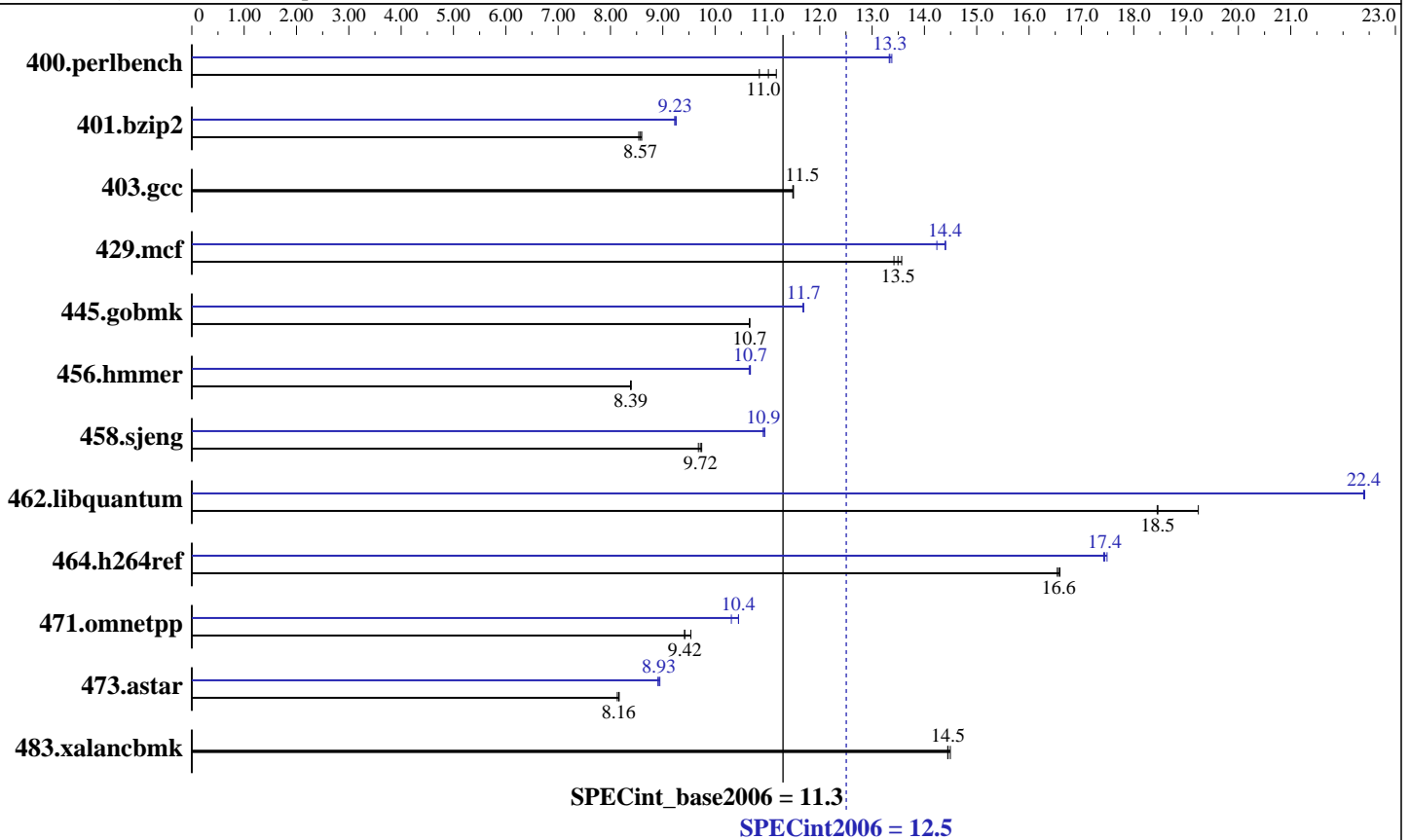
Test date: Aug-2007

Test sponsor: IBM Corporation

Hardware Availability: Jul-2006

Tested by: IBM Corporation

Software Availability: Jul-2007



## Hardware

CPU Name: Intel Xeon 5120  
 CPU Characteristics: 1066MHz system bus  
 CPU MHz: 1866  
 FPU: Integrated  
 CPU(s) enabled: 4 cores, 2 chips, 2 cores/chip  
 CPU(s) orderable: 1,2 chips  
 Primary Cache: 32 KB I + 32 KB D on chip per core  
 Secondary Cache: 4 MB I+D on chip per chip  
 L3 Cache: None  
 Other Cache: None  
 Memory: 16 GB (8 x 2GB DDR2-5300F ECC)  
 Disk Subsystem: 1 x 36 GB SAS, 15000 RPM  
 Other Hardware: None

## Software

Operating System: SLES 10 (x86\_64), 2.6.16.21-0.8-smp  
 Compiler: Intel C++ Compiler for Linux version 10.0  
 Build 20070426 Package ID: 1\_cc\_p\_10.0.023  
 Auto Parallel: No  
 File System: ReiserFS  
 System State: Multi-user, run level 3  
 Base Pointers: 32-bit  
 Peak Pointers: 32/64-bit  
 Other Software: MicroQuill SmartHeap 8.1



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## Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	875	11.2	<b>887</b>	<b>11.0</b>	901	10.8	730	13.4	733	13.3	<b>732</b>	<b>13.3</b>
401.bzip2	1130	8.54	<b>1125</b>	<b>8.57</b>	1122	8.60	1042	9.26	<b>1045</b>	<b>9.23</b>	1046	9.23
403.gcc	<b>701</b>	<b>11.5</b>	701	11.5	700	11.5	<b>701</b>	<b>11.5</b>	701	11.5	700	11.5
429.mcf	680	13.4	<b>676</b>	<b>13.5</b>	672	13.6	641	14.2	<b>634</b>	<b>14.4</b>	633	14.4
445.gobmk	<b>984</b>	<b>10.7</b>	984	10.7	985	10.7	898	11.7	<b>897</b>	<b>11.7</b>	897	11.7
456.hammer	1113	8.38	1111	8.40	<b>1112</b>	<b>8.39</b>	876	10.7	<b>874</b>	<b>10.7</b>	874	10.7
458.sjeng	1250	9.68	1242	9.74	<b>1245</b>	<b>9.72</b>	<b>1106</b>	<b>10.9</b>	1105	10.9	1108	10.9
462.libquantum	1123	18.4	<b>1122</b>	<b>18.5</b>	1077	19.2	924	22.4	925	22.4	<b>925</b>	<b>22.4</b>
464.h264ref	1338	16.5	1334	16.6	<b>1336</b>	<b>16.6</b>	1270	17.4	1265	17.5	<b>1268</b>	<b>17.4</b>
471.omnetpp	655	9.54	<b>664</b>	<b>9.42</b>	664	9.42	<b>598</b>	<b>10.4</b>	606	10.3	598	10.4
473.astar	<b>860</b>	<b>8.16</b>	860	8.17	863	8.13	788	8.91	785	8.94	<b>786</b>	<b>8.93</b>
483.xalancbmk	478	14.4	476	14.5	<b>477</b>	<b>14.5</b>	478	14.4	476	14.5	<b>477</b>	<b>14.5</b>

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

## Base Compiler Invocation

C benchmarks:  
icc  
  
C++ benchmarks:  
icpc

## Base Portability Flags

400.perlbench: -DSPEC\_CPU\_LINUX\_IA32  
462.libquantum: -DSPEC\_CPU\_LINUX  
483.xalancbmk: -DSPEC\_CPU\_LINUX

## Base Optimization Flags

C benchmarks:  
-fast  
  
C++ benchmarks:  
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap



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## Base Other Flags

C benchmarks:

```
403.gcc: -Dalloca=_alloca
```

## Peak Compiler Invocation

C benchmarks (except as noted below):

icc

```
401.bzip2: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include
```

```
456.hmmer: /opt/intel/cce/10.0.023/bin/icc
-L/opt/intel/cce/10.0.023/lib
-I/opt/intel/cce/10.0.023/include
```

C++ benchmarks:

icpc

## Peak Portability Flags

```
400.perlbenc: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX
```

## Peak Optimization Flags

C benchmarks:

```
400.perlbenc: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch
```

```
401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast
```

```
403.gcc: basepeak = yes
```

```
429.mcf: -fast -prefetch
```

```
445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec_div -ansi-alias
```

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## Peak Optimization Flags (Continued)

456.hmmcr: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2  
-ansi-alias

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -Ob0  
-prefetch -opt-streaming-stores always

464.h264ref: Same as 456.hmmcr

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo  
-no-prec\_div -ansi-alias -Wl,-z,muldefs  
-L/spec/cpu2006.1.0/lib -lsmartheap

473.astar: Same as 471.omnetpp

483.xalancbmk: basepeak = yes

## Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=\_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.45.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.45.xml>

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For other inquiries, please contact [webmaster@spec.org](mailto:webmaster@spec.org).

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