SPEC® CFP2006 Result

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Dell Inc.

(Test Sponsor: Intel Corporation)

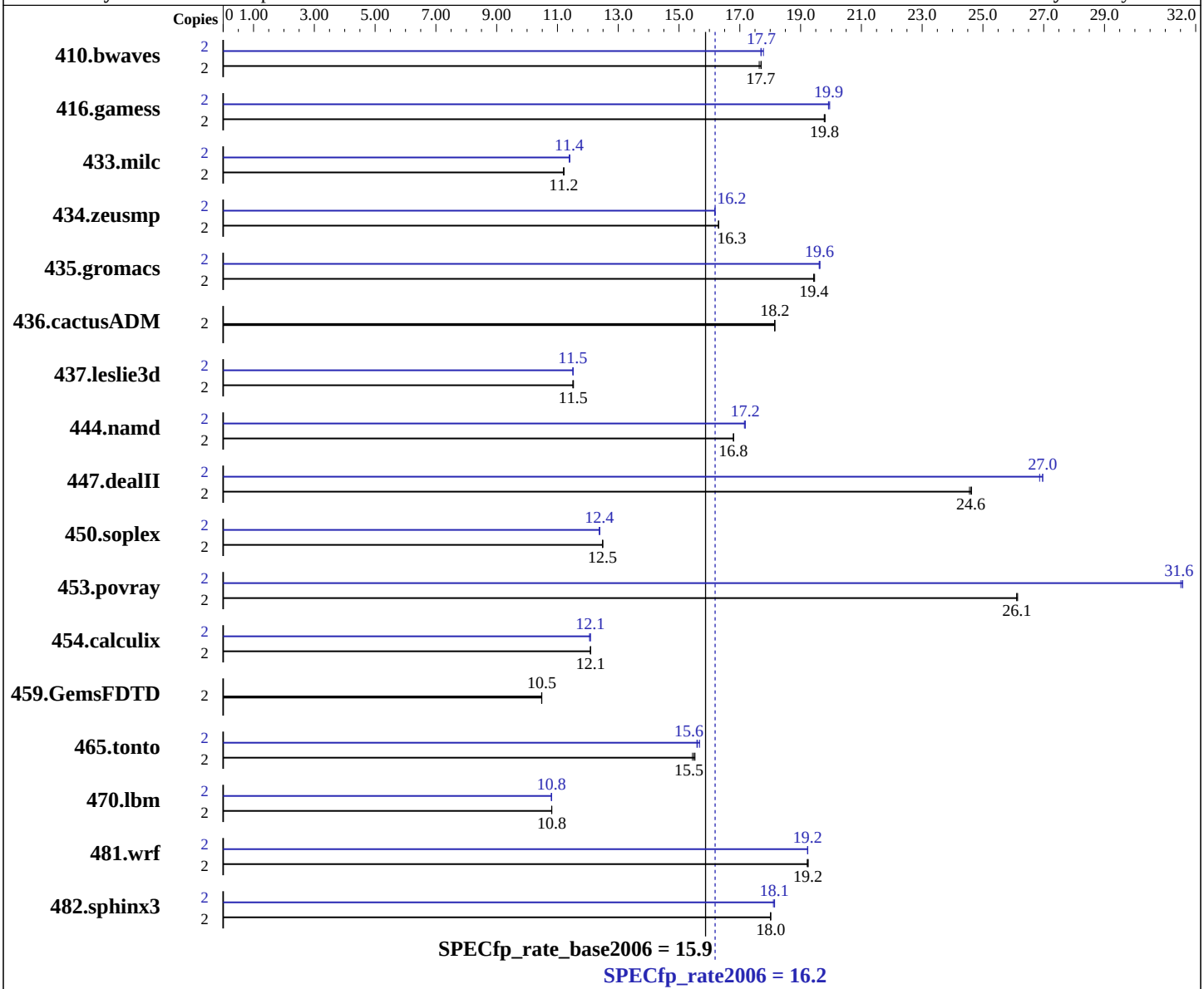
XPS M1710 (Intel Core Duo T2600)

SPECfp®_rate2006 = 16.2

SPECfp_rate_base2006 = 15.9

CPU2006 license: 13
Test sponsor: Intel Corporation
Tested by: Intel Corporation

Test date: Jul-2007
Hardware Availability: Jul-2007
Software Availability: May-2007



Hardware

CPU Name:	Intel Core Duo T2600
CPU Characteristics:	
CPU MHz:	2167
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	2 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ and Fortran Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default
Base Pointers:	32-bit

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L3 Cache: None
Other Cache: None
Memory: 2 GB (2x1GB Qimonda DDR2-667 CL5)
Disk Subsystem: 100GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1541	17.6	1535	17.7	**1536**	**17.7**	2	**1535**	**17.7**	1529	17.8	1536	17.7
416.gamess	2	**1979**	**19.8**	1980	19.8	1977	19.8	2	1962	20.0	1966	19.9	**1965**	**19.9**
433.milc	2	1639	11.2	1637	11.2	**1638**	**11.2**	2	1612	11.4	1609	11.4	**1612**	**11.4**
434.zeusmp	2	1116	16.3	**1116**	**16.3**	1117	16.3	2	1125	16.2	1124	16.2	**1125**	**16.2**
435.gromacs	2	734	19.5	735	19.4	**734**	**19.4**	2	728	19.6	**728**	**19.6**	727	19.6
436.cactusADM	2	**1317**	**18.2**	1317	18.1	1316	18.2	2	**1317**	**18.2**	1317	18.1	1316	18.2
437.leslie3d	2	**1633**	**11.5**	1631	11.5	1634	11.5	2	**1633**	**11.5**	1634	11.5	1633	11.5
444.namd	2	955	16.8	955	16.8	**955**	**16.8**	2	**934**	**17.2**	934	17.2	935	17.2
447.dealII	2	**930**	**24.6**	929	24.6	932	24.6	2	**849**	**27.0**	852	26.9	848	27.0
450.soplex	2	1337	12.5	1335	12.5	**1336**	**12.5**	2	1348	12.4	1346	12.4	**1347**	**12.4**
453.povray	2	407	26.1	408	26.1	**407**	**26.1**	2	**337**	**31.6**	338	31.5	337	31.6
454.calculix	2	**1365**	**12.1**	1365	12.1	1365	12.1	2	1369	12.1	1365	12.1	**1366**	**12.1**
459.GemsFDTD	2	2025	10.5	2024	10.5	**2024**	**10.5**	2	2025	10.5	2024	10.5	**2024**	**10.5**
465.tonto	2	**1270**	**15.5**	1274	15.4	1268	15.5	2	1255	15.7	1263	15.6	**1261**	**15.6**
470.lbm	2	2541	10.8	**2542**	**10.8**	2543	10.8	2	**2543**	**10.8**	2542	10.8	2545	10.8
481.wrf	2	1160	19.3	1162	19.2	**1161**	**19.2**	2	**1161**	**19.2**	1162	19.2	1161	19.2
482.sphinx3	2	2163	18.0	**2164**	**18.0**	2164	18.0	2	**2149**	**18.1**	2153	18.1	2148	18.1

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
nVidia GeForce Go 7900 discrete graphics card
Binaries were built on Windows XP Professional SP2
The start command with the /affinity switch was used to bind processes to cores
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

C++ benchmarks:

```
icl -Qvc7.1
```

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Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-QxP -Qipo -O3 -Qprec-div- -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-QxP -Qipo -O3 -Qprec-div- /F950000000

Benchmarks using both Fortran and C:
-QxP -Qipo -O3 -Qprec-div- /F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Oa /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 -Qscalar-rep- -Qprefetch /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qunroll2 /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Oa -Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qprefetch -Qcxx_features /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxP -Qipo -O3
-Qprec-div- -Qansi-alias -Qcxx_features /F950000000
shlw32m.lib -link /FORCE:MULTIPLE

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Peak Optimization Flags (Continued)

Fortran benchmarks:

410.bwaves: `-QxP -Qipo -O3 -Qprec-div- /F950000000`

416.gamess: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -Qipo -O3 -Qprec-div- -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F950000000`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -O2 -Qprec_div- -Qunroll0 -Qscalar-rep- /F950000000`

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -Qipo -O3 -Qprec-div- /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxP -Qipo -O3 -Qprec-div- -Oa /F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-QxP -Qipo -O3 -Qprec-div- /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.47.xml`

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Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 12:35:47 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 18 September 2007.