



SPEC® CINT2006 Result

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Dell Inc.

SPECint®_rate2006 = 112

PowerEdge 1950 (Intel Xeon X5365, 3.00 GHz)

SPECint_rate_base2006 = 99.1

CPU2006 license: 55

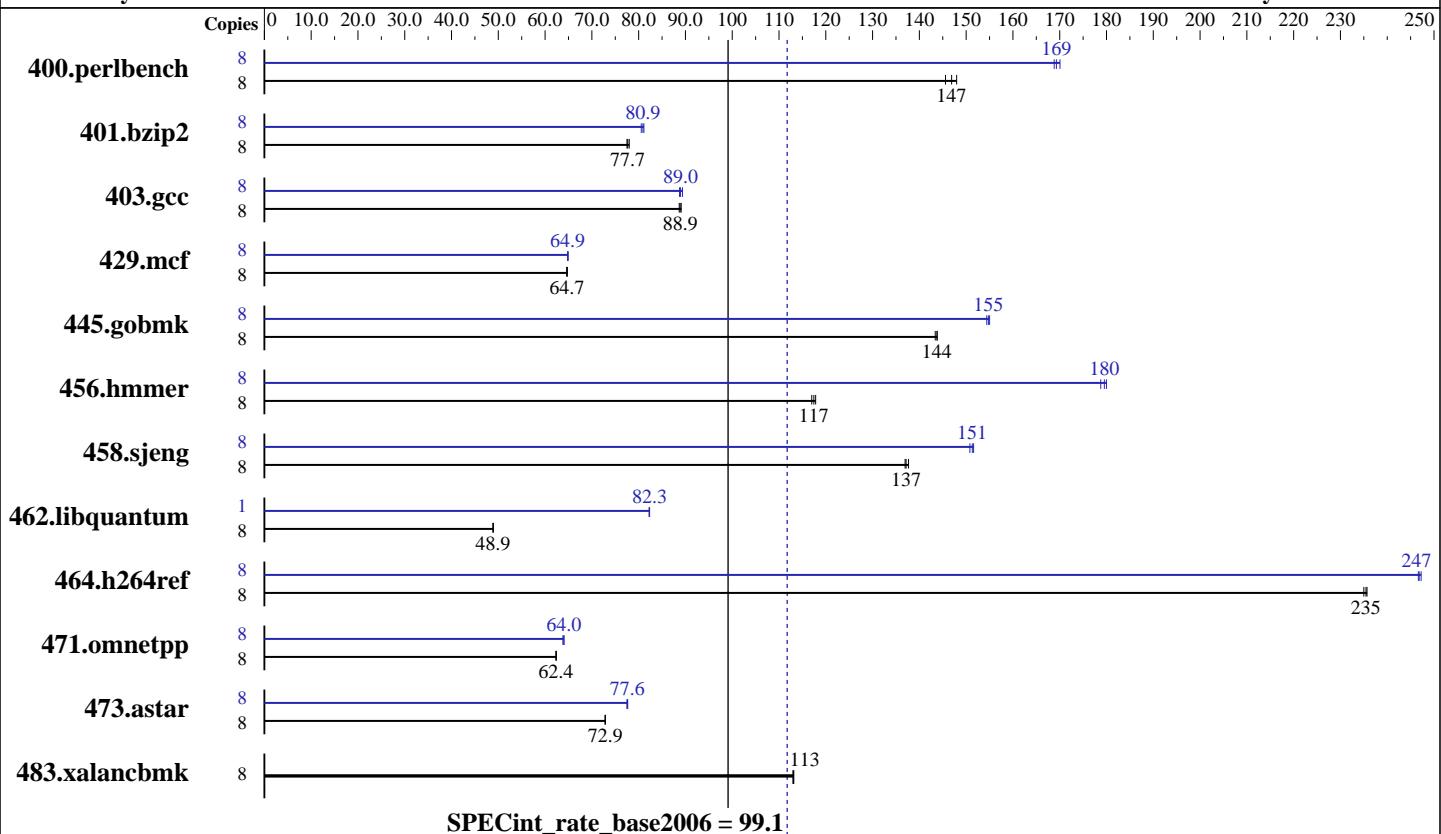
Test sponsor: Dell Inc.

Tested by: Dell Inc.

Test date: Aug-2007

Hardware Availability: Sep-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon X5365
CPU Characteristics: 1333MHz system bus
CPU MHz: 3000
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 8 MB I+D on chip per chip, 4 MB shared / 2 cores
L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB 667 MHz ECC CL5 FB-DIMM)
Disk Subsystem: 1 x 73 GB SAS 15k RPM
Other Hardware: None

Software

Operating System: 64-Bit SUSE LINUX Enterprise Server 10
Compiler: Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070725
Auto Parallel: Yes
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 32-bit
Peak Pointers: 32/64-bit
Other Software: SmartHeap library V8.1



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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	532	147	537	146	528	148	8	462	169	460	170	463	169
401.bzip2	8	990	78.0	996	77.5	994	77.7	8	958	80.6	954	80.9	952	81.1
403.gcc	8	726	88.7	723	89.1	725	88.9	8	721	89.4	724	89.0	726	88.7
429.mcf	8	1127	64.8	1129	64.6	1128	64.7	8	1125	64.8	1124	64.9	1125	64.9
445.gobmk	8	584	144	585	143	584	144	8	541	155	544	154	542	155
456.hammer	8	635	117	638	117	633	118	8	415	180	418	179	416	180
458.sjeng	8	706	137	703	138	707	137	8	640	151	642	151	639	152
462.libquantum	8	3389	48.9	3390	48.9	3390	48.9	1	252	82.3	252	82.3	252	82.2
464.h264ref	8	753	235	752	235	751	236	8	717	247	716	247	718	247
471.omnetpp	8	802	62.4	802	62.4	802	62.3	8	784	63.8	781	64.0	781	64.0
473.astar	8	771	72.9	771	72.8	771	72.9	8	725	77.5	723	77.7	724	77.6
483.xalancbmk	8	488	113	488	113	489	113	8	488	113	488	113	489	113

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Compiler Invocation Notes

OMP_NUM_THREADS set to number of cores

KMP_STACK_SIZE set to 64M

KMP_AFFINITY set to physical,0

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs

Platform Notes

BIOS Settings:

Adjacent Cache Line Prefetch = Disabled (default Enabled)

Hardware Prefetcher = Disabled (default Enabled)

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc



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Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:

-fast -inline-calloc -opt-malloc-options=3

C++ benchmarks:

-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/cmpllr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):

icc

401.bzip2: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

456.hmmr: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/bin/icc
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/lib
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux64/include

C++ benchmarks:

icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmr: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll12 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll14 -O0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs
-L/home/cmplr/usr3/alrahate/cpu2006.1.0/lib -lsmartheap

483.xalancbmk: basepeak = yes



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Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090713.02.xml>

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Tested with SPEC CPU2006 v1.0.

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