SPEC® CFP2006 Result

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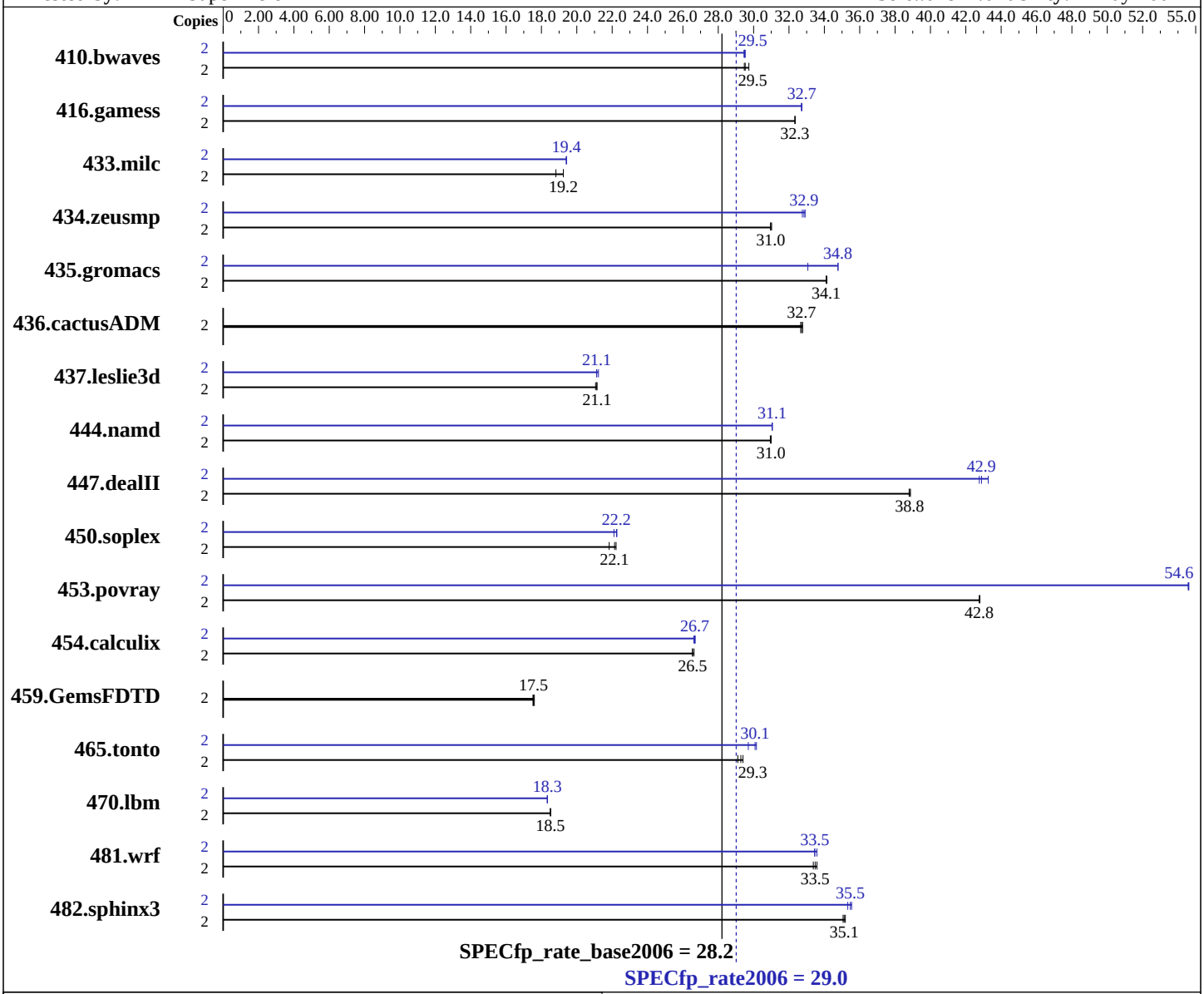
Supermicro
Motherboard C2SBA+

SPECfp®_rate2006 = 29.0

SPECfp_rate_base2006 = 28.2

CPU2006 license: 001176
Test sponsor: Supermicro
Tested by: Supermicro

Test date: Sep-2007
Hardware Availability: Jul-2007
Software Availability: May-2007



Hardware

CPU Name:	Intel Core 2 Duo E6850
CPU Characteristics:	3.00GHz, 1333MHz Bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	4 MB I+D on chip per chip

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Software

Operating System:	Windows Vista Ultimate (32-bit)
Compiler:	Intel C++ Compiler for IA32 version 10.0 Build 20070426 Package ID: W_CC_P_10.0.025 Intel Fortran Compiler for IA32 version 10.0 Build 20070426 Package ID: W_FC_P_10.0.025 Microsoft Visual Studio .Net 2003 (for libraries)
Auto Parallel:	No
File System:	NTFS
System State:	Default

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4 X 2GB PC2-6400, CL5)
Disk Subsystem: 74GB SATA, 7200RPM
Other Hardware: None

Base Pointers: 32-bit
Peak Pointers: 32-bit
Other Software: SmartHeap Library Version 8.0 from http://www.microquill.com/

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	914	29.7	922	29.5	**921**	**29.5**	2	**922**	**29.5**	921	29.5	923	29.5
416.gamess	2	1211	32.3	**1211**	**32.3**	1211	32.3	2	**1197**	**32.7**	1197	32.7	1197	32.7
433.milc	2	976	18.8	954	19.2	**954**	**19.2**	2	946	19.4	**946**	**19.4**	946	19.4
434.zeusmp	2	**588**	**31.0**	588	31.0	587	31.0	2	553	32.9	556	32.8	**554**	**32.9**
435.gromacs	2	419	34.1	**419**	**34.1**	418	34.1	2	411	34.8	432	33.1	**411**	**34.8**
436.cactusADM	2	729	32.8	**731**	**32.7**	732	32.7	2	729	32.8	**731**	**32.7**	732	32.7
437.leslie3d	2	**889**	**21.1**	889	21.1	893	21.1	2	886	21.2	890	21.1	**890**	**21.1**
444.namd	2	518	30.9	518	31.0	**518**	**31.0**	2	516	31.1	517	31.0	**517**	**31.1**
447.dealII	2	589	38.9	**589**	**38.8**	590	38.8	2	529	43.3	535	42.8	**533**	**42.9**
450.soplex	2	751	22.2	764	21.8	**753**	**22.1**	2	755	22.1	749	22.3	**750**	**22.2**
453.povray	2	**249**	**42.8**	249	42.8	249	42.8	2	195	54.6	195	54.6	**195**	**54.6**
454.calculix	2	620	26.6	**622**	**26.5**	622	26.5	2	**619**	**26.7**	618	26.7	620	26.6
459.GemsFDTD	2	1211	17.5	1206	17.6	**1209**	**17.5**	2	1211	17.5	1206	17.6	**1209**	**17.5**
465.tonto	2	676	29.1	669	29.4	**672**	**29.3**	2	663	29.7	**654**	**30.1**	653	30.2
470.lbm	2	1484	18.5	**1485**	**18.5**	1485	18.5	2	1499	18.3	1498	18.3	**1499**	**18.3**
481.wrf	2	669	33.4	**667**	**33.5**	665	33.6	2	668	33.5	665	33.6	**668**	**33.5**
482.sphinx3	2	1112	35.1	**1110**	**35.1**	1108	35.2	2	1097	35.5	1103	35.3	**1099**	**35.5**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Tested systems can be used with case CSE-733i-450
To ensure system stability, a 420W (minimum) ATX power supply [4-pin +12V AND (20 or 24-pin)]
is required.
Product description located at:
http://www.supermicro.com/products/motherboard/Core2Duo/G33/C2SBA+II.cfm
The system bus runs at 1333 MHz
BIOS Setting : Default
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc7.1 -Qc99
```

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Base Compiler Invocation (Continued)

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Base Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG -DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Base Optimization Flags

C benchmarks:
-fast /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:
-fast -Qcxx_features /F950000000 shlw32m.lib -link /FORCE:MULTIPLE

Fortran benchmarks:
-fast /F950000000

Benchmarks using both Fortran and C:
-fast /F950000000

Peak Compiler Invocation

C benchmarks:
icl -Qvc7.1 -Qc99

C++ benchmarks:
icl -Qvc7.1

Fortran benchmarks:
ifort

Continued on next page

Peak Compiler Invocation (Continued)

Benchmarks using both Fortran and C:
icl -Qvc7.1 -Qc99 ifort

Peak Portability Flags

436.cactusADM: -Qlowercase /assume:underscore
444.namd: -TP
447.dealII: -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
-DBOOST_NO_INTRINSIC_WCHAR_T
453.povray: -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_NOZMODIFIER -Qlowercase
481.wrf: -DSPEC_CPU_WINDOWS_ICL

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Oa
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

470.lbm: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
-Qscalar-rep- -Qprefetch /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

482.sphinx3: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

C++ benchmarks:

444.namd: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Oa
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qprefetch
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

450.soplex: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx_features
/F950000000 shlw32m.lib -link /FORCE:MULTIPLE

453.povray: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias
-Qcxx_features /F950000000 shlw32m.lib
-link /FORCE:MULTIPLE

Fortran benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

410.bwaves: `-fast /F950000000`

416.gamess: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qunroll2 -Ob0 -Qansi-alias -Qscalar-rep- /F950000000`

434.zeusmp: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-QxT -O2 -Qprec_div- -Qunroll0 -Qscalar-rep- /F950000000`

437.leslie3d: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F950000000`

459.GemsFDTD: `basepeak = yes`

465.tonto: `Same as 437.leslie3d`

Benchmarks using both Fortran and C:

435.gromacs: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Oa /F950000000`

436.cactusADM: `basepeak = yes`

454.calculix: `-fast /F950000000`

481.wrf: `Same as 454.calculix`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/Intel-ic10-ia32-intel64-linux-flags.20090714.18.xml`

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