SPEC® CFP2006 Result
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IBM Corporation

$SPECfp^{®}\_rate2006 =$ Not Run

IBM System x3550 (Intel Xeon X5460)
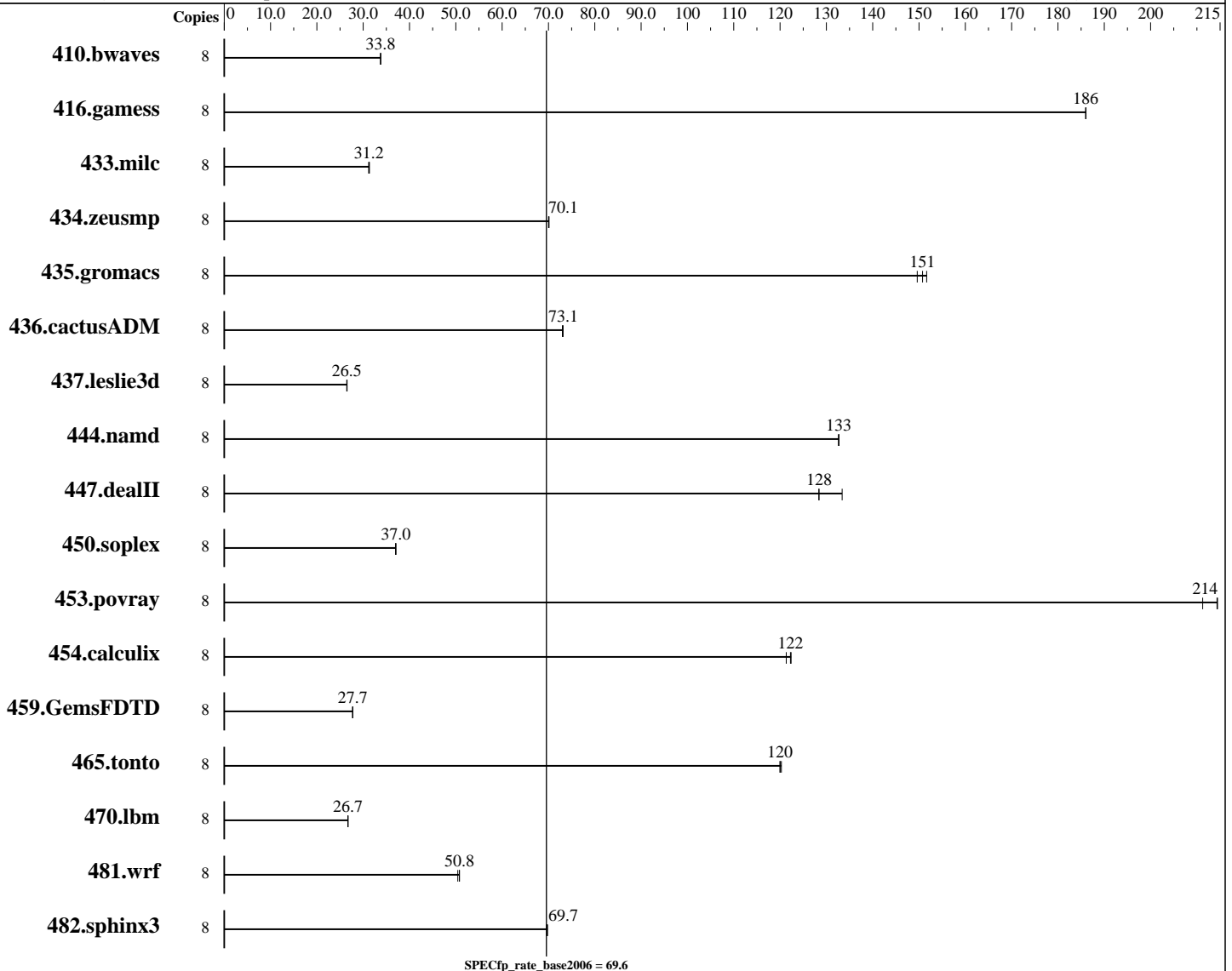
$SPECfp\_rate\_base2006 =$ 69.6

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Nov-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007

Benchmark	Copies	Result
410.bwaves	8	33.8
416.gamess	8	186
433.milc	8	31.2
434.zeusmp	8	70.1
435.gromacs	8	151
436.cactusADM	8	73.1
437.leslie3d	8	26.5
444.namd	8	133
447.dealII	8	128
450.soplex	8	37.0
453.povray	8	214
454.calculix	8	122
459.GemsFDTD	8	27.7
465.tonto	8	120
470.lbm	8	26.7
481.wrf	8	50.8
482.sphinx3	8	69.7

SPECfp_rate_base2006 = 69.6

Hardware
CPU Name: Intel Xeon X5460
CPU Characteristics: 1333MHz system bus
CPU MHz: 3158
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software
Operating System: SLES10 (x86_64), 2.6.16.21-0.8-smp
Compiler: Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel: No
File System: ReiserFS
System State: Multi-user, run level 3
Base Pointers: 64-bit
Peak Pointers: Not Applicable

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L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem:	1 x 36 GB SAS, 15000 RPM
Other Hardware:	None

Other Software: None
Binutils 2.17.50.0.15

Results Table

Benchmark		Base							Peak					
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3220**	**33.8**	3220	33.8	3221	33.8							
416.gamess	8	842	186	842	186	**842**	**186**							
433.milc	8	2354	31.2	2343	31.3	**2350**	**31.2**							
434.zeusmp	8	**1039**	**70.1**	1038	70.1	1040	70.0							
435.gromacs	8	**379**	**151**	377	152	382	150							
436.cactusADM	8	**1308**	**73.1**	1309	73.0	1307	73.1							
437.leslie3d	8	2841	26.5	**2841**	**26.5**	2835	26.5							
444.namd	8	483	133	**483**	**133**	484	133							
447.dealII	8	**713**	**128**	713	128	686	133							
450.soplex	8	1801	37.0	**1801**	**37.0**	1803	37.0							
453.povray	8	**199**	**214**	201	211	198	214							
454.calculix	8	544	121	539	122	**540**	**122**							
459.GemsFDTD	8	3059	27.7	3062	27.7	**3060**	**27.7**							
465.tonto	8	654	120	**656**	**120**	656	120							
470.lbm	8	4118	26.7	**4116**	**26.7**	4113	26.7							
481.wrf	8	1759	50.8	**1760**	**50.8**	1772	50.4							
482.sphinx3	8	2238	69.7	2234	69.8	**2236**	**69.7**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes
```

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Base Compiler Invocation

C benchmarks:
    icc

C++ benchmarks:
    icpc

Fortran benchmarks:
    ifort

Benchmarks using both Fortran and C:
    icc ifort

Base Portability Flags

```
    410.bwaves: -DSPEC_CPU_LP64
    416.gamess: -DSPEC_CPU_LP64
      433.milc: -DSPEC_CPU_LP64
    434.zeusmp: -DSPEC_CPU_LP64
   435.gromacs: -DSPEC_CPU_LP64 -nofor_main
  436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
   437.leslie3d: -DSPEC_CPU_LP64
      444.namd: -DSPEC_CPU_LP64
     447.dealII: -DSPEC_CPU_LP64
    450.soplex: -DSPEC_CPU_LP64
    453.povray: -DSPEC_CPU_LP64
   454.calculix: -DSPEC_CPU_LP64 -nofor_main
  459.GemsFDTD: -DSPEC_CPU_LP64
     465.tonto: -DSPEC_CPU_LP64
      470.lbm: -DSPEC_CPU_LP64
      481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
    482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
    -fast

C++ benchmarks:
    -fast

Fortran benchmarks:
    -fast

Benchmarks using both Fortran and C:
    -fast

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The flags file that was used to format this result can be browsed at

http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.26.html

You can also download the XML flags source by saving the following link:

http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.26.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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