SPEC® CFP2006 Result

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IBM Corporation

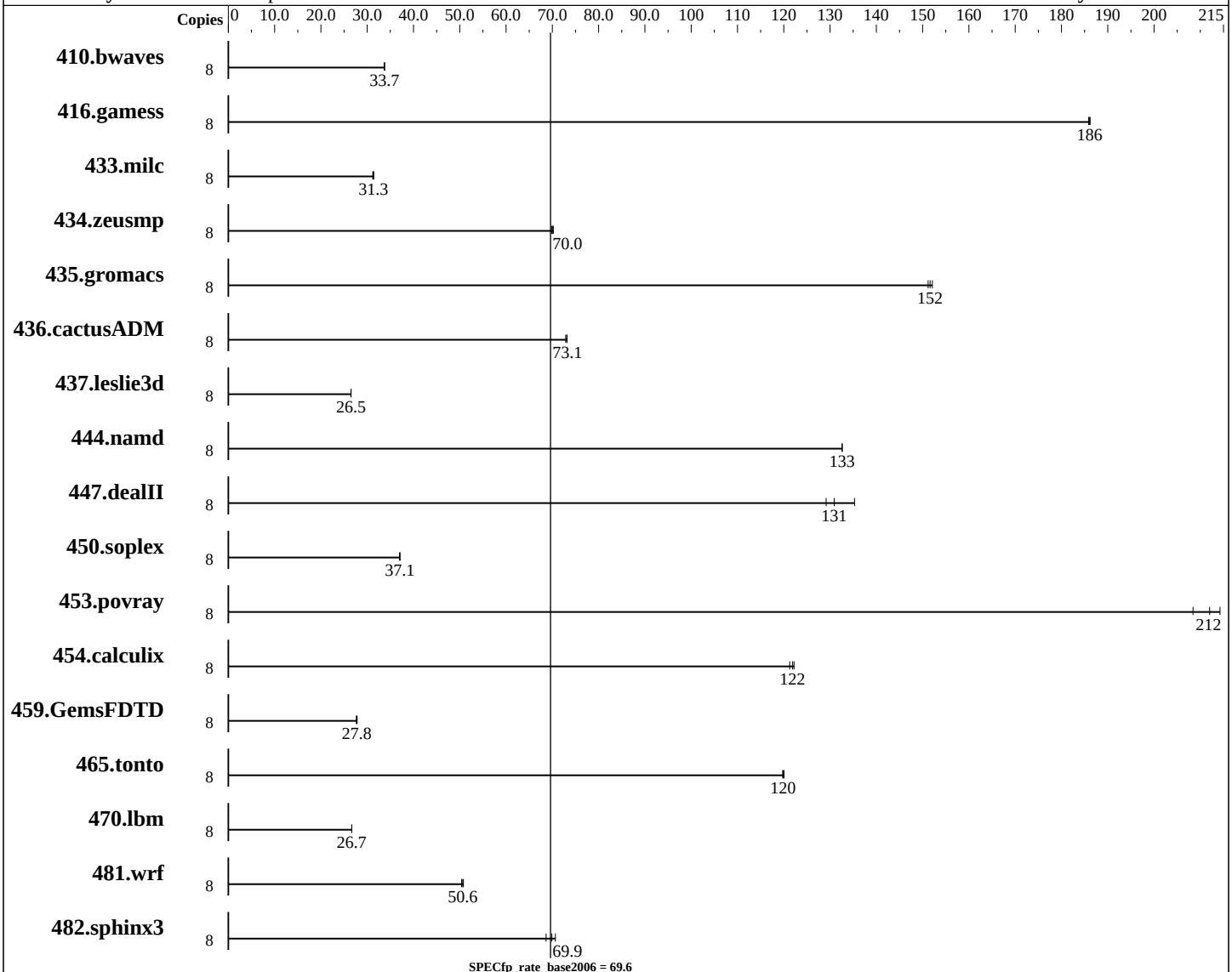
IBM System x3650 (Intel Xeon X5460)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 69.6

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Nov-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5460
CPU Characteristics:	1333MHz system bus
CPU MHz:	3158
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SLES10 (x86_64), 2.6.16.21-0.8-smp
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit
Peak Pointers:	Not Applicable

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
Other Hardware: None

Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3223**	**33.7**	3223	33.7	3223	33.7							
416.gamess	8	**842**	**186**	843	186	841	186							
433.milc	8	**2343**	**31.3**	2354	31.2	2338	31.4							
434.zeusmp	8	1037	70.2	**1040**	**70.0**	1042	69.9							
435.gromacs	8	376	152	**377**	**152**	378	151							
436.cactusADM	8	1312	72.9	**1308**	**73.1**	1306	73.2							
437.leslie3d	8	2840	26.5	2836	26.5	**2838**	**26.5**							
444.namd	8	484	133	484	133	**484**	**133**							
447.dealII	8	676	135	**699**	**131**	709	129							
450.soplex	8	1803	37.0	1800	37.1	**1800**	**37.1**							
453.povray	8	199	214	204	208	**201**	**212**							
454.calculix	8	**541**	**122**	540	122	544	121							
459.GemsFDTD	8	**3058**	**27.8**	3050	27.8	3071	27.6							
465.tonto	8	656	120	657	120	**657**	**120**							
470.lbm	8	4121	26.7	**4121**	**26.7**	4119	26.7							
481.wrf	8	**1765**	**50.6**	1760	50.8	1773	50.4							
482.sphinx3	8	2271	68.7	**2231**	**69.9**	2207	70.7							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes
```

Base Compiler Invocation

C benchmarks:
icc

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Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml

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For other inquiries, please contact webmaster@spec.org.

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