SPEC® CFP2006 Result

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Hewlett-Packard Company

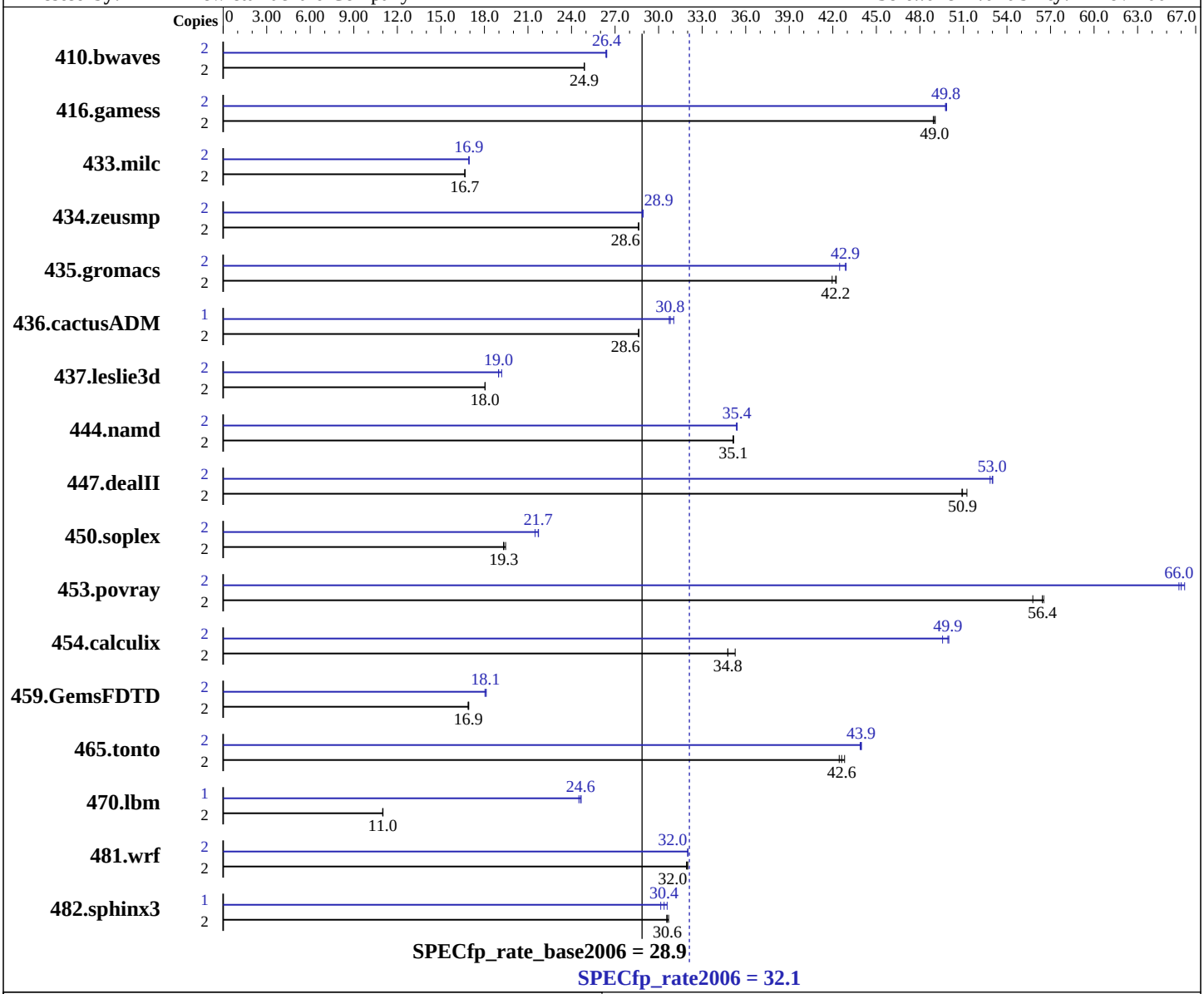
ProLiant BL480c
(3.33 GHz, Intel Xeon processor X5260)

SPECfp®_rate2006 = 32.1

SPECfp_rate_base2006 = 28.9

CPU2006 license: 3
Test sponsor: Hewlett-Packard Company
Tested by: Hewlett-Packard Company

Test date: Dec-2007
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5260
CPU Characteristics:	3.33 GHz, 6 MB L2 shared, 1333 MHz system bus
CPU MHz:	3333
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1 Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008 Intel Fortran Compiler for applications running on IA-32 and Intel 64, Version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

System State: Multi-user run level 3
Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: binutils-2.17.50

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1093	24.9	**1092**	**24.9**	1092	24.9	2	1029	26.4	**1029**	**26.4**	1031	26.4
416.gamess	2	800	48.9	798	49.1	**799**	**49.0**	2	786	49.8	787	49.8	**786**	**49.8**
433.milc	2	1102	16.7	1103	16.7	**1102**	**16.7**	2	1084	16.9	1084	16.9	**1084**	**16.9**
434.zeusmp	2	636	28.6	**636**	**28.6**	636	28.6	2	**630**	**28.9**	630	28.9	629	28.9
435.gromacs	2	340	42.0	**338**	**42.2**	338	42.2	2	336	42.5	**333**	**42.9**	333	42.9
436.cactusADM	2	835	28.6	834	28.6	**834**	**28.6**	1	**388**	**30.8**	389	30.7	385	31.0
437.leslie3d	2	1042	18.0	**1042**	**18.0**	1041	18.1	2	**990**	**19.0**	980	19.2	991	19.0
444.namd	2	457	35.1	456	35.2	**456**	**35.1**	2	**453**	**35.4**	454	35.4	453	35.4
447.dealII	2	450	50.9	**449**	**50.9**	447	51.2	2	**432**	**53.0**	433	52.8	432	53.0
450.soplex	2	857	19.5	864	19.3	**862**	**19.3**	2	**768**	**21.7**	776	21.5	768	21.7
453.povray	2	**189**	**56.4**	188	56.5	191	55.8	2	**161**	**66.0**	161	66.2	162	65.8
454.calculix	2	468	35.3	**475**	**34.8**	475	34.8	2	330	50.0	**330**	**49.9**	333	49.6
459.GemsFDTD	2	1254	16.9	**1255**	**16.9**	1258	16.9	2	**1173**	**18.1**	1176	18.0	1171	18.1
465.tonto	2	460	42.8	**462**	**42.6**	464	42.4	2	**448**	**43.9**	448	44.0	448	43.9
470.lbm	2	**2497**	**11.0**	2495	11.0	2501	11.0	1	557	24.7	**558**	**24.6**	561	24.5
481.wrf	2	698	32.0	**698**	**32.0**	700	31.9	2	698	32.0	698	32.0	**698**	**32.0**
482.sphinx3	2	1270	30.7	**1274**	**30.6**	1276	30.6	1	647	30.1	**642**	**30.4**	637	30.6

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
'/usr/bin/taskset' used to bind processes to CPUs
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 64M
```

Platform Notes

```
BIOS configuration:
  Power Regulator set to Static High Performance Mode
  Adjacent Sector Prefetch Disabled
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib -I/opt/intel/fc/10.1.008/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-fp-flags.html

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Software Availability: Nov-2007

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-fp-flags.xml`

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