SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

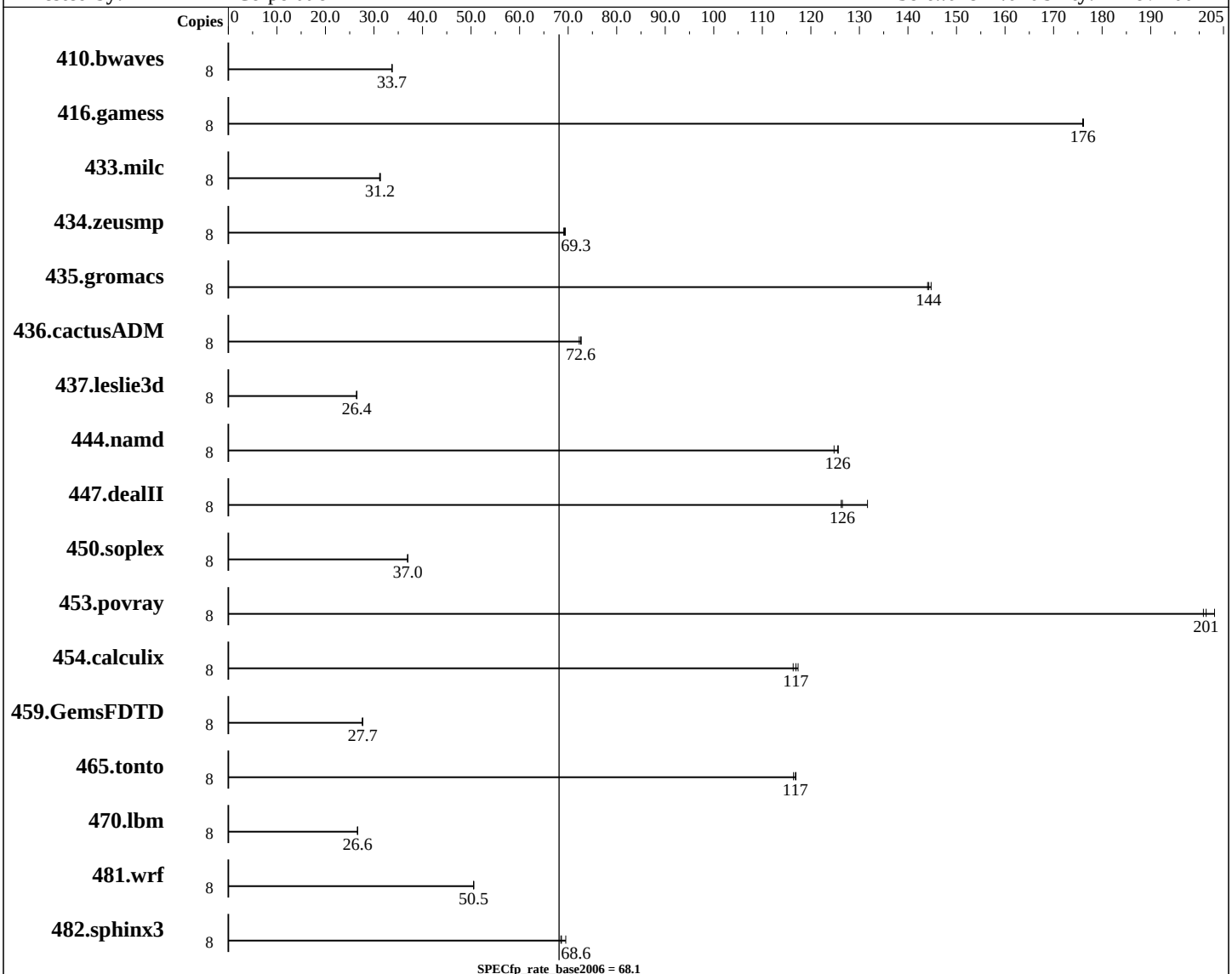
IBM System x3650 (Intel Xeon X5450)

SPECfp®_rate2006 = Not Run

SPECfp_rate_base2006 = 68.1

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon X5450
CPU Characteristics:	1333MHz system bus
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64), Kernel 2.6.16.21-0.8-smp
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	No
File System:	ReiserFS
System State:	Multi-user, run level 3
Base Pointers:	64-bit

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM System x3650 (Intel Xeon X5450)

SPECfp_rate2006 = Not Run

SPECfp_rate_base2006 = 68.1

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Jan-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007

L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 36 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: Not Applicable
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3223**	**33.7**	3225	33.7	3223	33.7							
416.gamess	8	890	176	**890**	**176**	889	176							
433.milc	8	**2351**	**31.2**	2344	31.3	2356	31.2							
434.zeusmp	8	1049	69.4	**1051**	**69.3**	1054	69.1							
435.gromacs	8	**396**	**144**	397	144	395	145							
436.cactusADM	8	**1317**	**72.6**	1315	72.7	1322	72.3							
437.leslie3d	8	2838	26.5	2849	26.4	**2844**	**26.4**							
444.namd	8	511	126	514	125	**511**	**126**							
447.dealII	8	695	132	725	126	**723**	**126**							
450.soplex	8	1807	36.9	1804	37.0	**1805**	**37.0**							
453.povray	8	209	203	212	201	**211**	**201**							
454.calculix	8	562	117	567	116	**564**	**117**							
459.GemsFDTD	8	3077	27.6	**3067**	**27.7**	3064	27.7							
465.tonto	8	**674**	**117**	673	117	676	116							
470.lbm	8	4130	26.6	**4129**	**26.6**	4129	26.6							
481.wrf	8	**1768**	**50.5**	1769	50.5	1767	50.6							
482.sphinx3	8	2243	69.5	2276	68.5	**2272**	**68.6**							

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 64-bit mode
Hardware Sector Prefetch Disabled and Adjacent Sector Prefetch Disabled
Powersaved dameon was disabled in OS
taskset utility used to bind CPU(s) to processes
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Continued on next page

Base Compiler Invocation (Continued)

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090714.11.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 16:27:00 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 6 February 2008.