



SPEC® CFP2006 Result

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Fujitsu Siemens Computers

SPECfp®_rate2006 = 48.0

PRIMERGY RX600 S4, Intel Xeon E7310, 1.60 GHz

SPECfp_rate_base2006 = 45.4

CPU2006 license: 22

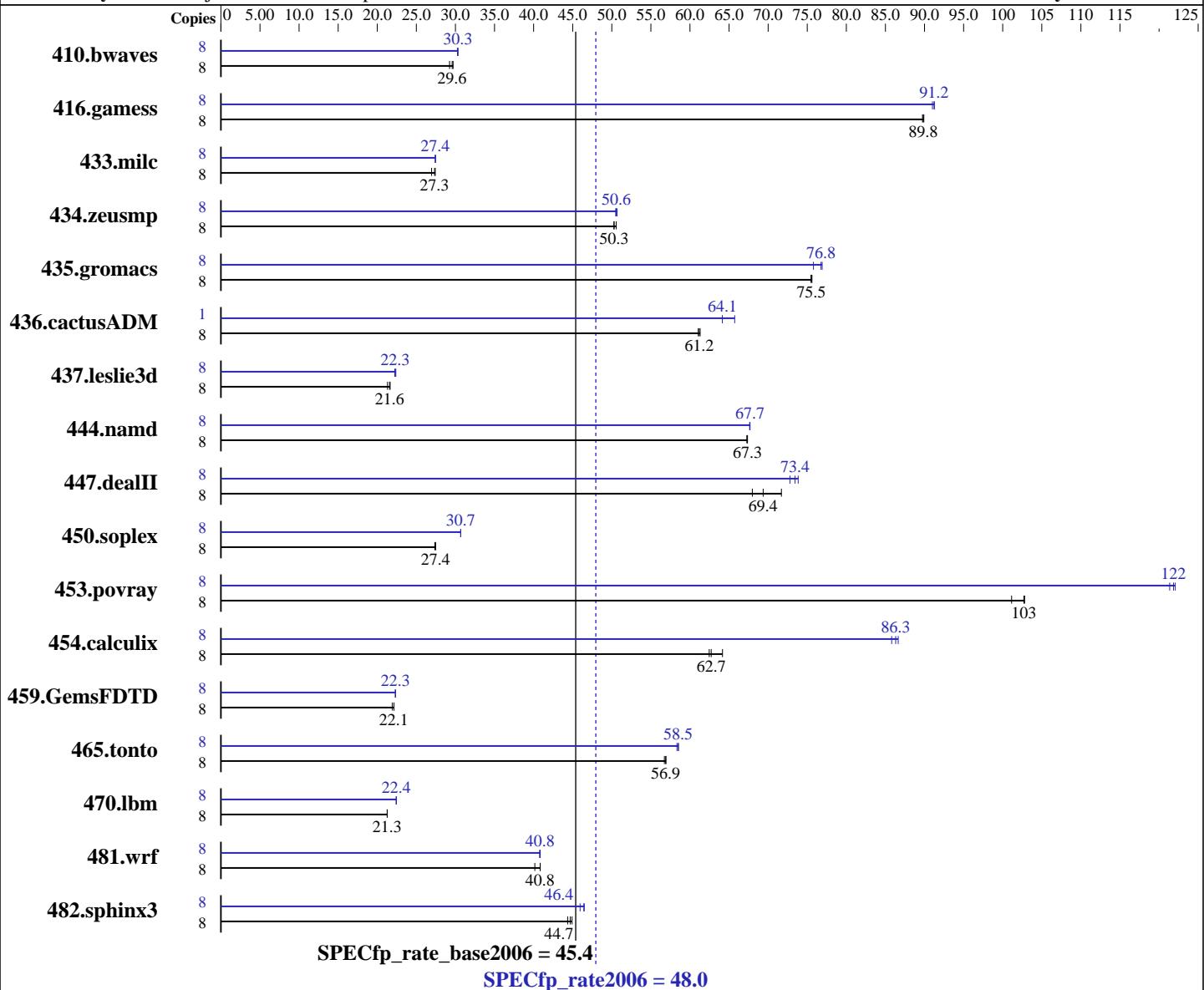
Test date: Feb-2008

Test sponsor: Fujitsu Siemens Computers

Hardware Availability: Dec-2007

Tested by: Fujitsu Siemens Computers

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E7310
CPU Characteristics: 1067 MHz system bus
CPU MHz: 1600
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2,4 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 4 MB I+D on chip per chip, 2 MB shared / 2 cores

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ and Fortran Compiler for Linux32 and Linux64 Version 10.1 - Build 20070725
Auto Parallel: Yes
File System: ext2
System State: Multiuser, Runlevel 3
Base Pointers: 64-bit

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L3 Cache:	None
Other Cache:	None
Memory:	64 GB (16x4 GB PC2-5300F, 2 rank, CAS 5-5-5, with ECC)
Disk Subsystem:	Seagate ST973451SS (SAS, 73GB, 15000rpm)
Other Hardware:	None

Peak Pointers: 32/64-bit
Other Software: None

Results Table

Benchmark	Base								Peak							
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	3717	29.3	3675	29.6	3661	29.7	8	3585	30.3	3592	30.3	3588	30.3		
416.gamess	8	1745	89.7	1744	89.8	1742	89.9	8	1717	91.2	1716	91.3	1721	91.0		
433.milc	8	2725	27.0	2687	27.3	2678	27.4	8	2677	27.4	2676	27.4	2678	27.4		
434.zeusmp	8	1440	50.6	1448	50.3	1448	50.3	8	1440	50.6	1442	50.5	1436	50.7		
435.gromacs	8	756	75.6	756	75.5	757	75.5	8	743	76.9	754	75.8	744	76.8		
436.cactusADM	8	1562	61.2	1566	61.1	1560	61.3	1	186	64.1	186	64.1	182	65.7		
437.leslie3d	8	3528	21.3	3474	21.6	3486	21.6	8	3371	22.3	3381	22.2	3361	22.4		
444.namd	8	954	67.3	953	67.3	953	67.4	8	949	67.6	948	67.7	948	67.7		
447.dealII	8	1346	68.0	1276	71.7	1319	69.4	8	1239	73.8	1246	73.4	1257	72.8		
450.soplex	8	2438	27.4	2431	27.4	2429	27.5	8	2175	30.7	2175	30.7	2175	30.7		
453.povray	8	414	103	421	101	414	103	8	351	121	349	122	349	122		
454.calculix	8	1029	64.2	1052	62.7	1057	62.5	8	769	85.8	762	86.6	765	86.3		
459.GemsFDTD	8	3873	21.9	3835	22.1	3834	22.1	8	3801	22.3	3802	22.3	3803	22.3		
465.tonto	8	1383	56.9	1384	56.9	1388	56.7	8	1345	58.5	1345	58.5	1349	58.3		
470.lbm	8	5168	21.3	5166	21.3	5166	21.3	8	4899	22.4	4899	22.4	4900	22.4		
481.wrf	8	2225	40.2	2188	40.8	2188	40.8	8	2192	40.8	2188	40.8	2188	40.8		
482.sphinx3	8	3515	44.4	3472	44.9	3489	44.7	8	3394	45.9	3359	46.4	3355	46.5		

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run 'OMP_NUM_THREADS' set to number of cores (default)

General Notes

This result has been produced with binaries provided and compiled by Intel.

All binaries were built with 64-bit Intel compiler except:

437.leslie3d, 450.soplex, 470.lbm and 482.sphinx3 in peak were built with 32-bit Intel compiler by changing the path for include and library files.

For information about Fujitsu Siemens Computers please see:

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General Notes (Continued)

<http://www.fujitsu-siemens.com>

Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-fast

C++ benchmarks:

-fast

Fortran benchmarks:

-fast

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Base Optimization Flags (Continued)

Benchmarks using both Fortran and C:

-fast

Peak Compiler Invocation

C benchmarks (except as noted below):

```
/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

433.milc: icc

C++ benchmarks (except as noted below):

icpc

```
450.soplex: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/icpc  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

Fortran benchmarks (except as noted below):

ifort

```
437.leslie3d: /home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/bin/ifort  
-L/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/lib  
-I/home/cmpllr/usr3/alrahate/compilers/ic10.1mainline/20070725/Linux32/include
```

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
444.namd: -DSPEC_CPU_LP64  
447.dealII: -DSPEC_CPU_LP64  
453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
465.tonto: -DSPEC_CPU_LP64  
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```



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Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-scalar-rep -prefetch -opt-malloc-options=3

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -O0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -O0
-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-prefetch -parallel -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.02.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic10.1-FP-intel64-linux-flags.20090713.02.xml>

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For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.

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