



SPEC® CFP2006 Result

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Hewlett-Packard Company

ProLiant BL460c
(2.66 GHz, Intel Xeon E5430)

SPECfp®2006 = 21.7

SPECfp_base2006 = 18.4

CPU2006 license: 3

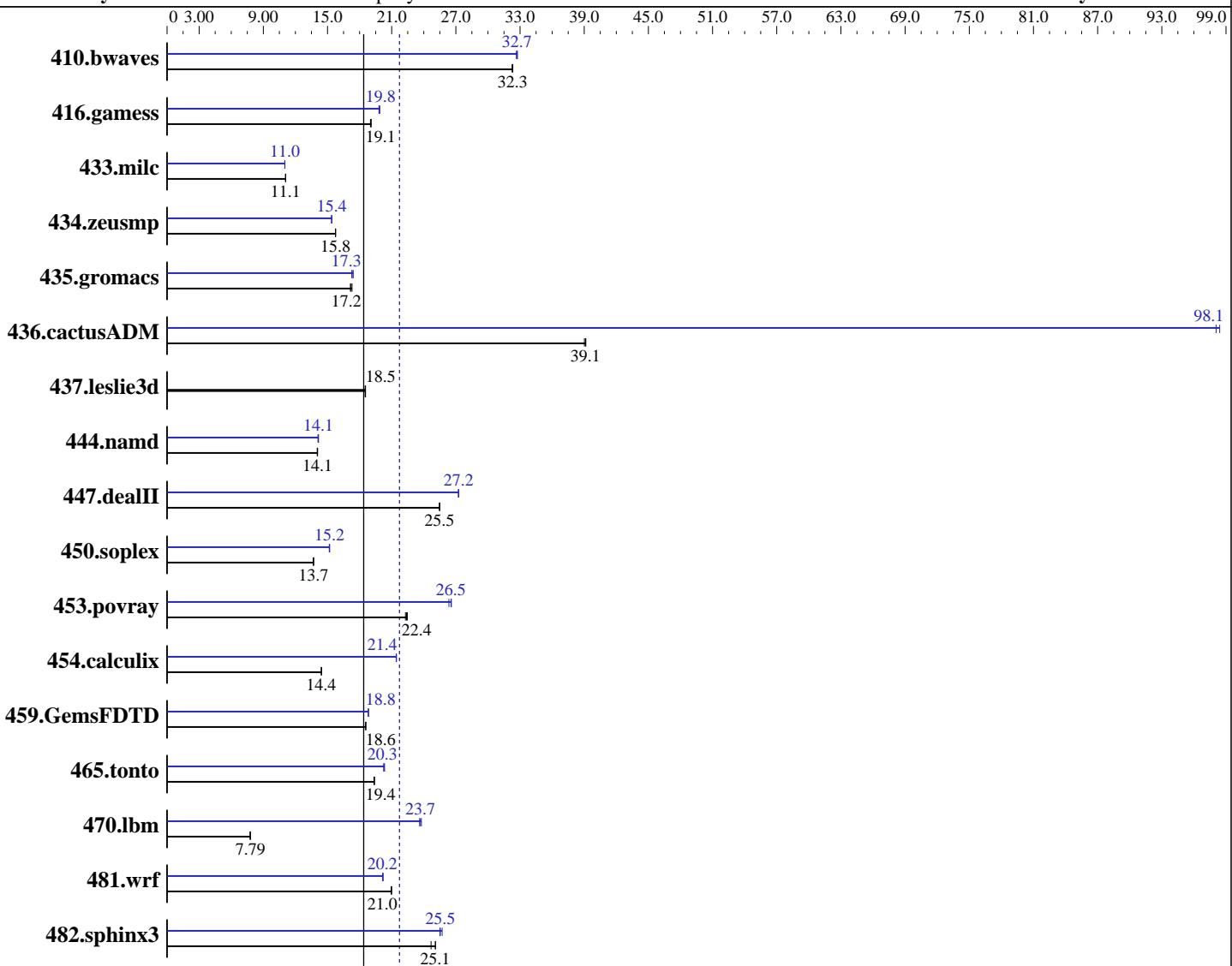
Test sponsor: Hewlett-Packard Company

Tested by: Hewlett-Packard Company

Test date: Feb-2008

Hardware Availability: Nov-2007

Software Availability: Nov-2007



Hardware

CPU Name: Intel Xeon E5430
CPU Characteristics: 2.66 GHz, 2x6 MB L2 shared, 1333 MHz system bus
CPU MHz: 2666
FPU: Integrated
CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip
CPU(s) orderable: 1,2 chips
Primary Cache: 32 KB I + 32 KB D on chip per core
Secondary Cache: 12 MB I+D on chip per chip, 6 MB shared / 2 cores

Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler: Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Intel Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel: Yes
File System: ext2
System State: Run level 3 (multi-user)

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F CL5)
Disk Subsystem: 1x72 GB 15 K SAS
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: binutils-2.17.50

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	420	32.3	421	32.3	421	32.3	416	32.7	415	32.7	415	32.8
416.gamess	1027	19.1	1027	19.1	1030	19.0	988	19.8	984	19.9	987	19.8
433.milc	828	11.1	828	11.1	829	11.1	833	11.0	833	11.0	834	11.0
434.zeusmp	577	15.8	578	15.7	578	15.8	591	15.4	591	15.4	591	15.4
435.gromacs	417	17.1	415	17.2	413	17.3	413	17.3	413	17.3	410	17.4
436.cactusADM	305	39.2	305	39.1	306	39.0	122	98.1	122	98.1	121	98.4
437.leslie3d	507	18.6	507	18.5	508	18.5	507	18.6	507	18.5	508	18.5
444.namd	571	14.0	570	14.1	570	14.1	566	14.2	567	14.1	568	14.1
447.dealII	449	25.5	449	25.5	449	25.5	420	27.2	420	27.2	419	27.3
450.soplex	608	13.7	610	13.7	610	13.7	550	15.2	549	15.2	549	15.2
453.povray	239	22.3	238	22.4	237	22.5	200	26.5	200	26.6	202	26.4
454.calculix	572	14.4	571	14.4	572	14.4	385	21.4	385	21.4	385	21.4
459.GemsFDTD	571	18.6	571	18.6	571	18.6	564	18.8	564	18.8	564	18.8
465.tonto	507	19.4	508	19.4	509	19.3	484	20.3	485	20.3	486	20.3
470.lbm	1764	7.79	1771	7.76	1765	7.79	578	23.8	582	23.6	580	23.7
481.wrf	532	21.0	533	21.0	533	20.9	554	20.2	554	20.2	553	20.2
482.sphinx3	777	25.1	790	24.7	777	25.1	758	25.7	763	25.5	764	25.5

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
KMP_STACKSIZE set to 200M

Platform Notes

BIOS configuration:
Power Regulator set to Static High Performance Mode



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Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
  433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
  444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
  465.tonto: -DSPEC_CPU_LP64
  470.lbm: -DSPEC_CPU_LP64
  481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64
```

Base Optimization Flags

C benchmarks:
-fast -parallel

C++ benchmarks:
-fast -parallel

Fortran benchmarks:
-fast -parallel

Benchmarks using both Fortran and C:
-fast -parallel



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Peak Compiler Invocation

C benchmarks (except as noted below):

```
/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

433.milc: icc

C++ benchmarks (except as noted below):

```
icpc
```

```
450.soplex: /opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib  
-I/opt/intel/cc/10.1.008/include
```

Fortran benchmarks:

```
ifort
```

Benchmarks using both Fortran and C:

```
icc ifort
```

Peak Portability Flags

```
410.bwaves: -DSPEC_CPU_LP64  
416.gamess: -DSPEC_CPU_LP64  
    433.milc: -DSPEC_CPU_LP64  
434.zeusmp: -DSPEC_CPU_LP64  
435.gromacs: -DSPEC_CPU_LP64 -nofor_main  
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main  
437.leslie3d: -DSPEC_CPU_LP64  
    444.namd: -DSPEC_CPU_LP64  
    447.dealII: -DSPEC_CPU_LP64  
    453.povray: -DSPEC_CPU_LP64  
454.calculix: -DSPEC_CPU_LP64 -nofor_main  
459.GemsFDTD: -DSPEC_CPU_LP64  
    465.tonto: -DSPEC_CPU_LP64  
    481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
```

Peak Optimization Flags

C benchmarks:

```
433.milc: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias  
-auto-ilp32
```

```
470.lbm: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12  
-scalar-rep -prefetch -opt-malloc-options=3
```

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Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll12

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias
-auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast
-opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14
-ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12 -O0
-ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12 -O0
-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll14 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch
-auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll12
-prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -parallel -prefetch -auto-ilp32

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-fp-flags.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/HP-Intel-ic10.1-linux-fp-flags.xml>



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For other inquiries, please contact webmaster@spec.org.

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