SPEC® CINT2006 Result
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Bull SAS
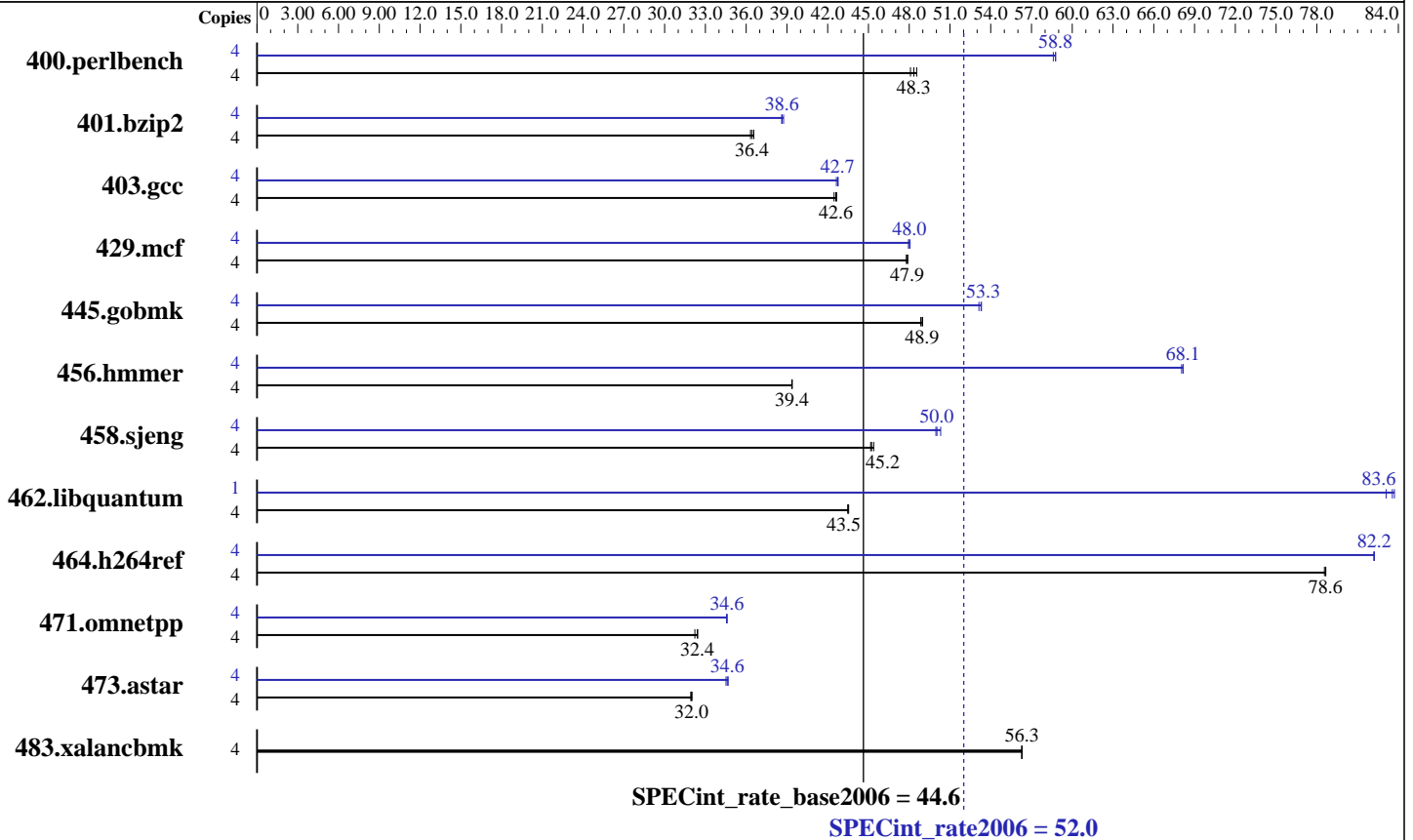
NovaScale T860 E1
(Intel Xeon E5205,1.86GHz)

$SPECint^®\_rate2006 = \qquad 52.0$

$SPECint\_rate\_base2006 = 44.6$

CPU2006 license: 20

Test sponsor: Bull SAS

Tested by: NEC Corporation

Test date: Feb-2008

Hardware Availability: Feb-2008

Software Availability: Nov-2007

Copies

Benchmark		
400.perlbench	4	58.8
	4	48.3
401.bzip2	4	38.6
	4	36.4
403.gcc	4	42.7
	4	42.6
429.mcf	4	48.0
	4	47.9
445.gobmk	4	53.3
	4	48.9
456.hmmer	4	68.1
	4	39.4
458.sjeng	4	50.0
	4	45.2
462.libquantum	1	83.6
	4	43.5
464.h264ref	4	82.2
	4	78.6
471.omnetpp	4	34.6
	4	32.4
473.astar	4	34.6
	4	32.0
483.xalancbmk	4	56.3

SPECint_rate_base2006 = 44.6

SPECint_rate2006 = 52.0

Hardware

CPU Name:	Intel Xeon E5205
CPU Characteristics:	1.86 GHz, 6 MB L2, 1066 MHz bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	12 GB (12x1 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem:	1x73.2 GB SAS, 15000RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ Compiler for Linux32 and Linux64 version 10.1 Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	MicroQuill SmartHeap library 8.1 binutils-2.17.tar.gz, Version 2.17

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Results Table

| Benchmark | Base | | | | | | Peak | | | | | |
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	4	**808**	**48.3**	813	48.1	805	48.6	4	665	58.8	667	58.6	**665**	**58.8**
401.bzip2	4	1056	36.6	**1059**	**36.4**	1063	36.3	4	996	38.8	**999**	**38.6**	1000	38.6
403.gcc	4	**756**	**42.6**	758	42.5	755	42.7	4	755	42.6	753	42.8	**753**	**42.7**
429.mcf	4	762	47.9	**762**	**47.9**	763	47.8	4	759	48.1	761	47.9	**760**	**48.0**
445.gobmk	4	**857**	**48.9**	859	48.8	857	49.0	4	**787**	**53.3**	790	53.1	787	53.3
456.hmmer	4	948	39.4	947	39.4	**948**	**39.4**	4	548	68.0	**548**	**68.1**	548	68.2
458.sjeng	4	1072	45.2	1066	45.4	**1070**	**45.2**	4	**967**	**50.0**	962	50.3	969	50.0
462.libquantum	4	1903	43.5	1906	43.5	**1906**	**43.5**	1	248	83.7	249	83.1	**248**	**83.6**
464.h264ref	4	1127	78.6	**1126**	**78.6**	1125	78.7	4	**1076**	**82.2**	1077	82.2	1076	82.2
471.omnetpp	4	**771**	**32.4**	771	32.4	776	32.2	4	723	34.6	723	34.6	**723**	**34.6**
473.astar	4	**878**	**32.0**	880	31.9	877	32.0	4	**811**	**34.6**	813	34.5	810	34.7
483.xalancbmk	4	490	56.3	490	56.3	**490**	**56.3**	4	490	56.3	490	56.3	**490**	**56.3**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
 OMP_NUM_THREADS set to number of cores
```

Platform Notes

```
Bios settings:
Intel SpeedStep Technology: Disabled
```

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2,
456.hmmer, for peak, are compiled in 64-bit mode

 The NEC Express5800/120Lj(Intel Xeon E5205) and
 the Bull NovaScale T860 E1(Intel Xeon E5205,1.86GHz) models are electronically equivalent.
 The results have been measured on a NEC Express5800/120Lj(Intel Xeon E5205) model.
```

Base Compiler Invocation

C benchmarks:
```
icc
```

C++ benchmarks:
```
icpc
```

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Base Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
    462.libquantum: -DSPEC_CPU_LINUX
    483.xalancbmk: -DSPEC_CPU_LINUX
```

Base Optimization Flags

C benchmarks:
```
    -fast -inline-calloc -opt-malloc-options=3
```

C++ benchmarks:
```
    -xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
    -L/opt/SmartHeap_8.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

```
        403.gcc: -Dalloca=_alloca
```

Peak Compiler Invocation

C benchmarks (except as noted below):
```
    icc
```

```
      401.bzip2: /opt/intel/cce/10.1.008/bin/icc
            -L/opt/intel/cce/10.1.008/lib
            -I/opt/intel/cce/10.1.008/include
```

```
      456.hmmer: /opt/intel/cce/10.1.008/bin/icc
            -L/opt/intel/cce/10.1.008/lib
            -I/opt/intel/cce/10.1.008/include
```

C++ benchmarks:
```
    icpc
```

Peak Portability Flags

```
    400.perlbench: -DSPEC_CPU_LINUX_IA32
      401.bzip2: -DSPEC_CPU_LP64
      456.hmmer: -DSPEC_CPU_LP64
    462.libquantum: -DSPEC_CPU_LINUX
```

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Peak Portability Flags (Continued)

483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/opt/SmartHeap_8.1/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

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Peak Other Flags (Continued)

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/NEC-Intel-ic10.1-INT-ia32-linux-flags.20090713.xml

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

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