SPEC® CINT2006 Result

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HITACHI

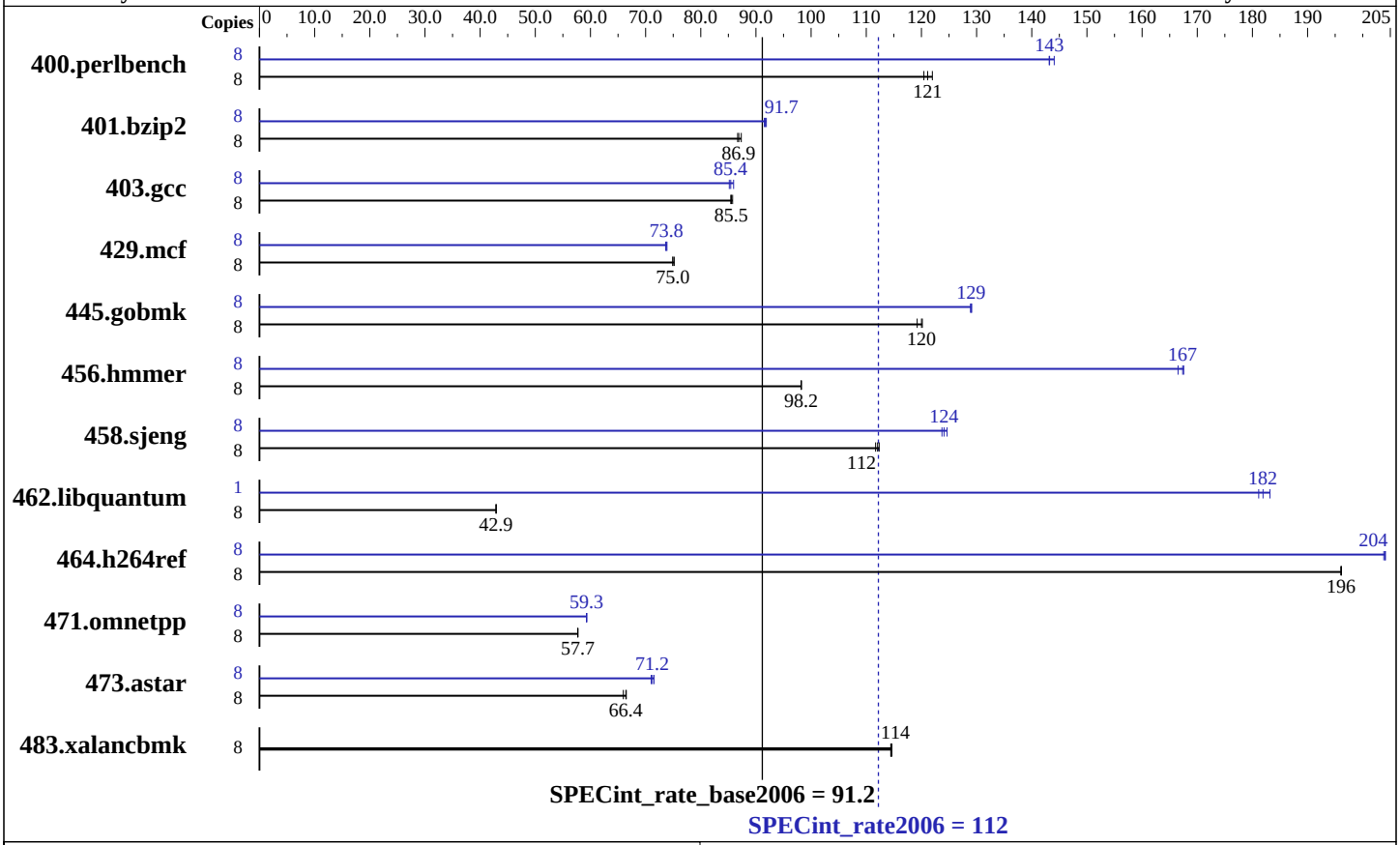
BladeSymphony BS320 es (Intel Xeon L5410)

SPECint®_rate2006 = 112

SPECint_rate_base2006 = 91.2

CPU2006 license: 872
Test sponsor: HITACHI
Tested by: HITACHI

Test date: May-2008
Hardware Availability: May-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon L5410
CPU Characteristics:	1333MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1, 2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	8 GB(4 x 2 GB PC2-5300F CAS 5-5-5)
Disk Subsystem:	1 x 73 GB 10000 rpm SAS
Other Hardware:	None

Software

Operating System:	Red Hat Enterprise Linux Server release 5.1 (Tikanga) Kernel 2.6.18-53.el5 on an x86_64
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008
Auto Parallel:	Yes
File System:	ext3
System State:	Multi-user run level 3
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap library V8.1

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Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	8	641	122	649	120	**645**	**121**	8	543	144	**546**	**143**	546	143
401.bzip2	8	884	87.3	**888**	**86.9**	891	86.7	8	**842**	**91.7**	840	91.9	843	91.5
403.gcc	8	751	85.8	**753**	**85.5**	754	85.4	8	749	86.0	756	85.2	**754**	**85.4**
429.mcf	8	970	75.2	**973**	**75.0**	974	74.9	8	**989**	**73.8**	991	73.6	988	73.9
445.gobmk	8	704	119	698	120	**699**	**120**	8	650	129	651	129	**650**	**129**
456.hmmer	8	**760**	**98.2**	760	98.2	760	98.2	8	448	167	445	168	**446**	**167**
458.sjeng	8	867	112	862	112	**864**	**112**	8	**780**	**124**	777	125	782	124
462.libquantum	8	3864	42.9	**3864**	**42.9**	3865	42.9	1	**114**	**182**	113	183	114	181
464.h264ref	8	903	196	903	196	**903**	**196**	8	**868**	**204**	867	204	869	204
471.omnetpp	8	**866**	**57.7**	866	57.7	867	57.7	8	844	59.3	842	59.3	**843**	**59.3**
473.astar	8	851	66.0	845	66.5	**846**	**66.4**	8	**788**	**71.2**	785	71.5	791	71.0
483.xalancbmk	8	482	114	481	115	**482**	**114**	8	482	114	481	115	**482**	**114**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
'/bin/taskset' used to bind processes to CPUs
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to physical,0
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Base Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fast -inline-calloc -opt-malloc-options=3

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/home/bsc/smartheap/lib -lsmartheap

Base Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

401.bzip2: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

456.hmmer: /opt/intel/cce/10.1.008/bin/icc
-L/opt/intel/cce/10.1.008/lib
-I/opt/intel/cce/10.1.008/include

C++ benchmarks:
icpc

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LINUX_IA32
401.bzip2: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LINUX
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -prof-gen(pass 1) -prof-use(pass 2) -fast -ansi-alias
-prefetch

Continued on next page

Peak Optimization Flags (Continued)

401.bzip2: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch

403.gcc: -fast -inline-calloc -opt-malloc-options=3

429.mcf: -fast -prefetch

445.gobmk: -prof-gen(pass 1) -prof-use(pass 2) -xT -O2 -ipo
-no-prec-div -ansi-alias

456.hmmer: -fast -unroll2 -ansi-alias -opt-multi-version-aggressive

458.sjeng: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4

462.libquantum: -fast -unroll4 -Ob0 -prefetch
-opt-streaming-stores always -vec-guard-write
-opt-malloc-options=3 -parallel -par-runtime-control

464.h264ref: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2
-ansi-alias

C++ benchmarks:

471.omnetpp: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=block
-Wl,-z,muldefs -L/home/bsc/smartheap/lib -lsmartheap

473.astar: -prof-gen(pass 1) -prof-use(pass 2) -xT -O3 -ipo
-no-prec-div -ansi-alias -opt-ra-region-strategy=routine
-Wl,-z,muldefs -L/home/bsc/smartheap/lib -lsmartheap

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.03.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-INT-ia32-linux-flags.20090714.03.xml

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