SPEC® CFP2006 Result

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Bull SAS

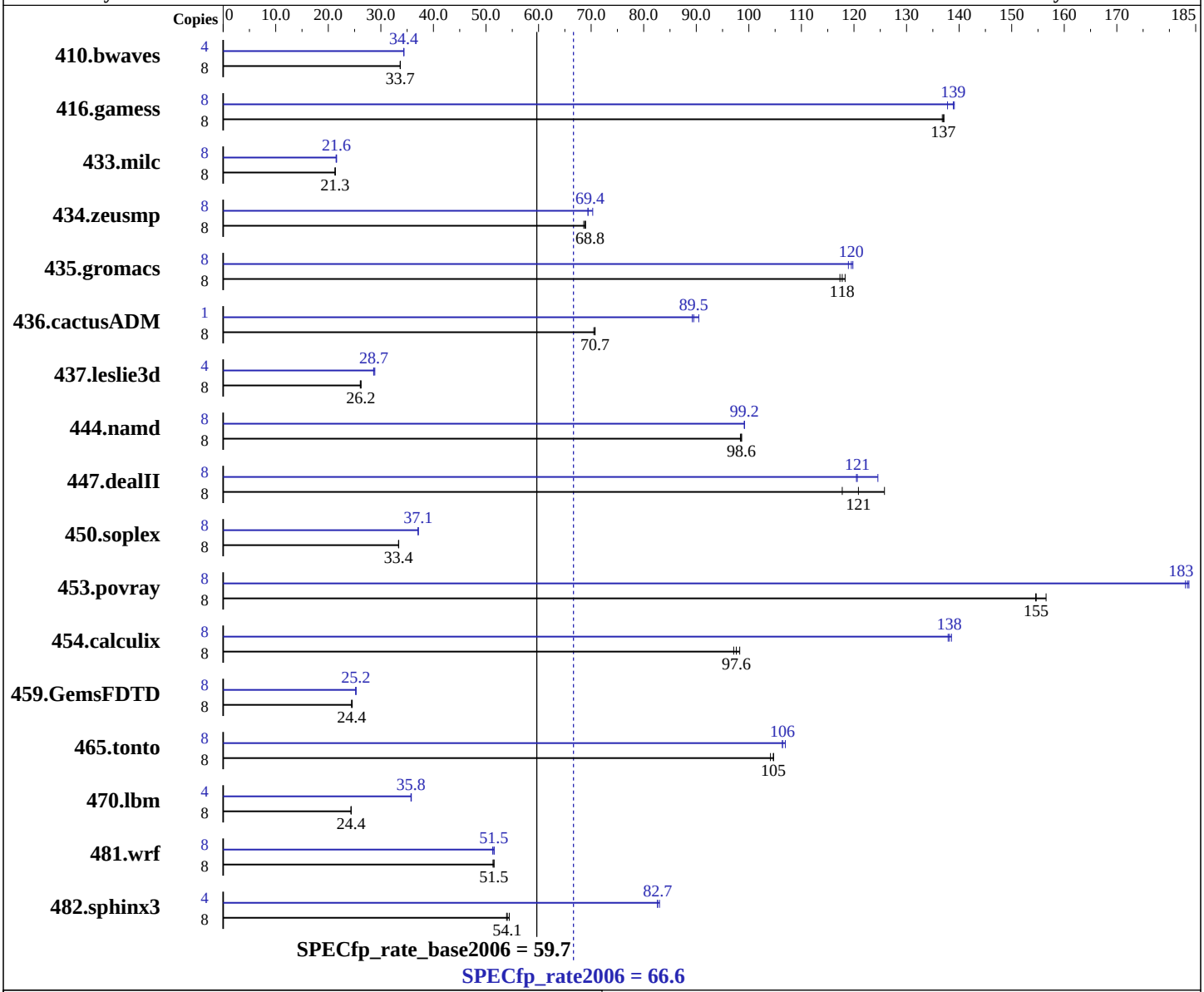
NovaScale R440 E1
(Intel Xeon E5410, 2.33 GHz)

SPECfp®_rate2006 = 66.6

SPECfp_rate_base2006 = 59.7

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: Bull SAS

Test date: Jun-2008
Hardware Availability: Jan-2008
Software Availability: Nov-2007



Hardware

CPU Name:	Intel Xeon E5410
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2333
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

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Software

Operating System:	SUSE LINUX Enterprise Server 10 SP1 Kernel 2.6.16.46-0.12-smp for x86_64
Compiler:	Intel C++ and Fortran Compiler 10.1 for Linux Build 20070913 Package ID: l_cc_p_10.1.008, l_fc_p_10.1.008
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB) FB-DIMM PC2-5300F ECC CL5
Disk Subsystem: 1x73 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.17.50.0.15

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	8	**3226**	**33.7**	3226	33.7	3229	33.7	4	1582	34.4	**1582**	**34.4**	1581	34.4
416.gamess	8	**1143**	**137**	1145	137	1142	137	8	**1128**	**139**	1137	138	1126	139
433.milc	8	**3445**	**21.3**	3442	21.3	3450	21.3	8	3405	21.6	3419	21.5	**3407**	**21.6**
434.zeusmp	8	1061	68.6	1056	69.0	**1058**	**68.8**	8	1049	69.4	**1049**	**69.4**	1036	70.3
435.gromacs	8	483	118	**485**	**118**	487	117	8	480	119	**478**	**120**	477	120
436.cactusADM	8	1356	70.5	**1352**	**70.7**	1351	70.8	1	134	89.2	**133**	**89.5**	132	90.5
437.leslie3d	8	2867	26.2	2883	26.1	**2873**	**26.2**	4	**1311**	**28.7**	1314	28.6	1303	28.9
444.namd	8	651	98.6	**651**	**98.6**	652	98.4	8	647	99.2	647	99.2	**647**	**99.2**
447.dealII	8	**757**	**121**	727	126	777	118	8	760	120	**759**	**121**	735	125
450.soplex	8	1997	33.4	2000	33.4	**1998**	**33.4**	8	**1798**	**37.1**	1799	37.1	1798	37.1
453.povray	8	272	157	**275**	**155**	275	155	8	**232**	**183**	232	183	232	184
454.calculix	8	**676**	**97.6**	672	98.3	680	97.1	8	479	138	**478**	**138**	476	139
459.GemsFDTD	8	3456	24.6	**3472**	**24.4**	3473	24.4	8	**3364**	**25.2**	3357	25.3	3365	25.2
465.tonto	8	**752**	**105**	752	105	756	104	8	740	106	736	107	**740**	**106**
470.lbm	8	**4514**	**24.4**	4513	24.4	4519	24.3	4	1535	35.8	**1537**	**35.8**	1538	35.7
481.wrf	8	1741	51.3	**1735**	**51.5**	1734	51.5	8	1743	51.3	1733	51.6	**1735**	**51.5**
482.sphinx3	8	2892	53.9	**2884**	**54.1**	2865	54.4	4	944	82.6	**943**	**82.7**	939	83.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'/usr/bin/taskset' used to bind processes to CPUs
KMP_AFFINITY set to physical,0
OMP_NUM_THREADS set to number of cores
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
```

General Notes

```
All benchmarks compiled in 64-bit mode except 450.soplex,
470.lbm and 482.sphinx3, at peak, are compiled in 32-bit mode
The Bull NovaScale R440 E1 (Intel Xeon E5410, 2.33 GHz) and
the Bull NovaScale R460 E1 (Intel Xeon E5410, 2.33 GHz) models are electronically equivalent.
The results have been measured on a Bull NovaScale R460 E1 (Intel Xeon E5410, 2.33 GHz) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-fast

C++ benchmarks:
-fast

Fortran benchmarks:
-fast

Benchmarks using both Fortran and C:
-fast

Peak Compiler Invocation

C benchmarks (except as noted below):
`/opt/intel/cc/10.1.008/bin/icc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

433.milc: `icc`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/cc/10.1.008/bin/icpc -L/opt/intel/cc/10.1.008/lib -I/opt/intel/cc/10.1.008/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/fc/10.1.008/bin/ifort -L/opt/intel/fc/10.1.008/lib -I/opt/intel/fc/10.1.008/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -fno-alias -auto-ilp32`

470.lbm: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -scalar-rep- -prefetch -opt-malloc-options=3`

Continued on next page

Peak Optimization Flags (Continued)

482.sphinx3: -fast -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -fast -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -fast -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -fast -prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -fast

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -opt-malloc-options=3

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -Ob0 -prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -fast -prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -fast -unroll2 -prefetch -parallel -auto-ilp32

454.calculix: -fast -unroll-aggressive -auto-ilp32

481.wrf: -fast -auto-ilp32

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.00.html

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/EM64T_Intel101_fp_flags.20090713.00.xml`

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For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.0.
Report generated on Tue Jul 22 18:44:33 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 5 August 2008.