SPEC® CINT2006 Result

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Dell Inc.

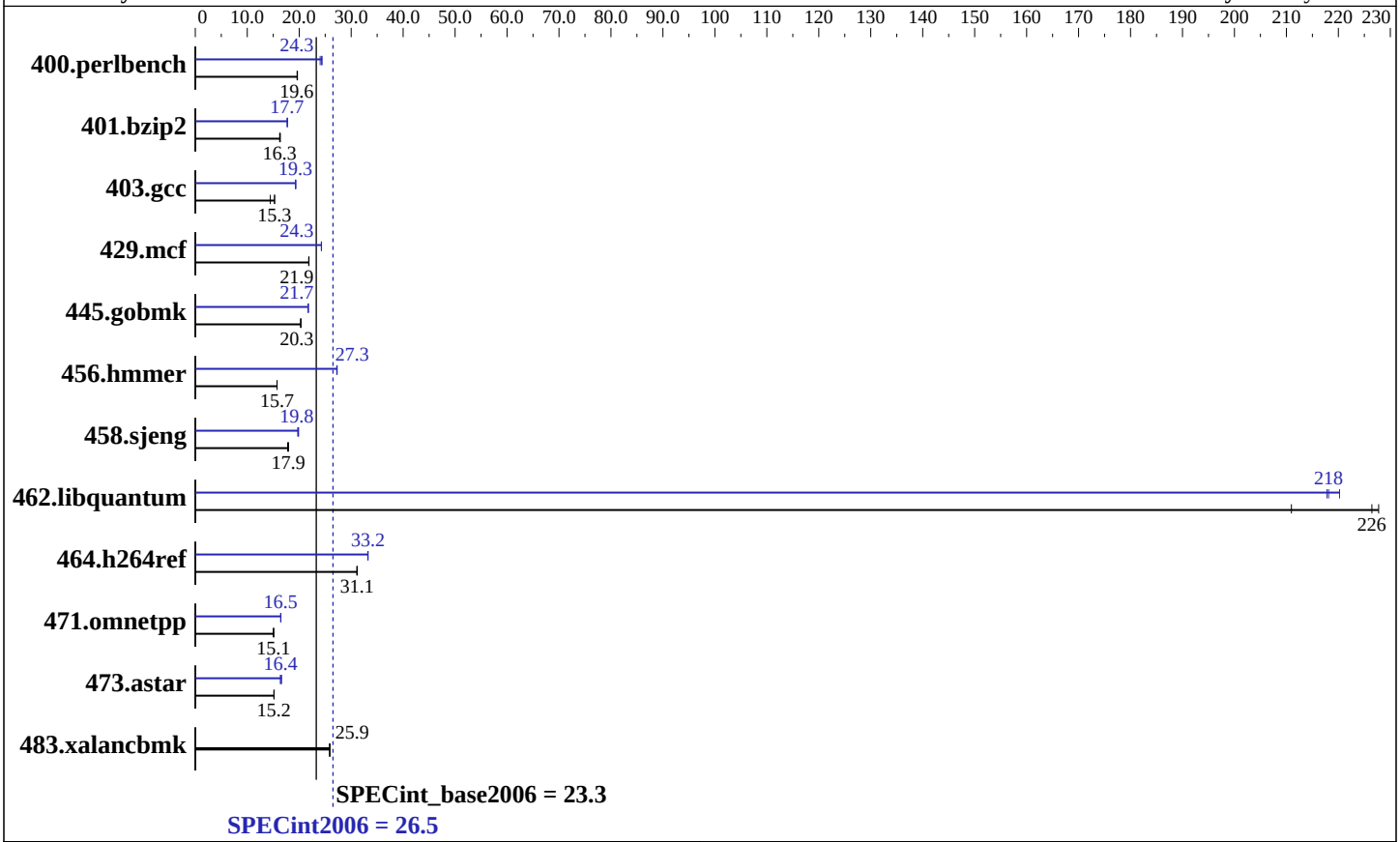
PowerEdge M600 (Intel Xeon E5450, 3.00 GHz)

SPECint®2006 = 26.5

SPECint_base2006 = 23.3

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Jul-2008
Hardware Availability: Jan-2008
Software Availability: May-2008



Hardware

CPU Name:	Intel Xeon E5450
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (4x4 GB, DDR2-667 MHz, CL5, Reg, Dual Rank)
Disk Subsystem:	1 x 80 GB SATA 5400 RPM
Other Hardware:	None

Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2 Kernel 2.6.16-60.0.21-smp
Compiler:	Intel C++ Compiler 10.1 for Linux Build 20080312 Package ID: l_cc_p_10.1.015
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	32-bit
Peak Pointers:	32/64-bit
Other Software:	Microquill SmartHeap V8.1 Binutils 2.18.50.0.7.20080502

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	**498**	**19.6**	497	19.7	499	19.6	407	24.0	400	24.4	**402**	**24.3**
401.bzip2	590	16.4	**593**	**16.3**	594	16.2	544	17.7	**545**	**17.7**	545	17.7
403.gcc	527	15.3	558	14.4	**528**	**15.3**	417	19.3	**417**	**19.3**	417	19.3
429.mcf	417	21.9	418	21.8	**417**	**21.9**	**376**	**24.3**	376	24.3	375	24.3
445.gobmk	**517**	**20.3**	517	20.3	516	20.3	**483**	**21.7**	483	21.7	483	21.7
456.hmmer	**593**	**15.7**	593	15.7	593	15.7	342	27.3	**342**	**27.3**	343	27.2
458.sjeng	675	17.9	681	17.8	**676**	**17.9**	**610**	**19.8**	609	19.9	614	19.7
462.libquantum	98.2	211	91.0	228	**91.5**	**226**	95.1	218	94.1	220	**95.0**	**218**
464.h264ref	710	31.2	**711**	**31.1**	712	31.1	667	33.2	**666**	**33.2**	665	33.3
471.omnetpp	414	15.1	416	15.0	**415**	**15.1**	**380**	**16.5**	381	16.4	379	16.5
473.astar	463	15.2	**463**	**15.2**	464	15.1	428	16.4	423	16.6	**428**	**16.4**
483.xalancbmk	**267**	**25.9**	266	25.9	267	25.9	**267**	**25.9**	266	25.9	267	25.9

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
All benchmarks compiled in 32-bit mode except 401.bzip2 and 456.hmmer,
for peak, are compiled in 64-bit mode
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
```

Base Compiler Invocation

C benchmarks:
`icc`

C++ benchmarks:
`icpc`

Base Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Base Optimization Flags

C benchmarks:
```
-fast -vec-guard-write -parallel -par-runtime-control
```

C++ benchmarks:
```
-xT -ipo -O3 -no-prec-div -Wl,-z,muldefs
-L/spec/cpu2006.1.1/lib -lsmartheap
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

401.bzip2:
```
/opt/intel/cce/10.1.015/bin/icc
-L/opt/intel/cce/10.1.015/lib
-I/opt/intel/cce/10.1.015/include
```

456.hmmer:
```
/opt/intel/cce/10.1.015/bin/icc
-L/opt/intel/cce/10.1.015/lib
-I/opt/intel/cce/10.1.015/include
```

C++ benchmarks:
```
icpc
```

Peak Portability Flags

400.perlbench: `-DSPEC_CPU_LINUX_IA32`
401.bzip2: `-DSPEC_CPU_LP64`
456.hmmer: `-DSPEC_CPU_LP64`
462.libquantum: `-DSPEC_CPU_LINUX`
483.xalancbmk: `-DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

400.perlbench: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -ansi-alias -prefetch`

401.bzip2: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -prefetch -auto-ilp32`

403.gcc: `-fast -inline-calloc -opt-malloc-options=3`

429.mcf: `-fast -prefetch`

445.gobmk: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O2 -ipo -no-prec-div -ansi-alias`

456.hmmer: `-fast -unroll2 -ansi-alias -opt-multi-version-aggressive -auto-ilp32`

458.sjeng: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll4`

462.libquantum: `-fast -unroll4 -Ob0 -prefetch -opt-streaming-stores always -vec-guard-write -opt-malloc-options=3 -parallel -par-runtime-control`

464.h264ref: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-fast -unroll2 -ansi-alias`

C++ benchmarks:

471.omnetpp: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=block -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

473.astar: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xT -O3 -ipo -no-prec-div -ansi-alias -opt-ra-region-strategy=routine -Wl,-z,muldefs -L/spec/cpu2006.1.1/lib -lsmartheap`

483.xalancbmk: `basepeak = yes`

Peak Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.html`

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic10.1-int-linux64-revD.20090713.00.xml
http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.07.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 19:31:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 19 August 2008.