SPEC® CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

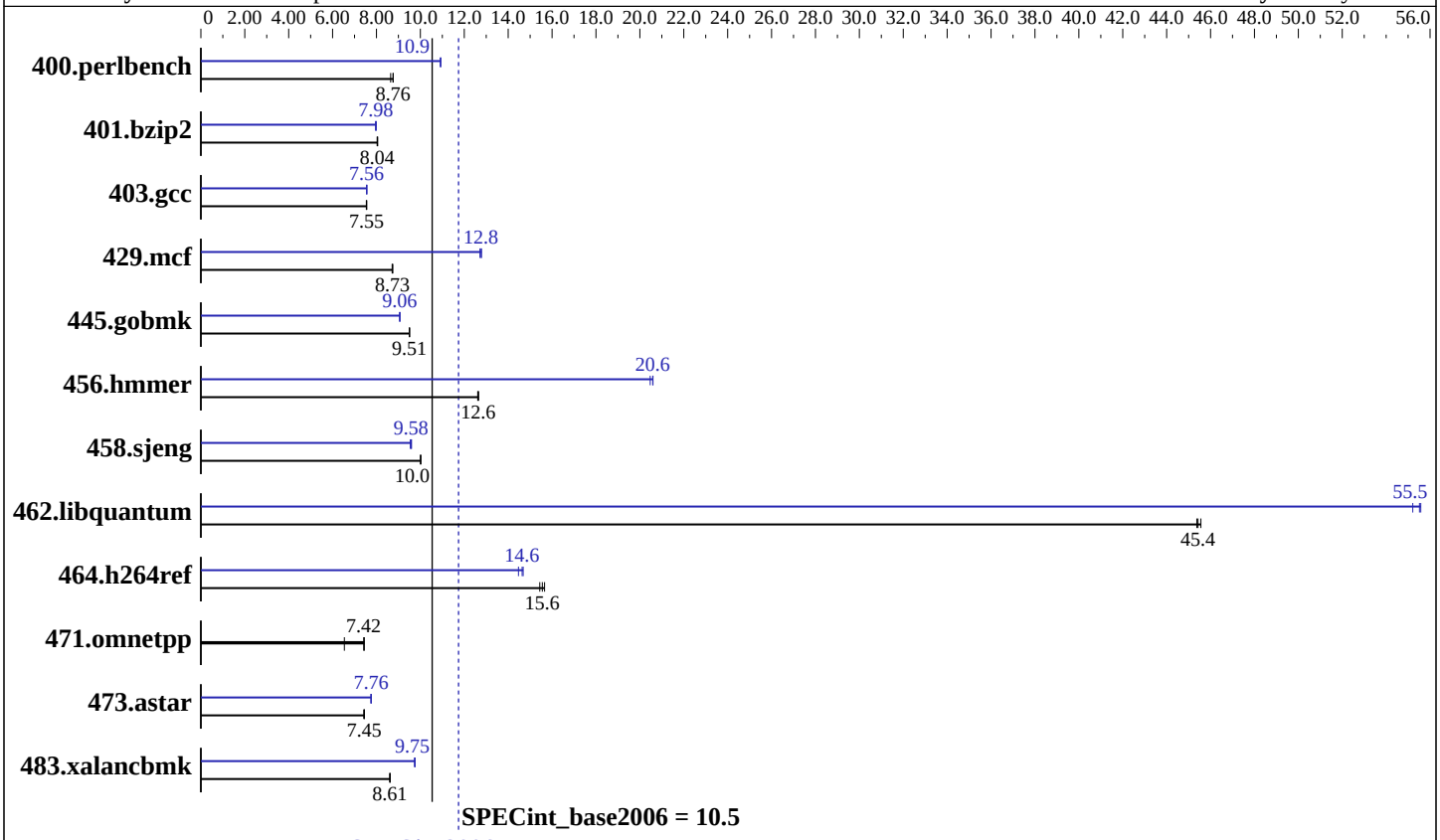
IBM BladeCenter LS42 (AMD Opteron 8347 HE)

SPECint®2006 = 11.7

SPECint_base2006 = 10.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2008
Hardware Availability: Sep-2008
Software Availability: May-2008



Hardware

CPU Name:	AMD Opteron 8347 HE
CPU Characteristics:	
CPU MHz:	1900
FPU:	Integrated
CPU(s) enabled:	16 cores, 4 chips, 4 cores/chip
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	64 KB I + 64 KB D on chip per core
Secondary Cache:	512 KB I+D on chip per core
L3 Cache:	2 MB I+D on chip per chip
Other Cache:	None
Memory:	64 GB (16 x 4 GB DDR2-6400 ECC)
Disk Subsystem:	1 x 73 GB SAS, 10000 RPM
Other Hardware:	None

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	PGI Server Complete Version 7.2
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (Full multiuser with network)
Base Pointers:	32/64-bit
Peak Pointers:	32/64-bit
Other Software:	SmartHeap 8.1 32-bit Library for Linux binutils 2.18.50

SPEC CINT2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

IBM Corporation

IBM BladeCenter LS42 (AMD Opteron 8347 HE)

SPECint2006 = 11.7

SPECint_base2006 = 10.5

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Aug-2008
Hardware Availability: Sep-2008
Software Availability: May-2008

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	1130	8.65	**1116**	**8.76**	1116	8.76	894	10.9	896	10.9	**894**	**10.9**
401.bzip2	1198	8.06	1201	8.03	**1201**	**8.04**	1208	7.99	1213	7.96	**1210**	**7.98**
403.gcc	**1067**	**7.55**	1065	7.56	1068	7.54	1065	7.56	1065	7.56	**1065**	**7.56**
429.mcf	1043	8.74	1047	8.71	**1044**	**8.73**	713	12.8	**714**	**12.8**	717	12.7
445.gobmk	**1104**	**9.51**	1105	9.50	1103	9.51	**1157**	**9.06**	1158	9.06	1157	9.07
456.hmmer	**739**	**12.6**	740	12.6	737	12.7	456	20.5	**453**	**20.6**	453	20.6
458.sjeng	1208	10.0	1210	10.0	**1208**	**10.0**	**1263**	**9.58**	1268	9.54	1261	9.60
462.libquantum	457	45.4	**456**	**45.4**	455	45.6	373	55.6	375	55.2	**373**	**55.5**
464.h264ref	1413	15.7	1433	15.4	**1423**	**15.6**	1507	14.7	1530	14.5	**1512**	**14.6**
471.omnetpp	839	7.45	**843**	**7.42**	956	6.54	839	7.45	**843**	**7.42**	956	6.54
473.astar	946	7.42	**942**	**7.45**	942	7.45	905	7.76	**905**	**7.76**	906	7.75
483.xalancbmk	802	8.61	**802**	**8.61**	800	8.63	709	9.73	707	9.76	**708**	**9.75**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'numactl' was used to bind copies to the cores.
Environment stack size set to 'unlimited'.
'ulimit -l 2097152' was used to set environment locked pages in memory quantity.
NCPUS set to number of cores.
PGI_HUGE_PAGES set to 896.
Set vm/nr_hugepages=14336 in /etc/sysctl.conf
mount -t hugetlbfs none /mnt/hugepages
Processor Performance States Disabled in BIOS
Memory ChipKill Disabled in BIOS
```

Base Compiler Invocation

C benchmarks:

```
pgcc
```

C++ benchmarks:

```
pgcpp
```

Base Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64
403.gcc: -DSPEC_CPU_LP64
429.mcf: -DSPEC_CPU_LP64
445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Base Optimization Flags

C benchmarks:
-fastsse -Msmartalloc=huge:896 -Mloop32 -Mconcur=innermost
-Mfprelaxed -Mipa=fast -Mipa=inline -tp barcelona-64 -Bstatic_pgi

C++ benchmarks:
-fastsse -Msmartalloc=huge:896 -Mloop32 -Mfprelaxed --zc_eh
-Mipa=fast -Mipa=inline -tp barcelona-32 -Bstatic_pgi

Base Other Flags

C benchmarks:
-Mipa=jobs:8

C++ benchmarks:
-Mipa=jobs:8

Peak Compiler Invocation

C benchmarks:
pgcc

C++ benchmarks:
pgcpp

Peak Portability Flags

400.perlbench: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX_X64
401.bzip2: -DSPEC_CPU_LP64

Continued on next page

Peak Portability Flags (Continued)

445.gobmk: -DSPEC_CPU_LP64
456.hmmer: -DSPEC_CPU_LP64
458.sjeng: -DSPEC_CPU_LP64
462.libquantum: -DSPEC_CPU_LP64 -DSPEC_CPU_LINUX
464.h264ref: -DSPEC_CPU_LP64
483.xalancbmk: -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

400.perlbench: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=inline(pass 2) -fastsse
-O4 -Msmartalloc=huge:896 -Mnovect -Mnounroll -Mfprelaxed
-tp barcelona-64 -Bstatic_pgi

401.bzip2: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2) -fastsse -O4
-Msmartalloc=huge:896 -Mprefetch=t0 -Mnounroll
-tp barcelona-64 -Bstatic_pgi

403.gcc: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline(pass 2) -fastsse -Msmartalloc=huge:896
-Mprefetch=t0 -Mnodalign -Mloop32 -Mfprelaxed
-tp barcelona-32 -Bstatic_pgi

429.mcf: -fastsse -Msmartalloc=huge:896 -Mipa=fast -Mipa=inline:1
-tp barcelona-32 -Bstatic_pgi

445.gobmk: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2) -fastsse
-O4 -Msmartalloc=huge:896 -Mnovect -Mfprelaxed
-tp barcelona-64 -Bstatic_pgi

456.hmmer: -fastsse -Mvect=partial -Munroll=n:8 -Msmartalloc=huge:896
-Msafeptr -Mprefetch=t0 -Mfprelaxed -Mipa=const -Mipa=ptr
-Mipa=arg -Mipa=inline -tp barcelona-64 -Bstatic_pgi

458.sjeng: -Mpfi(pass 1) -Mpfo(pass 2) -Mipa=fast(pass 2)
-Mipa=inline:1(pass 2) -Mipa=noarg(pass 2) -fastsse
-Msmartalloc=huge:896 -Mfprelaxed -tp barcelona-64
-Bstatic_pgi

462.libquantum: -fastsse -Munroll=m:8 -Msmartalloc=huge:896
-Mprefetch=distance:8 -Mconcur=innermost -Mconcur=noaltcode
-Mfprelaxed -Mipa=fast -Mipa=noarg -tp barcelona-64
-Bstatic_pgi

464.h264ref: -Mpfi=indirect(pass 1) -Mpfo=indirect(pass 2)
-Mipa=fast(pass 2) -Mipa=inline(pass 2) -fastsse
-Msmartalloc=huge:896 -Mfprelaxed -tp barcelona-64
-Bstatic_pgi

Continued on next page

Peak Optimization Flags (Continued)

C++ benchmarks:

471.omnetpp: `basepeak = yes`

473.astar: `-Mpfi`(pass 1) `-Mpfo`(pass 2) `-Mipa=fast`(pass 2) `-Mipa=inline:6`(pass 2) `-fastsse -O4 -Msmartalloc=huge:896 -Msafeptr=global -Mloop32 -Mfprelaxed --zc_eh -tp barcelona-32 -Bstatic_pgi`

483.xalancbmk: `--zc_eh -fastsse -O4 -Mfprelaxed -Msmartalloc -Mipa=fast -Mipa=inline -tp barcelona-32 -Bstatic_pgi -lsmartheap`

Peak Other Flags

C benchmarks (except as noted below):
`-Mipa=jobs:8`(pass 2)

401.bzip2: `No flags used`

C++ benchmarks (except as noted below):
`-Mipa=jobs:8`(pass 2)

483.xalancbmk: `-Mipa=jobs:8 -L/proj/qa/smartheap/SmartHeap_8.1/lib`

The flags file that was used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/pgi72_flags.html`

You can also download the XML flags source by saving the following link:
`http://www.spec.org/cpu2006/flags/pgi72_flags.xml`

SPEC and SPECint are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 19:05:45 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 2 September 2008.