SPEC® CINT2006 Result

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Dell Inc.

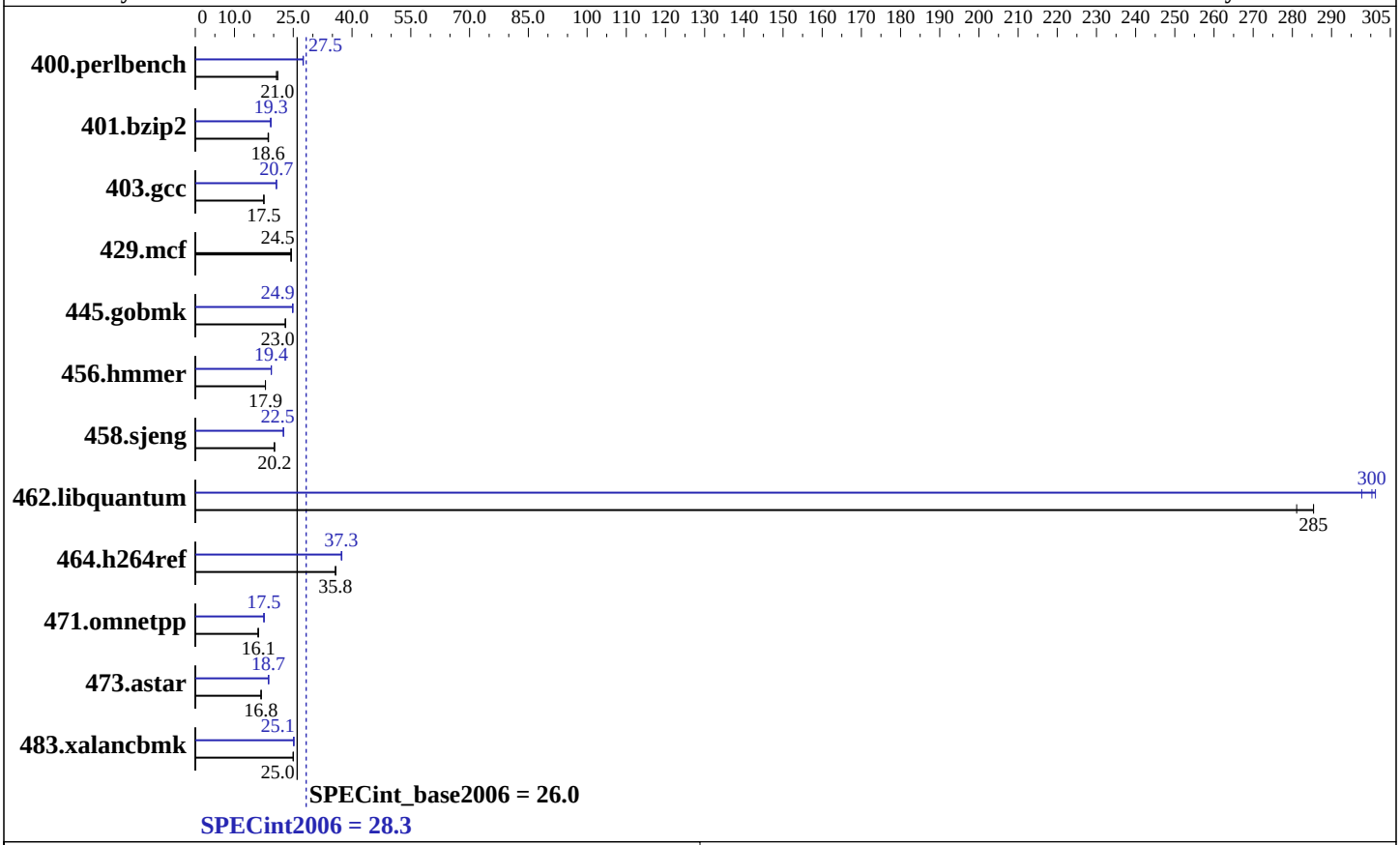
Dell Precision T7400 (Intel Xeon X5492, 3.40 GHz)

SPECint®2006 = 28.3

SPECint_base2006 = 26.0

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Sep-2008
Hardware Availability: Sep-2008
Software Availability: Mar-2008



Hardware

CPU Name:	Intel Xeon X5492
CPU Characteristics:	1600 MHz Bus Speed
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	8 cores, 2 chips, 4 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB 800 MHz CL5 FB-DIMM)
Disk Subsystem:	1 x 80 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Business SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA-32, Version 10.1 Build 20080312 Package ID: w_cc_p_10.1.021 Microsoft Visual Studio 2005 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	471	20.7	**466**	**21.0**	466	21.0	355	27.6	355	27.5	**355**	**27.5**
401.bzip2	**518**	**18.6**	518	18.6	517	18.7	501	19.3	**501**	**19.3**	501	19.2
403.gcc	459	17.5	**460**	**17.5**	460	17.5	**389**	**20.7**	389	20.7	389	20.7
429.mcf	**373**	**24.5**	373	24.5	374	24.4	**373**	**24.5**	373	24.5	374	24.4
445.gobmk	456	23.0	**457**	**23.0**	457	23.0	422	24.8	422	24.9	**422**	**24.9**
456.hmmer	**521**	**17.9**	521	17.9	521	17.9	**480**	**19.4**	480	19.4	480	19.4
458.sjeng	**599**	**20.2**	599	20.2	599	20.2	539	22.5	**538**	**22.5**	538	22.5
462.libquantum	72.6	285	**72.6**	**285**	73.7	281	68.8	301	69.6	298	**69.0**	**300**
464.h264ref	617	35.8	**618**	**35.8**	618	35.8	**593**	**37.3**	593	37.3	592	37.4
471.omnetpp	388	16.1	**389**	**16.1**	390	16.0	356	17.6	357	17.5	**357**	**17.5**
473.astar	418	16.8	**417**	**16.8**	417	16.8	375	18.7	**375**	**18.7**	375	18.7
483.xalancbmk	**276**	**25.0**	276	25.0	276	25.0	275	25.1	**275**	**25.1**	274	25.2

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)
BIOS Settings
  Adjacent Cache Line Prefetch set to ON
```

Base Compiler Invocation

C benchmarks:

```
icl -Qstd=c99
```

C++ benchmarks:

```
icl
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -Qpar-runtime-control -Qvec-guard-write /F512000000
libguide40.lib
```

C++ benchmarks:
```
-fast -Qcxx_features /F512000000 shlw32m.lib libguide40.lib
            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qstd=c99
```

C++ benchmarks:
```
icl
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qansi-alias -Qprefetch -Qparallel -Qpar-runtime-control /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast -Qprefetch /F512000000 libguide40.lib`

403.gcc: `-Qprof_gen`(pass 1) `-Qprof_use`(pass 2) `-fast /F512000000 libguide40.lib`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: basepeak = yes

445.gobmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -QxT -O2 -Qipo -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias -Qopt-multi-version-aggressive /F512000000 libguide40.lib

458.sjeng: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll4 /F512000000 libguide40.lib

462.libquantum: -fast -Qunroll4 -Ob0 -Qprefetch -Qopt-streaming-stores:always -Qparallel -Qpar-runtime-control /F512000000 libguide40.lib

464.h264ref: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll2 -Qansi-alias /F512000000 libguide40.lib

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=block -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qopt-ra-region-strategy=routine -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

483.xalancbmk: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qansi-alias -Qcxx_features /F512000000 shlw32m.lib libguide40.lib -link /FORCE:MULTIPLE

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic10.1.windows.flags.xml

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Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 21:02:05 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 1 October 2008.