SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

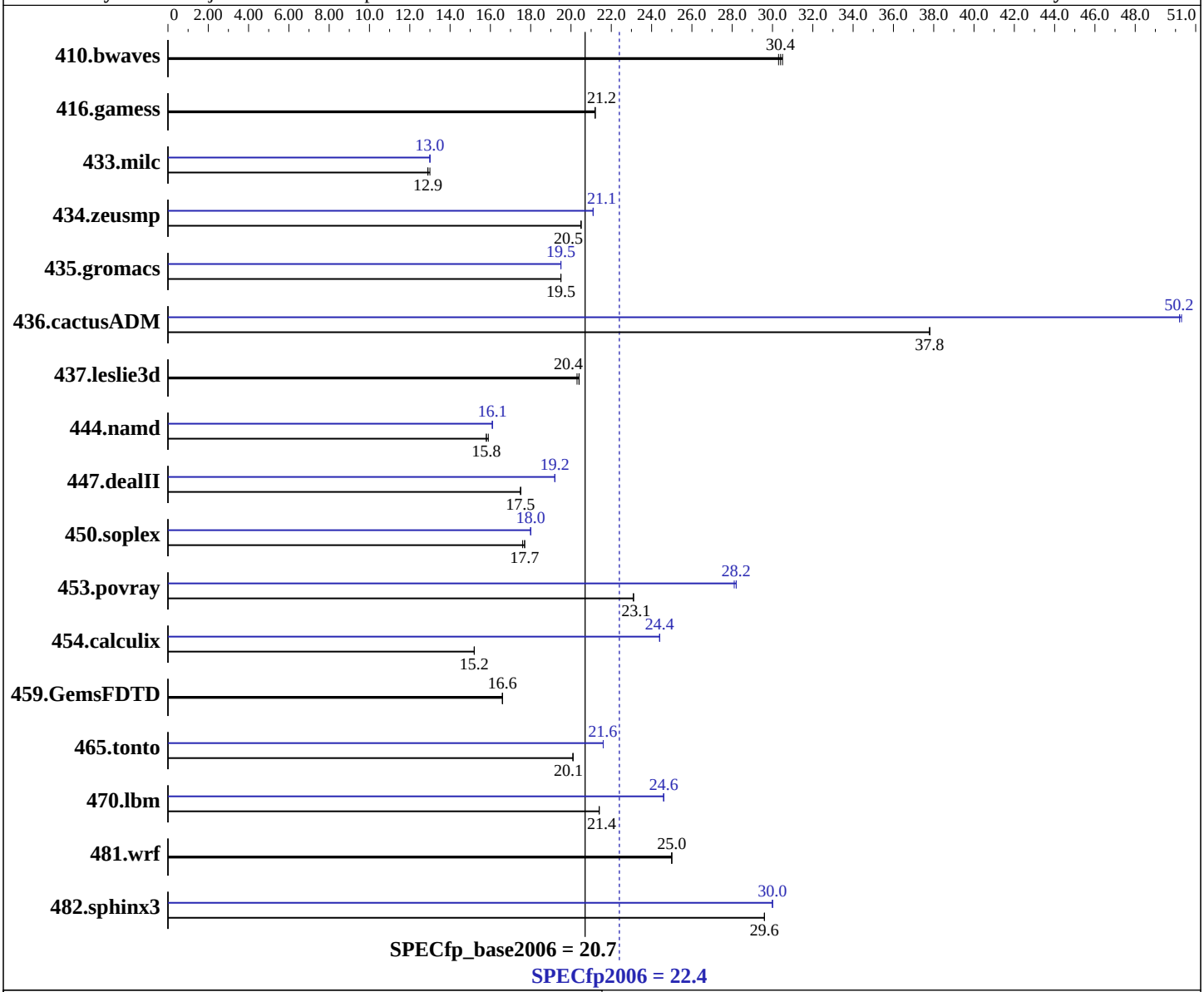
CELSIUS M460, Intel Core 2 Quad Q9650 processor

SPECfp®2006 = 22.4

SPECfp_base2006 = 20.7

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Feb-2008



Hardware

CPU Name:	Intel Core 2 Quad Q9650
CPU Characteristics:	
CPU MHz:	3000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System:	Windows Vista Ultimate (x64)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Fujitsu Siemens Computers

CELSIUS M460, Intel Core 2 Quad Q9650 processor

SPECfp2006 = 22.4

SPECfp_base2006 = 20.7

CPU2006 license: 22
Test sponsor: Fujitsu Siemens Computers
Tested by: Fujitsu Siemens Computers

Test date: Oct-2008
Hardware Availability: Sep-2008
Software Availability: Feb-2008

L3 Cache: None
Other Cache: None
Memory: 4 GB (4x1 GB PC2-6400 CL6 SDRAM)
Disk Subsystem: 1 x 400 GB SATA 7200 RPM
Other Hardware: None

Compiler: Intel C++ Compiler for applications running on Intel 64, Version 10.1, Build 20080212
Intel Visual Fortran Compiler for applications running on Intel 64, Version 10.1, Build 20080212
Microsoft Visual Studio 2005 with SP1 (for libraries)
Auto Parallel: Yes
File System: NTFS
System State: Default
Base Pointers: 64-bit
Peak Pointers: 64-bit
Other Software: MicroQuill SmartHeap Library 8.0 (64 bit)

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	445	30.5	**447**	**30.4**	448	30.3	445	30.5	**447**	**30.4**	448	30.3
416.gamess	923	21.2	**923**	**21.2**	923	21.2	923	21.2	**923**	**21.2**	923	21.2
433.milc	708	13.0	**709**	**12.9**	710	12.9	705	13.0	**706**	**13.0**	706	13.0
434.zeusmp	444	20.5	444	20.5	**444**	**20.5**	431	21.1	431	21.1	**431**	**21.1**
435.gromacs	366	19.5	366	19.5	**366**	**19.5**	367	19.5	**367**	**19.5**	366	19.5
436.cactusADM	316	37.8	**316**	**37.8**	316	37.8	238	50.2	238	50.3	**238**	**50.2**
437.leslie3d	461	20.4	**462**	**20.4**	462	20.3	461	20.4	**462**	**20.4**	462	20.3
444.namd	**506**	**15.8**	506	15.8	506	15.9	499	16.1	**499**	**16.1**	498	16.1
447.dealII	**653**	**17.5**	653	17.5	653	17.5	597	19.2	**597**	**19.2**	597	19.2
450.soplex	472	17.7	**472**	**17.7**	473	17.6	**463**	**18.0**	463	18.0	463	18.0
453.povray	**230**	**23.1**	230	23.1	230	23.1	**189**	**28.2**	189	28.2	189	28.1
454.calculix	541	15.2	**541**	**15.2**	542	15.2	338	24.4	338	24.4	**338**	**24.4**
459.GemsFDTD	638	16.6	**638**	**16.6**	638	16.6	638	16.6	**638**	**16.6**	638	16.6
465.tonto	**489**	**20.1**	489	20.1	489	20.1	**456**	**21.6**	457	21.6	456	21.6
470.lbm	642	21.4	**642**	**21.4**	643	21.4	**558**	**24.6**	559	24.6	558	24.6
481.wrf	447	25.0	447	25.0	**447**	**25.0**	447	25.0	447	25.0	**447**	**25.0**
482.sphinx3	658	29.6	**658**	**29.6**	658	29.6	650	30.0	**650**	**30.0**	650	30.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
Screen Saver was disabled
Power scheme was set to Always On, modified to Turnoff monitor: Never.
```

Platform Notes

```
BIOS default settings have been used.
```

General Notes

```
For information about Fujitsu Siemens Computers please see:
http://www.fujitsu-siemens.com
```

Base Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc8 -Qc99 ifort
```

Base Portability Flags

410.bwaves: -DSPEC_CPU_P64
416.gamess: -DSPEC_CPU_P64
433.milc: -DSPEC_CPU_P64
434.zeusmp: -DSPEC_CPU_P64
435.gromacs: -DSPEC_CPU_P64
436.cactusADM: -DSPEC_CPU_P64 -Qlowercase /assume:underscore
437.leslie3d: -DSPEC_CPU_P64
444.namd: -DSPEC_CPU_P64 /TP
447.dealII: -DSPEC_CPU_P64 -DDEAL_II_MEMBER_VAR_SPECIALIZATION_BUG
450.soplex: -DSPEC_CPU_P64
453.povray: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
454.calculix: -DSPEC_CPU_P64 -DSPEC_CPU_NOZMODIFIER -Qlowercase
459.GemsFDTD: -DSPEC_CPU_P64
465.tonto: -DSPEC_CPU_P64
470.lbm: -DSPEC_CPU_P64
481.wrf: -DSPEC_CPU_P64 -DSPEC_CPU_WINDOWS_ICL
482.sphinx3: -DSPEC_CPU_P64

Base Optimization Flags

C benchmarks:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

Continued on next page

Base Optimization Flags (Continued)

C++ benchmarks:
```
-fast -Qparallel -Qcxx-features -F1000000000 libguide40.lib
shlW64M.lib -link -FORCE:MULTIPLE
```

Fortran benchmarks:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

Benchmarks using both Fortran and C:
```
-fast -Qparallel -F1000000000 libguide40.lib
```

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc8 -Qc99
```

C++ benchmarks:
```
icl -Qvc8
```

Fortran benchmarks:
```
ifort
```

Benchmarks using both Fortran and C:
```
icl -Qvc8 -Qc99 ifort
```

Peak Portability Flags

Same as Base Portability Flags

Peak Optimization Flags

C benchmarks:

433.milc: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

470.lbm: -fast -Qunroll2 -Qscalar-rep- -Qprefetch -F1000000000 libguide40.lib

482.sphinx3: -fast -Qunroll2 -F1000000000 libguide40.lib

C++ benchmarks:

444.namd: -fast -Qcxx-features -Oa -F1000000000 libguide40.lib shlW64M.lib -link -FORCE:MULTIPLE

Continued on next page

Peak Optimization Flags (Continued)

447.dealII: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qcxx-features -F10000000000 libguide40.lib shlW64M.lib -link -FORCE:MULTIPLE

450.soplex: Same as 447.dealII

453.povray: Same as 447.dealII

Fortran benchmarks:

410.bwaves: basepeak = yes

416.gamess: basepeak = yes

434.zeusmp: -O2 -Qunroll0 -QxT -Qscalar-rep- -Qprec-div- -F1000000000 libguide40.lib

437.leslie3d: basepeak = yes

459.GemsFDTD: basepeak = yes

465.tonto: -fast -Qunroll4 -Qauto -F1000000000 libguide40.lib

Benchmarks using both Fortran and C:

435.gromacs: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -F1000000000 libguide40.lib

436.cactusADM: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qparallel -Qprefetch -Qunroll2 -F1000000000 libguide40.lib

454.calculix: -Qprof_gen(pass 1) -Qprof_use(pass 2) -fast -Qunroll-aggressive -F1000000000 libguide40.lib

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.00.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/CPU2006_flags.20090713.00.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 20:29:42 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 29 October 2008.