SPEC® CFP2006 Result

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IBM Corporation

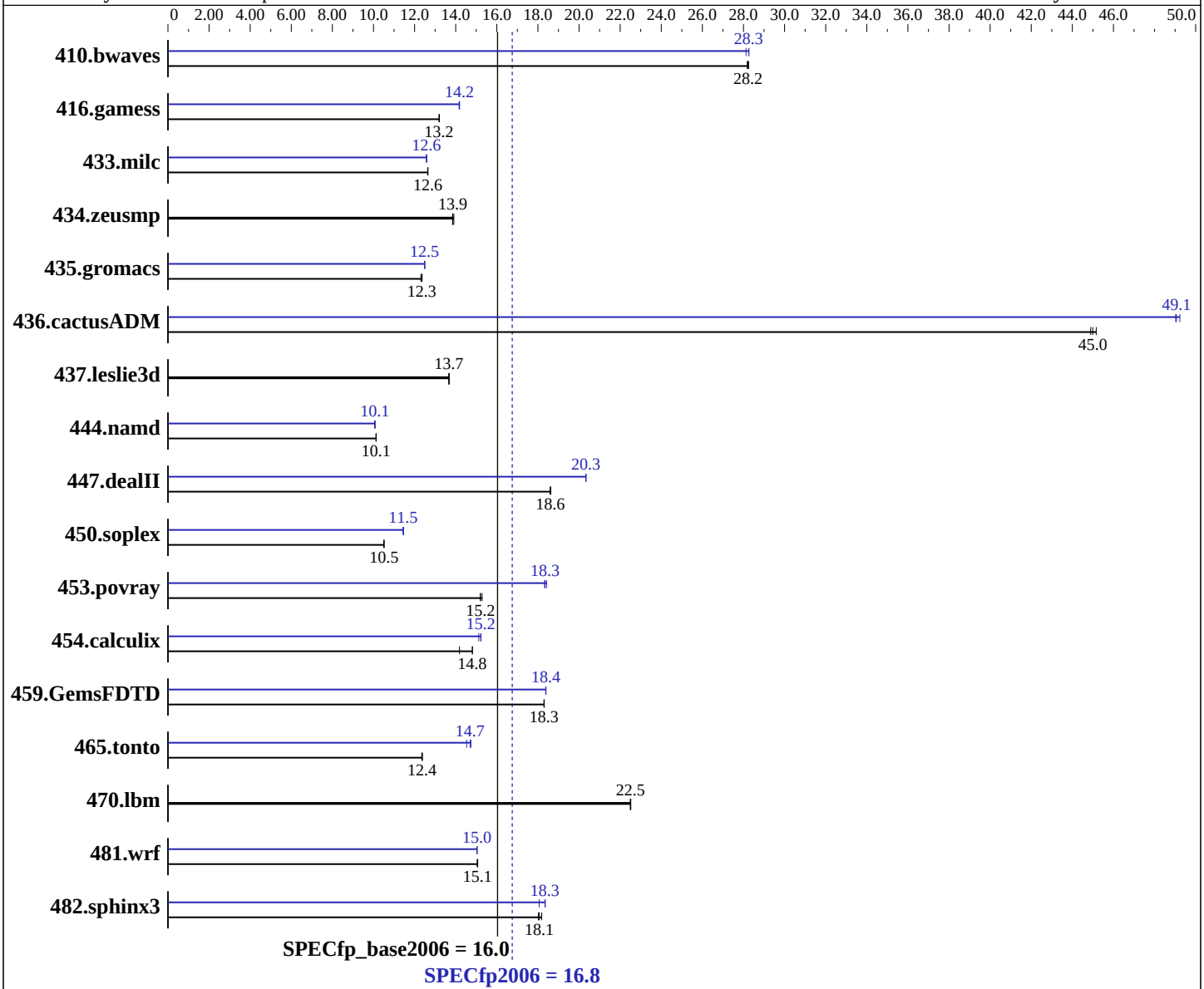
IBM System x3500 (Intel Xeon E5205)

SPECfp®2006 = 16.8

SPECfp_base2006 = 16.0

CPU2006 license: 11
Test sponsor: IBM Corporation
Tested by: IBM Corporation

Test date: Oct-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5205
CPU Characteristics:	1066MHz system bus
CPU MHz:	1866
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

Continued on next page

Software

Operating System:	SuSE Linux Enterprise Server 10 (x86_64) SP1, Kernel 2.6.16.46-0.12-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8 x 2 GB DDR2-5300F ECC)
Disk Subsystem: 1 x 80 GB SATA, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	482	28.2	**482**	**28.2**	481	28.3	**481**	**28.3**	483	28.1	481	28.3
416.gamess	**1484**	**13.2**	1484	13.2	1482	13.2	1380	14.2	**1381**	**14.2**	1382	14.2
433.milc	**726**	**12.6**	727	12.6	726	12.7	**730**	**12.6**	730	12.6	729	12.6
434.zeusmp	654	13.9	657	13.8	**657**	**13.9**	654	13.9	657	13.8	**657**	**13.9**
435.gromacs	**578**	**12.3**	577	12.4	580	12.3	572	12.5	571	12.5	**572**	**12.5**
436.cactusADM	265	45.2	266	44.9	**266**	**45.0**	243	49.2	244	49.0	**244**	**49.1**
437.leslie3d	**687**	**13.7**	687	13.7	688	13.7	**687**	**13.7**	687	13.7	688	13.7
444.namd	792	10.1	**792**	**10.1**	792	10.1	796	10.1	**796**	**10.1**	797	10.1
447.dealII	**615**	**18.6**	614	18.6	615	18.6	**563**	**20.3**	563	20.3	562	20.3
450.soplex	793	10.5	**793**	**10.5**	795	10.5	729	11.4	728	11.5	**728**	**11.5**
453.povray	348	15.3	350	15.2	**350**	**15.2**	290	18.3	**290**	**18.3**	289	18.4
454.calculix	582	14.2	557	14.8	**557**	**14.8**	542	15.2	546	15.1	**542**	**15.2**
459.GemsFDTD	580	18.3	**580**	**18.3**	580	18.3	577	18.4	**577**	**18.4**	577	18.4
465.tonto	795	12.4	**796**	**12.4**	797	12.3	668	14.7	**669**	**14.7**	677	14.5
470.lbm	611	22.5	**611**	**22.5**	610	22.5	611	22.5	**611**	**22.5**	610	22.5
481.wrf	742	15.0	**742**	**15.1**	741	15.1	743	15.0	742	15.0	**743**	**15.0**
482.sphinx3	1072	18.2	1081	18.0	**1078**	**18.1**	1079	18.1	**1063**	**18.3**	1062	18.4

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M
Hardware Prefetch Enabled, Adjacent Sector Prefetch Enabled
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
This result is measured on an IBM System x 3500 Server. Note that the IBM
System x 3500 and IBM System x 3400 are electrically equivalent.
```

Base Compiler Invocation

C benchmarks:

```
icc
```

Continued on next page

Base Compiler Invocation (Continued)

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

482.sphinx3: /opt/intel/Compiler/11.0/042/bin/ia32/icc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/Compiler/11.0/042/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/042/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/042/ipp/ia32/include

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

Continued on next page

Peak Optimization Flags (Continued)

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

Continued on next page

Peak Optimization Flags (Continued)

481.wrf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.13.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.13.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.03.xml`

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