SPEC® CFP2006 Result

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Bull SAS

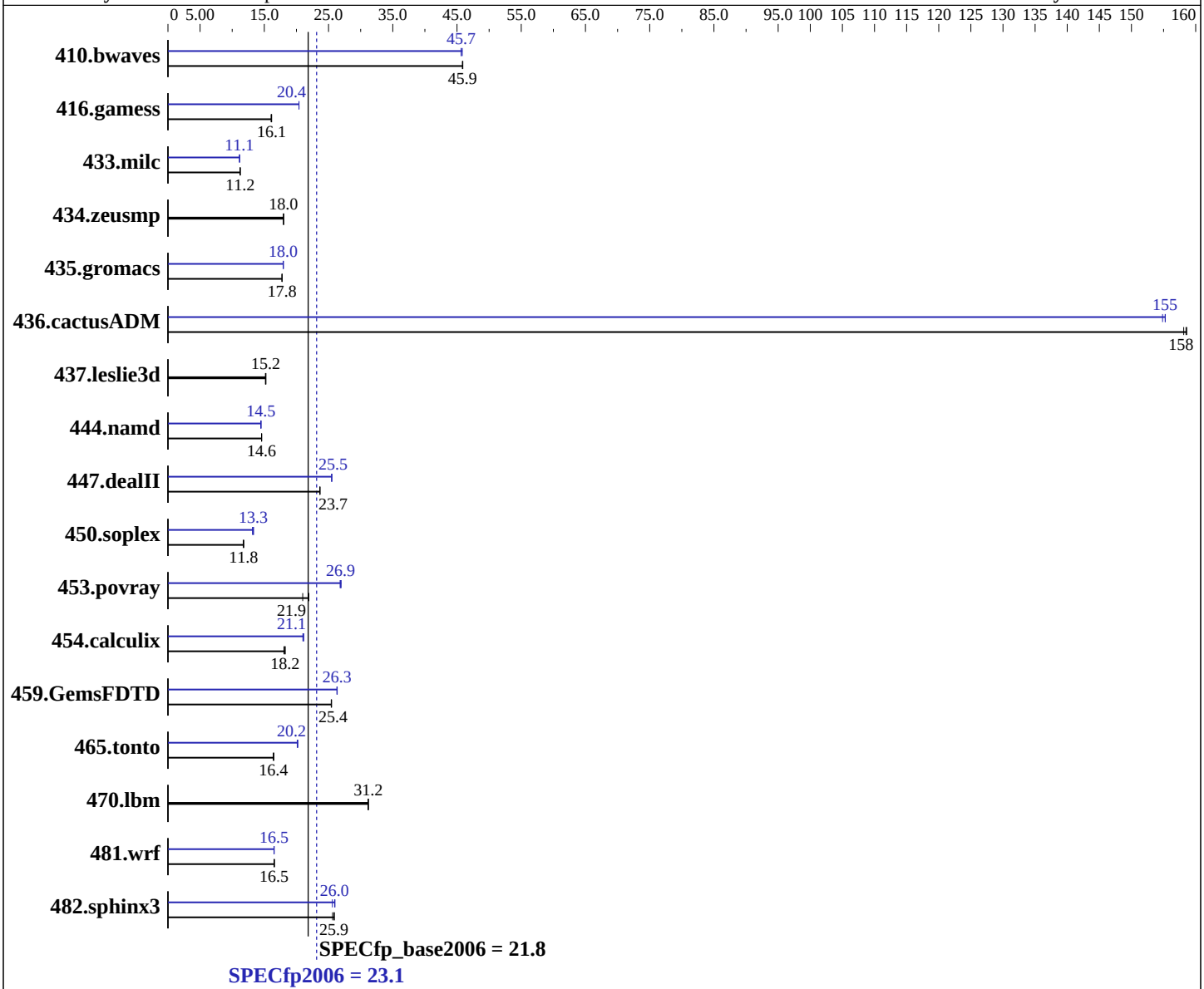
NovaScale R480 E1
(2.66 GHz, Intel Xeon X7460)

SPECfp®2006 = 23.1

SPECfp_base2006 = 21.8

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Nov-2008
Hardware Availability: Nov-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X7460
CPU Characteristics:	1066 MHz system bus
CPU MHz:	2667
FPU:	Integrated
CPU(s) enabled:	24 cores, 4 chips, 6 cores/chip
CPU(s) orderable:	1,2,3,4 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	9 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080930 Package ID: l_cproc_p_11.0.069, l_fproc_p_11.0.069
Auto Parallel:	Yes
File System:	ext2
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: 16 MB I+D on chip per chip
Other Cache: None
Memory: 32 GB (16x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 15000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	296	45.8	**296**	**45.9**	296	45.9	**298**	**45.7**	297	45.8	298	45.6
416.gamess	1220	16.1	1213	16.1	**1215**	**16.1**	**960**	**20.4**	960	20.4	960	20.4
433.milc	**817**	**11.2**	817	11.2	816	11.2	**824**	**11.1**	824	11.1	823	11.2
434.zeusmp	505	18.0	506	18.0	**506**	**18.0**	505	18.0	506	18.0	**506**	**18.0**
435.gromacs	**402**	**17.8**	403	17.7	401	17.8	**398**	**18.0**	398	18.0	398	17.9
436.cactusADM	**75.4**	**158**	75.6	158	75.3	159	77.2	155	77.0	155	**77.0**	**155**
437.leslie3d	**617**	**15.2**	617	15.2	620	15.2	**617**	**15.2**	617	15.2	620	15.2
444.namd	550	14.6	550	14.6	**550**	**14.6**	555	14.4	**554**	**14.5**	553	14.5
447.dealII	483	23.7	484	23.6	**483**	**23.7**	449	25.5	449	25.5	**449**	**25.5**
450.soplex	706	11.8	**709**	**11.8**	711	11.7	626	13.3	634	13.2	**628**	**13.3**
453.povray	243	21.9	254	21.0	**243**	**21.9**	199	26.8	**198**	**26.9**	197	27.0
454.calculix	**452**	**18.2**	457	18.1	452	18.2	390	21.1	393	21.0	**391**	**21.1**
459.GemsFDTD	**417**	**25.4**	417	25.5	417	25.4	403	26.4	**403**	**26.3**	403	26.3
465.tonto	597	16.5	600	16.4	**599**	**16.4**	487	20.2	489	20.1	**488**	**20.2**
470.lbm	441	31.1	440	31.2	**440**	**31.2**	441	31.1	440	31.2	**440**	**31.2**
481.wrf	674	16.6	676	16.5	**675**	**16.5**	677	16.5	676	16.5	**676**	**16.5**
482.sphinx3	**754**	**25.9**	752	25.9	760	25.6	749	26.0	762	25.6	**751**	**26.0**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 200M
```

Platform Notes

```
Bios settings:
Hardware Prefetcher:              Enabled
Adjacent Cache Line Prefetch:     Enabled
FSB High Bandwidth Optimization: Disabled
```

General Notes

```
The NEC Express5800/R140a-4(Intel Xeon X7460) and
the Bull NovaScale R480 E1(Intel Xeon X7460, 2.66 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/R140a-4(Intel Xeon X7460) model.
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch

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Base Optimization Flags (Continued)

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -parallel -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/069/bin/ia32/icc -L/opt/intel/Compiler/11.0/069/ipp/ia32/lib -I/opt/intel/Compiler/11.0/069/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/069/bin/ia32/icpc -L/opt/intel/Compiler/11.0/069/ipp/ia32/lib -I/opt/intel/Compiler/11.0/069/ipp/ia32/include`

Fortran benchmarks:
`ifort`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
437.leslie3d: `-DSPEC_CPU_LP64`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep- -opt-prefetch

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: basepeak = yes

437.leslie3d: basepeak = yes

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch -parallel

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

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Peak Optimization Flags (Continued)

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -parallel -auto-ilp32`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revD.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml`

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