SPEC® CINT2006 Result

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Dell Inc.

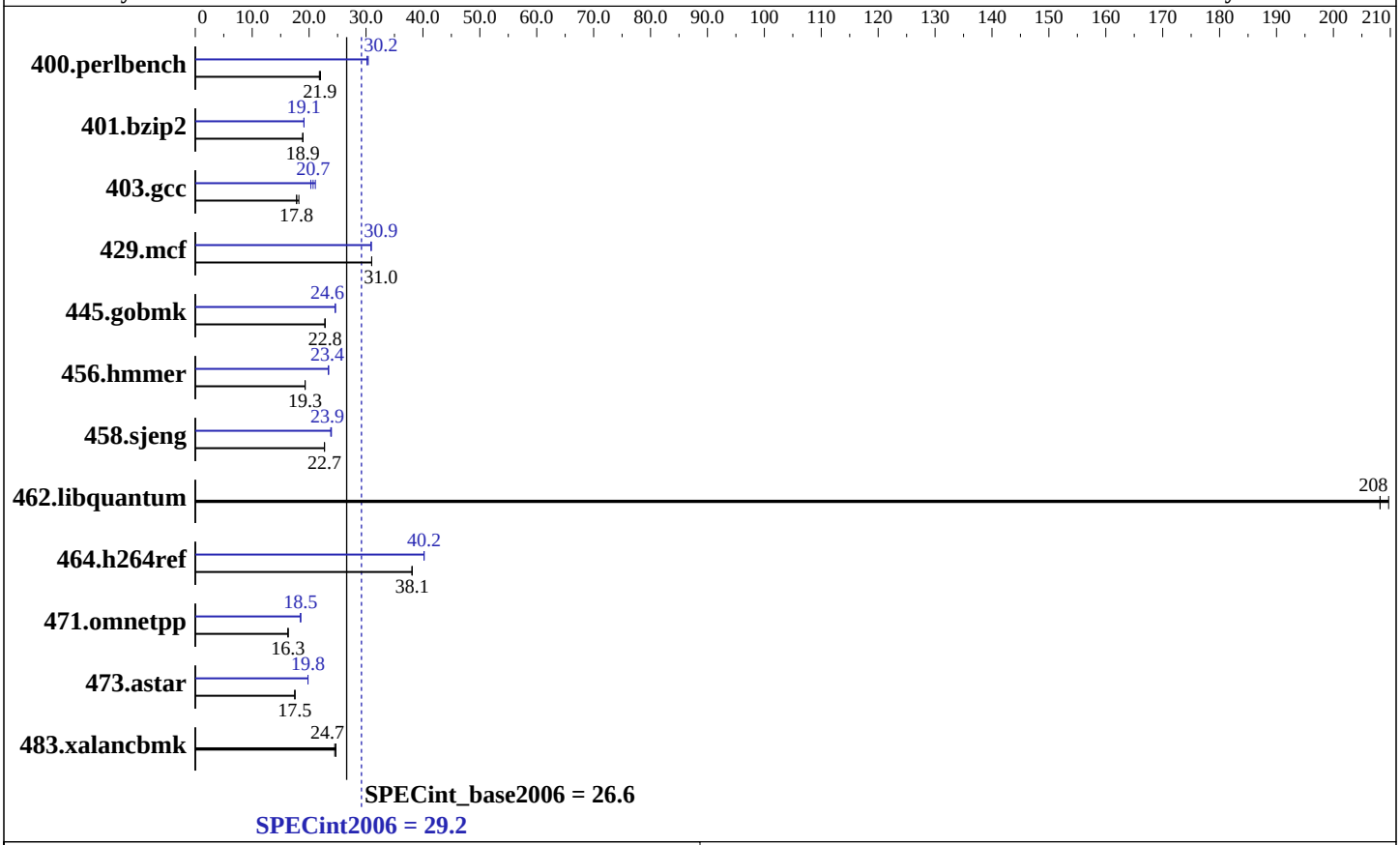
Dell Precision T7400 (Intel Xeon X5272, 3.40 GHz)

SPECint®2006 = 29.2

SPECint_base2006 = 26.6

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Dec-2008
Hardware Availability: Sep-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X5272
CPU Characteristics:	1600 MHz Bus Speed
CPU MHz:	3400
FPU:	Integrated
CPU(s) enabled:	4 cores, 2 chips, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip
L3 Cache:	None
Other Cache:	None
Memory:	16 GB (8x2 GB DDR2-800 FB-DIMM, CL5)
Disk Subsystem:	1 x 80 GB SATA 7200 RPM
Other Hardware:	None

Software

Operating System:	Windows Vista Business SP1 (64-bit)
Compiler:	Intel C++ Compiler for IA-32, Version 11.0 Build 20080930 Package ID: w_cproc_p_11.0.061 Microsoft Visual Studio 2008 SP1
Auto Parallel:	Yes
File System:	NTFS
System State:	Default
Base Pointers:	32-bit
Peak Pointers:	32-bit
Other Software:	MicroQuill SmartHeap Library 8.1

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Results Table

Benchmark	Base						Peak					
	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
400.perlbench	447	21.8	**445**	**21.9**	444	22.0	321	30.4	324	30.2	**323**	**30.2**
401.bzip2	510	18.9	**511**	**18.9**	511	18.9	504	19.1	504	19.1	**504**	**19.1**
403.gcc	**451**	**17.8**	442	18.2	452	17.8	381	21.1	396	20.3	**389**	**20.7**
429.mcf	294	31.0	294	31.0	**294**	**31.0**	**295**	**30.9**	295	30.9	295	30.9
445.gobmk	460	22.8	459	22.8	**460**	**22.8**	427	24.6	**427**	**24.6**	427	24.6
456.hmmer	484	19.3	**485**	**19.3**	485	19.3	399	23.4	398	23.4	**398**	**23.4**
458.sjeng	533	22.7	533	22.7	**533**	**22.7**	507	23.8	**507**	**23.9**	507	23.9
462.libquantum	98.8	210	**99.5**	**208**	99.5	208	98.8	210	**99.5**	**208**	99.5	208
464.h264ref	**581**	**38.1**	581	38.1	581	38.1	551	40.2	**551**	**40.2**	551	40.2
471.omnetpp	384	16.3	384	16.3	**384**	**16.3**	337	18.5	337	18.5	**337**	**18.5**
473.astar	400	17.5	400	17.5	**400**	**17.5**	354	19.8	**355**	**19.8**	355	19.8
483.xalancbmk	282	24.5	280	24.7	**280**	**24.7**	282	24.5	280	24.7	**280**	**24.7**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

General Notes

```
Binaries were built on Windows Vista Ultimate (64-bit)

BIOS Settings
  Adjacent Cache Line Prefetch set to ON
```

Base Compiler Invocation

C benchmarks:

```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:

```
icl -Qvc9
```

Base Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Base Optimization Flags

C benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qparallel
-Qpar-runtime-control -Qvec-guard-write /F512000000
```

C++ benchmarks:
```
-QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch -Qcxx_features
/F512000000 shlw32m.lib            -link /FORCE:MULTIPLE
```

Base Other Flags

C benchmarks:

403.gcc: `-Dalloca=_alloca`

Peak Compiler Invocation

C benchmarks:
```
icl -Qvc9 -Qstd=c99
```

C++ benchmarks:
```
icl -Qvc9
```

Peak Portability Flags

403.gcc: `-DSPEC_CPU_WIN32`
464.h264ref: `-DSPEC_CPU_NO_INTTYPES -DWIN32`
483.xalancbmk: `-Qoption,cpp,--no_wchar_t_keyword`

Peak Optimization Flags

C benchmarks:

400.perlbench: `-Qprof_gen`(pass 1) `-QxSSE4.1 -Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000 shlw32m.lib -link /FORCE:MULTIPLE`

401.bzip2: `-Qprof_gen`(pass 1) `-QxSSE4.1`(pass 2) `-Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-prefetch /F512000000`

403.gcc: `-Qprof_gen`(pass 1) `-QxSSE4.1 -Qprof_use`(pass 2) `-Qipo -O3 -Qprec-div- /F512000000`

Continued on next page

Peak Optimization Flags (Continued)

429.mcf: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qopt-prefetch /F512000000

445.gobmk: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2) -Qipo -O2 -Qprec-div- -Qansi-alias /F512000000

456.hmmer: -QxSSE4.1 -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

458.sjeng: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll4 /F512000000

462.libquantum: basepeak = yes

464.h264ref: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qunroll2 -Qansi-alias /F512000000

C++ benchmarks:

471.omnetpp: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=block /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

473.astar: -Qprof_gen(pass 1) -QxSSE4.1(pass 2) -Qprof_use(pass 2) -Qipo -O3 -Qprec-div- -Qansi-alias -Qopt-ra-region-strategy=routine /F512000000 shlw32m.lib -link /FORCE:MULTIPLE

483.xalancbmk: basepeak = yes

Peak Other Flags

C benchmarks:

403.gcc: -Dalloca=_alloca

The flags file that was used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.html

You can also download the XML flags source by saving the following link:
http://www.spec.org/cpu2006/flags/dell.ic11.0.windows.flags.xml

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