SPEC® CFP2006 Result

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Dell Inc.

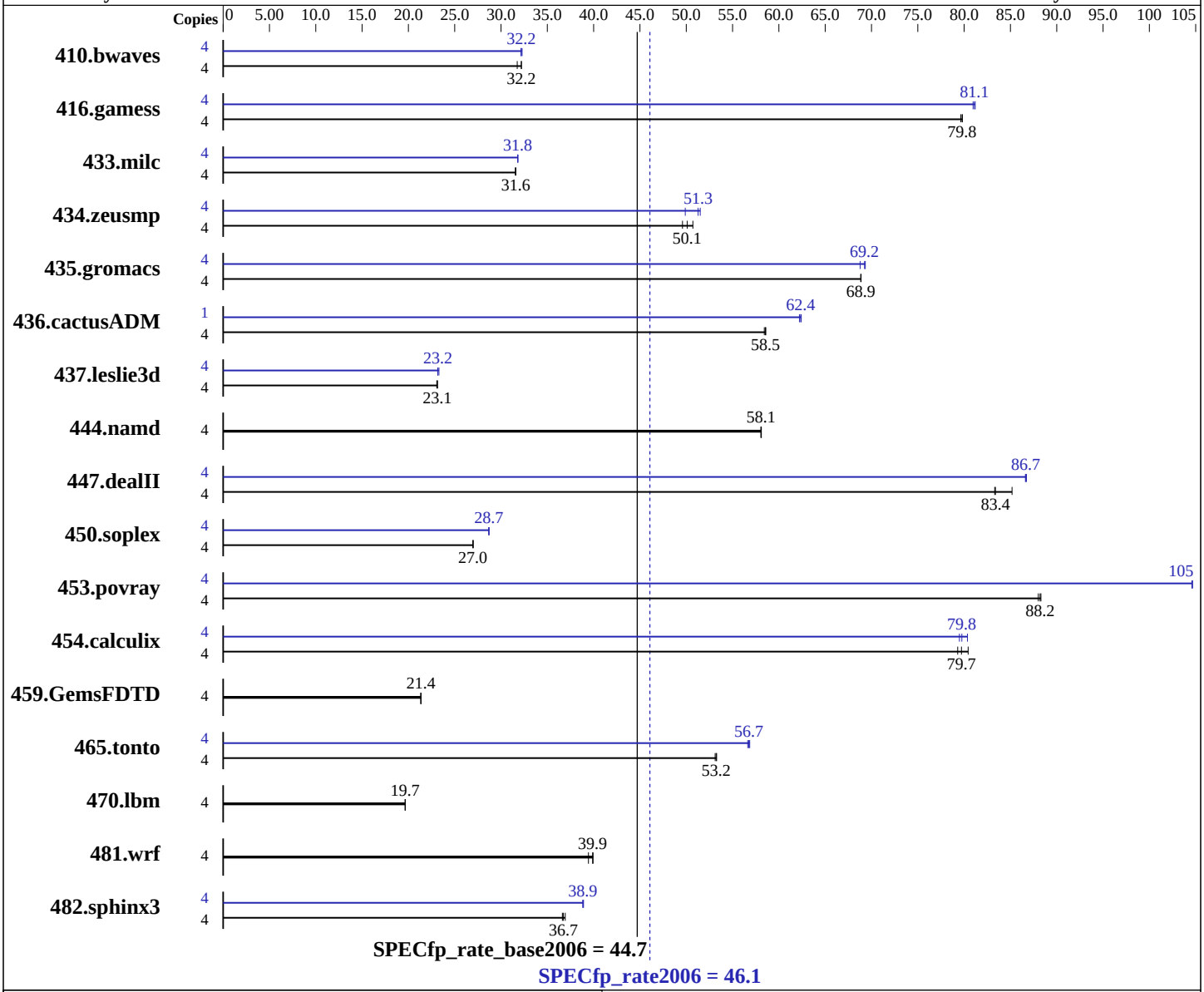
PowerEdge T100 (Intel Xeon X3330, 2.66 GHz)

SPECfp®_rate2006 = 46.1

SPECfp_rate_base2006 = 44.7

CPU2006 license: 55
Test sponsor: Dell Inc.
Tested by: Dell Inc.

Test date: Nov-2008
Hardware Availability: Oct-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon X3330
CPU Characteristics:	
CPU MHz:	2666
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1 chip
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip, 3 MB shared / 2 cores

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20080730 Package ID: l_cproc_b_11.0.042, l_fproc_b_11.0.042
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 8 GB (4 x 2 GB DDR2-800)
Disk Subsystem: 1 x 160 GB 7200 RPM SATA
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	1712	31.7	**1689**	**32.2**	1688	32.2	4	1691	32.2	1684	32.3	**1689**	**32.2**
416.gamess	4	981	79.8	**982**	**79.8**	983	79.6	4	**965**	**81.1**	965	81.2	967	81.0
433.milc	4	**1163**	**31.6**	1163	31.6	1164	31.6	4	1154	31.8	**1154**	**31.8**	1154	31.8
434.zeusmp	4	718	50.7	**727**	**50.1**	734	49.6	4	**710**	**51.3**	730	49.9	707	51.5
435.gromacs	4	415	68.9	415	68.8	**415**	**68.9**	4	**412**	**69.2**	412	69.3	415	68.8
436.cactusADM	4	818	58.4	816	58.6	**817**	**58.5**	1	192	62.2	**192**	**62.4**	191	62.4
437.leslie3d	4	1625	23.1	1630	23.1	**1626**	**23.1**	4	**1622**	**23.2**	1623	23.2	1614	23.3
444.namd	4	**552**	**58.1**	552	58.1	553	58.1	4	**552**	**58.1**	552	58.1	553	58.1
447.dealII	4	**549**	**83.4**	537	85.2	549	83.3	4	528	86.6	528	86.7	**528**	**86.7**
450.soplex	4	1236	27.0	1237	27.0	**1237**	**27.0**	4	**1163**	**28.7**	1163	28.7	1162	28.7
453.povray	4	**241**	**88.2**	241	88.3	242	88.0	4	203	105	203	105	**203**	**105**
454.calculix	4	**414**	**79.7**	416	79.3	410	80.4	4	415	79.5	**414**	**79.8**	411	80.4
459.GemsFDTD	4	1987	21.4	1988	21.3	**1988**	**21.4**	4	1987	21.4	1988	21.3	**1988**	**21.4**
465.tonto	4	**739**	**53.2**	739	53.3	741	53.1	4	692	56.9	695	56.6	**694**	**56.7**
470.lbm	4	2796	19.7	**2796**	**19.7**	2796	19.7	4	2796	19.7	**2796**	**19.7**	2796	19.7
481.wrf	4	1133	39.4	1119	39.9	**1120**	**39.9**	4	1133	39.4	1119	39.9	**1120**	**39.9**
482.sphinx3	4	2129	36.6	2111	36.9	**2122**	**36.7**	4	2004	38.9	2008	38.8	**2007**	**38.9**

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stack size to unlimited prior to run
```

Platform Notes

```
BIOS Settings:
Hardware Prefetcher = Disabled (Default = Enabled)
Adjacent Cache Line Prefetch = Disabled (Default = Enabled)
```

General Notes

```
taskset was used to bind processes to cores except
for 436.cactusADM peak
OMP_NUM_THREADS set to number of processors
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

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Base Optimization Flags (Continued)

C++ benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Fortran benchmarks:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Benchmarks using both Fortran and C:
`-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch`

Peak Compiler Invocation

C benchmarks (except as noted below):
`icc`

482.sphinx3: `/opt/intel/Compiler/11.0/042/bin/ia32/icc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

C++ benchmarks (except as noted below):
`icpc`

450.soplex: `/opt/intel/Compiler/11.0/042/bin/ia32/icpc -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Fortran benchmarks (except as noted below):
`ifort`

437.leslie3d: `/opt/intel/Compiler/11.0/042/bin/ia32/ifort -L/opt/intel/Compiler/11.0/042/ipp/ia32/lib -I/opt/intel/Compiler/11.0/042/ipp/ia32/include`

Benchmarks using both Fortran and C:
`icc ifort`

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`

Continued on next page

Peak Portability Flags (Continued)

453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: basepeak = yes

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: basepeak = yes

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: basepeak = yes

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Peak Optimization Flags (Continued)

465.tonto: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto`

Benchmarks using both Fortran and C:

435.gromacs: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32`

436.cactusADM: `-prof-gen`(pass 1) `-prof-use`(pass 2) `-xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32`

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.05.html`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090713.05.xml`
`http://www.spec.org/cpu2006/flags/Intel-Linux64-Platform.20090713.11.xml`

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Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 21:39:07 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 24 December 2008.