SPEC® CFP2006 Result

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NEC Corporation

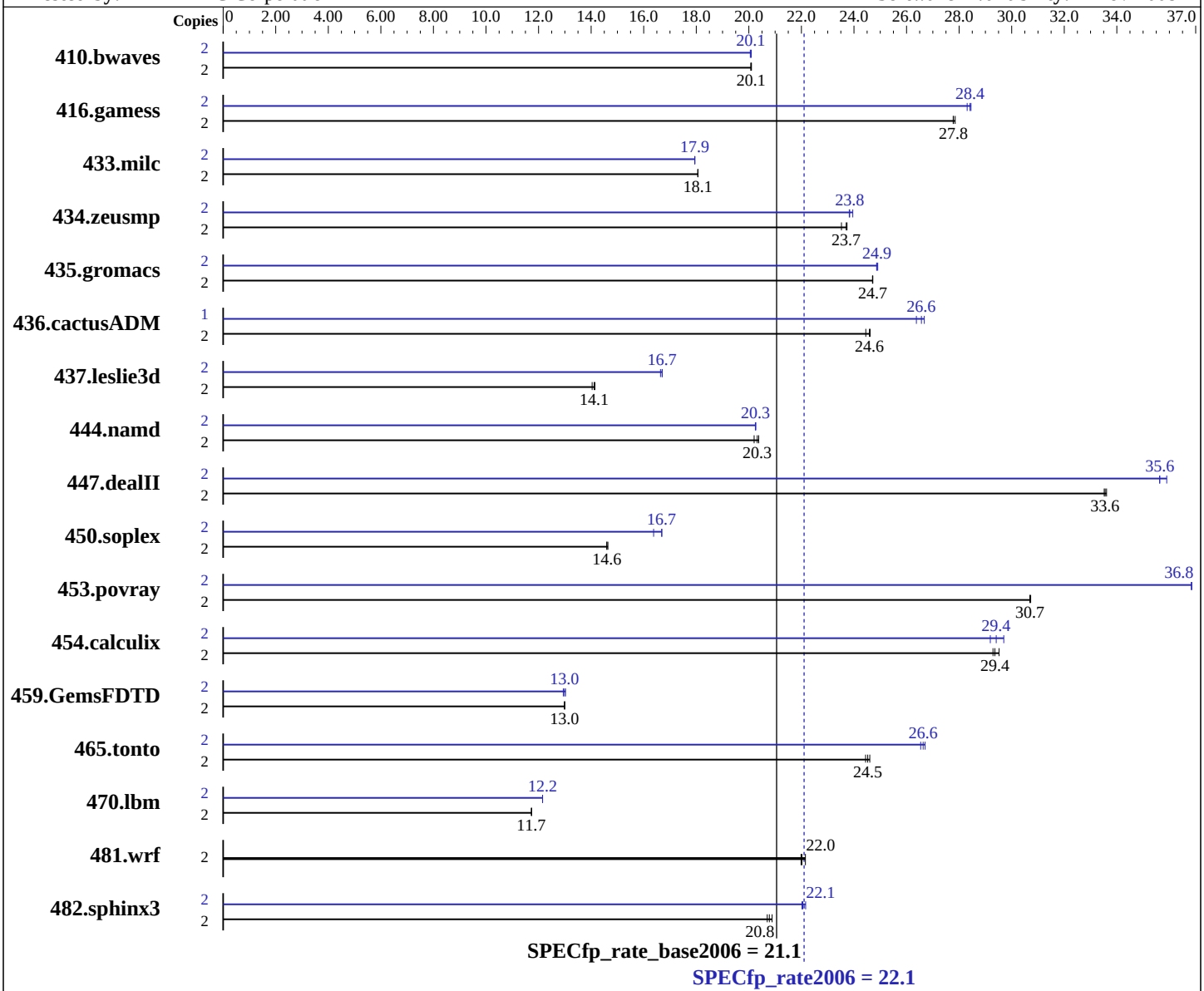
Express5800/120Bb-m6
(Intel Xeon E5205)

SPECfp®_rate2006 = 22.1

SPECfp_rate_base2006 = 21.1

CPU2006 license: 9006
Test sponsor: NEC Corporation
Tested by: NEC Corporation

Test date: Dec-2008
Hardware Availability: Dec-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5205
CPU Characteristics:	1066 MHz system bus
CPU MHz:	1867
FPU:	Integrated
CPU(s) enabled:	2 cores, 1 chip, 2 cores/chip
CPU(s) orderable:	1,2 chips
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	6 MB I+D on chip per chip

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Software

Operating System:	SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smp
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074, l_cprof_p_11.0.074
Auto Parallel:	Yes
File System:	ReiserFS
System State:	Run level 3 (multi-user)
Base Pointers:	64-bit

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L3 Cache: None
Other Cache: None
Memory: 16 GB (8x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 1x73.2 GB SAS, 10000 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	2	1354	20.1	1352	20.1	**1353**	**20.1**	2	1353	20.1	**1354**	**20.1**	1355	20.1
416.gamess	2	1406	27.9	**1409**	**27.8**	1410	27.8	2	1376	28.5	**1378**	**28.4**	1383	28.3
433.milc	2	1016	18.1	**1016**	**18.1**	1017	18.1	2	**1023**	**17.9**	1023	17.9	1023	17.9
434.zeusmp	2	767	23.7	774	23.5	**768**	**23.7**	2	**764**	**23.8**	764	23.8	760	24.0
435.gromacs	2	578	24.7	**578**	**24.7**	578	24.7	2	573	24.9	574	24.9	**574**	**24.9**
436.cactusADM	2	978	24.4	**972**	**24.6**	971	24.6	1	448	26.7	**450**	**26.6**	453	26.4
437.leslie3d	2	1329	14.1	1339	14.0	**1332**	**14.1**	2	**1126**	**16.7**	1130	16.6	1125	16.7
444.namd	2	794	20.2	**789**	**20.3**	787	20.4	2	791	20.3	792	20.2	**792**	**20.3**
447.dealII	2	681	33.6	**682**	**33.6**	683	33.5	2	642	35.6	637	35.9	**642**	**35.6**
450.soplex	2	1139	14.6	**1142**	**14.6**	1143	14.6	2	999	16.7	**1000**	**16.7**	1019	16.4
453.povray	2	**347**	**30.7**	346	30.7	347	30.7	2	**289**	**36.8**	289	36.8	289	36.8
454.calculix	2	563	29.3	559	29.5	**562**	**29.4**	2	**561**	**29.4**	565	29.2	556	29.7
459.GemsFDTD	2	1633	13.0	**1634**	**13.0**	1634	13.0	2	1629	13.0	**1634**	**13.0**	1640	12.9
465.tonto	2	800	24.6	805	24.4	**803**	**24.5**	2	742	26.5	737	26.7	**739**	**26.6**
470.lbm	2	2341	11.7	**2343**	**11.7**	2343	11.7	2	**2261**	**12.2**	2262	12.2	2261	12.2
481.wrf	2	1008	22.2	1016	22.0	**1015**	**22.0**	2	1008	22.2	1016	22.0	**1015**	**22.0**
482.sphinx3	2	1866	20.9	**1876**	**20.8**	1883	20.7	2	1759	22.2	**1768**	**22.1**	1770	22.0

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 436.cactusADM peak
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
```
icc
```

482.sphinx3: `/opt/intel/Compiler/11.0/074/bin/ia32/icc -L/opt/intel/Compiler/11.0/074/ipp/ia32/lib -I/opt/intel/Compiler/11.0/074/ipp/ia32/include`

C++ benchmarks (except as noted below):
```
icpc
```

450.soplex: `/opt/intel/Compiler/11.0/074/bin/ia32/icpc -L/opt/intel/Compiler/11.0/074/ipp/ia32/lib -I/opt/intel/Compiler/11.0/074/ipp/ia32/include`

Fortran benchmarks (except as noted below):
```
ifort
```

437.leslie3d: `/opt/intel/Compiler/11.0/074/bin/ia32/ifort -L/opt/intel/Compiler/11.0/074/ipp/ia32/lib -I/opt/intel/Compiler/11.0/074/ipp/ia32/include`

Benchmarks using both Fortran and C:
```
icc ifort
```

Peak Portability Flags

410.bwaves: `-DSPEC_CPU_LP64`
416.gamess: `-DSPEC_CPU_LP64`
433.milc: `-DSPEC_CPU_LP64`
434.zeusmp: `-DSPEC_CPU_LP64`
435.gromacs: `-DSPEC_CPU_LP64 -nofor_main`
436.cactusADM: `-DSPEC_CPU_LP64 -nofor_main`
444.namd: `-DSPEC_CPU_LP64`
447.dealII: `-DSPEC_CPU_LP64`
453.povray: `-DSPEC_CPU_LP64`
454.calculix: `-DSPEC_CPU_LP64 -nofor_main`
459.GemsFDTD: `-DSPEC_CPU_LP64`
465.tonto: `-DSPEC_CPU_LP64`
470.lbm: `-DSPEC_CPU_LP64`
481.wrf: `-DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX`

Peak Optimization Flags

C benchmarks:

Continued on next page

Peak Optimization Flags (Continued)

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2 -opt-prefetch -parallel -auto-ilp32

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Peak Optimization Flags (Continued)

454.calculix: `-xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32`

481.wrf: `basepeak = yes`

The flags files that were used to format this result can be browsed at
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.html`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html`

You can also download the XML flags sources by saving the following links:
`http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.xml`
`http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml`

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