SPEC® CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

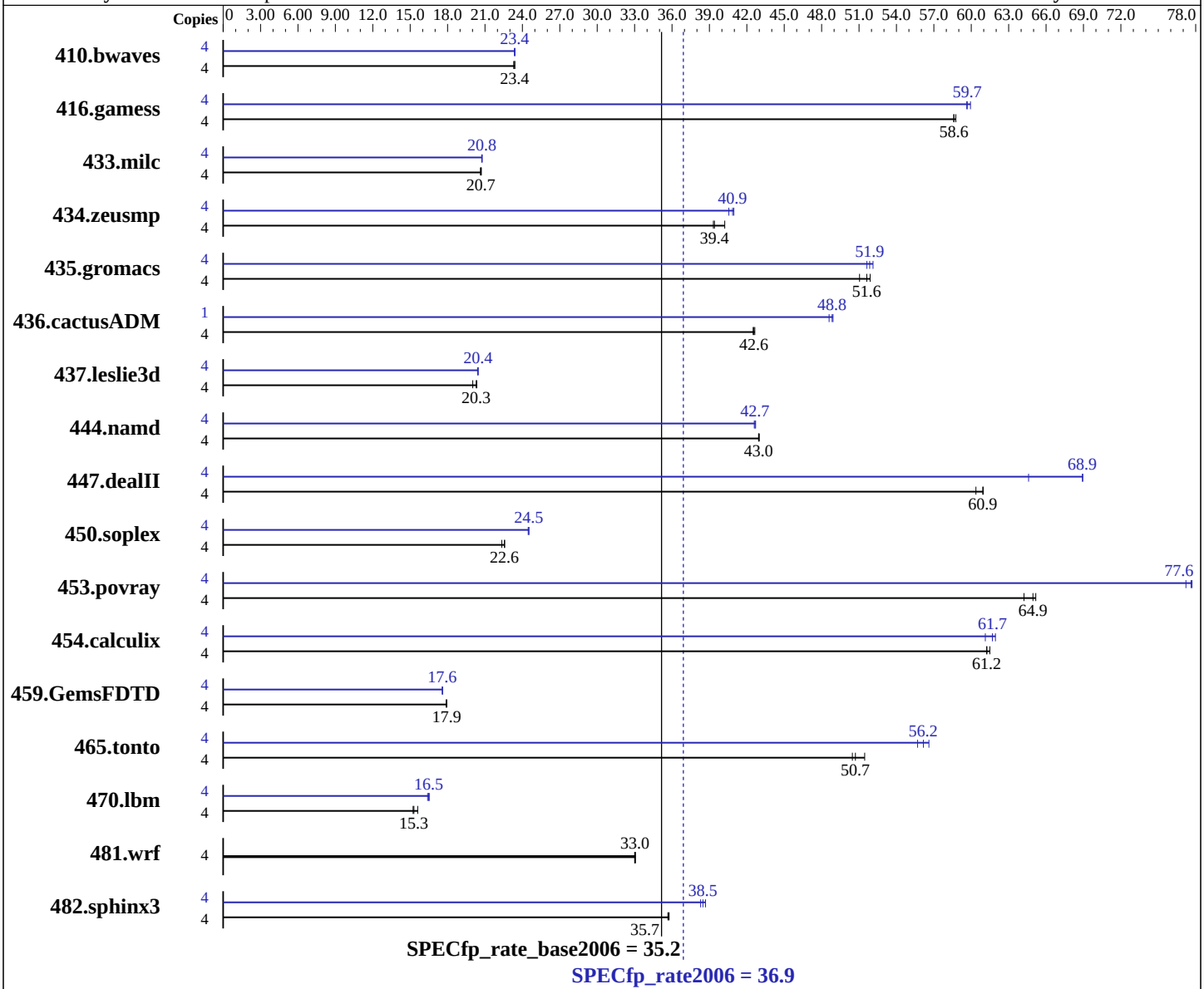
NovaScale R630 E1 LR
(Intel Xeon E5405, 2.00 GHz)

SPECfp®_rate2006 = 36.9

SPECfp_rate_base2006 = 35.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Dec-2008
Hardware Availability: Oct-2008
Software Availability: Nov-2008



Hardware

CPU Name:	Intel Xeon E5405
CPU Characteristics:	1333 MHz system bus
CPU MHz:	2000
FPU:	Integrated
CPU(s) enabled:	4 cores, 1 chip, 4 cores/chip
CPU(s) orderable:	1,2 chips (fault tolerant, see Platform Notes)
Primary Cache:	32 KB I + 32 KB D on chip per core
Secondary Cache:	12 MB I+D on chip per chip, 6 MB shared / 2 cores

Continued on next page

Software

Operating System:	Red Hat Enterprise Linux Server release 5.2 Advanced Platform, Kernel 2.6.18-92.1.13.el5 on an x86_64
Compiler:	Intel C++ and Fortran Compiler 11.0 for Linux Build 20081105 Package ID: l_cproc_p_11.0.074, l_cprof_p_11.0.074
Auto Parallel:	Yes
File System:	ext3
System State:	Run level 3 (multi-user)

Continued on next page

SPEC CFP2006 Result

Copyright 2006-2014 Standard Performance Evaluation Corporation

Bull SAS

NovaScale R630 E1 LR
(Intel Xeon E5405, 2.00 GHz)

SPECfp_rate2006 = 36.9

SPECfp_rate_base2006 = 35.2

CPU2006 license: 20
Test sponsor: Bull SAS
Tested by: NEC Corporation

Test date: Dec-2008
Hardware Availability: Oct-2008
Software Availability: Nov-2008

L3 Cache: None
Other Cache: None
Memory: 12 GB (6x2 GB PC2-5300F, 2 rank, CL5-5-5, ECC)
Disk Subsystem: 2x146.5 GB SAS, 15000 RPM, Software RAID Level1
Other Hardware: None

Base Pointers: 64-bit
Peak Pointers: 32/64-bit
Other Software: ft Server Control Software 6.0.2-198

Results Table

Benchmark	Base							Peak						
	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio	Copies	Seconds	Ratio	Seconds	Ratio	Seconds	Ratio
410.bwaves	4	2333	23.3	**2325**	**23.4**	2325	23.4	4	2326	23.4	**2326**	**23.4**	2322	23.4
416.gamess	4	1333	58.8	1337	58.6	**1336**	**58.6**	4	1313	59.6	1307	59.9	**1312**	**59.7**
433.milc	4	1780	20.6	**1776**	**20.7**	1773	20.7	4	1771	20.7	1768	20.8	**1768**	**20.8**
434.zeusmp	4	926	39.3	**924**	**39.4**	905	40.2	4	897	40.6	**891**	**40.9**	889	41.0
435.gromacs	4	560	51.0	550	51.9	**553**	**51.6**	4	548	52.1	553	51.6	**551**	**51.9**
436.cactusADM	4	1121	42.6	**1123**	**42.6**	1125	42.5	1	246	48.6	**245**	**48.8**	244	48.9
437.leslie3d	4	1878	20.0	**1853**	**20.3**	1848	20.3	4	**1839**	**20.4**	1843	20.4	1838	20.5
444.namd	4	746	43.0	747	42.9	**747**	**43.0**	4	751	42.7	**752**	**42.7**	753	42.6
447.dealII	4	**751**	**60.9**	758	60.4	751	61.0	4	**664**	**68.9**	708	64.6	664	69.0
450.soplex	4	1493	22.4	1477	22.6	**1479**	**22.6**	4	1360	24.5	**1361**	**24.5**	1364	24.5
453.povray	4	326	65.2	**328**	**64.9**	331	64.2	4	274	77.7	**274**	**77.6**	276	77.2
454.calculix	4	539	61.2	**539**	**61.2**	537	61.5	4	533	61.9	**535**	**61.7**	540	61.1
459.GemsFDTD	4	**2367**	**17.9**	2374	17.9	2367	17.9	4	2415	17.6	2412	17.6	**2413**	**17.6**
465.tonto	4	780	50.5	**776**	**50.7**	765	51.5	4	707	55.7	**701**	**56.2**	695	56.6
470.lbm	4	**3592**	**15.3**	3608	15.2	3522	15.6	4	3351	16.4	3325	16.5	**3334**	**16.5**
481.wrf	4	1354	33.0	**1353**	**33.0**	1351	33.1	4	1354	33.0	**1353**	**33.0**	1351	33.1
482.sphinx3	4	2180	35.8	**2182**	**35.7**	2186	35.7	4	2036	38.3	**2026**	**38.5**	2015	38.7

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

```
The config file option 'submit' was used.
 taskset was used to bind processes to cores except
 for 436.cactusADM peak
```

Operating System Notes

```
'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run
OMP_NUM_THREADS set to number of cores
KMP_AFFINITY set to "physical,0"
KMP_STACKSIZE set to 64M
```

Platform Notes

This Express5800/320Fd-LR is a fault-tolerant server.
Two modules are installed in this server.
Each module physically has "1CPU chips,12GB memory", The total physical configuration is "2CPU chips,24GB memory".
Using fault-tolerant lockstep technology, these two modules communicate with each other and execute the same instructions at the same time, The operating system only sees "1CPU chips,12GB memory" as the other components add only redundancy and do not contribute to any performance benefit.

General Notes

The NEC Express5800/320Fd-LR(Intel Xeon E5405) and
the Bull NovaScale R630 E1 LR(Intel Xeon E5405, 2.00 GHz) models are electronically equivalent.
The results have been measured on a NEC Express5800/320Fd-LR(Intel Xeon E5405) model.

Base Compiler Invocation

C benchmarks:
icc

C++ benchmarks:
icpc

Fortran benchmarks:
ifort

Benchmarks using both Fortran and C:
icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64

Continued on next page

Base Portability Flags (Continued)

481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

C++ benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Fortran benchmarks:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Benchmarks using both Fortran and C:
-xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

Peak Compiler Invocation

C benchmarks (except as noted below):
icc

482.sphinx3: /opt/intel/Compiler/11.0/074/bin/ia32/icc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

C++ benchmarks (except as noted below):
icpc

450.soplex: /opt/intel/Compiler/11.0/074/bin/ia32/icpc
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Fortran benchmarks (except as noted below):
ifort

437.leslie3d: /opt/intel/Compiler/11.0/074/bin/ia32/ifort
-L/opt/intel/Compiler/11.0/074/ipp/ia32/lib
-I/opt/intel/Compiler/11.0/074/ipp/ia32/include

Benchmarks using both Fortran and C:
icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -nofor_main
436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -nofor_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias

470.lbm: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.1 -ipo -O3 -no-prec-div -static -unroll2

C++ benchmarks:

444.namd: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -fno-alias -auto-ilp32

447.dealII: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -ansi-alias -scalar-rep-

450.soplex: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3

453.povray: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.1 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -ansi-alias
-scalar-rep-

Continued on next page

Peak Optimization Flags (Continued)

434.zeusmp: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static

437.leslie3d: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-malloc-options=3 -opt-prefetch

459.GemsFDTD: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -Ob0 -opt-prefetch

465.tonto: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -opt-prefetch -auto-ilp32

436.cactusADM: -prof-gen(pass 1) -prof-use(pass 2) -xSSE4.1 -ipo -O3
-no-prec-div -static -unroll2 -opt-prefetch -parallel
-auto-ilp32

454.calculix: -xSSE4.1 -ipo -O3 -no-prec-div -static -auto-ilp32

481.wrf: basepeak = yes

The flags files that were used to format this result can be browsed at
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.html
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.html

You can also download the XML flags sources by saving the following links:
http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revE.20090710.xml
http://www.spec.org/cpu2006/flags/NEC-Intel-Linux-Settings-flags-revB.xml

SPEC and SPECfp are registered trademarks of the Standard Performance Evaluation Corporation. All other brand and product names appearing in this result are trademarks or registered trademarks of their respective holders.

For questions about this result, please contact the tester.
For other inquiries, please contact webmaster@spec.org.

Tested with SPEC CPU2006 v1.1.
Report generated on Tue Jul 22 22:25:35 2014 by SPEC CPU2006 PS/PDF formatter v6932.
Originally published on 20 January 2009.