



SPEC® CFP2006 Result

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Fujitsu

SPECfp®_rate2006 = 200

PRIMERGY RX300 S5, Intel Xeon X5570, 2.93 GHz

SPECfp_rate_base2006 = 193

CPU2006 license: 19

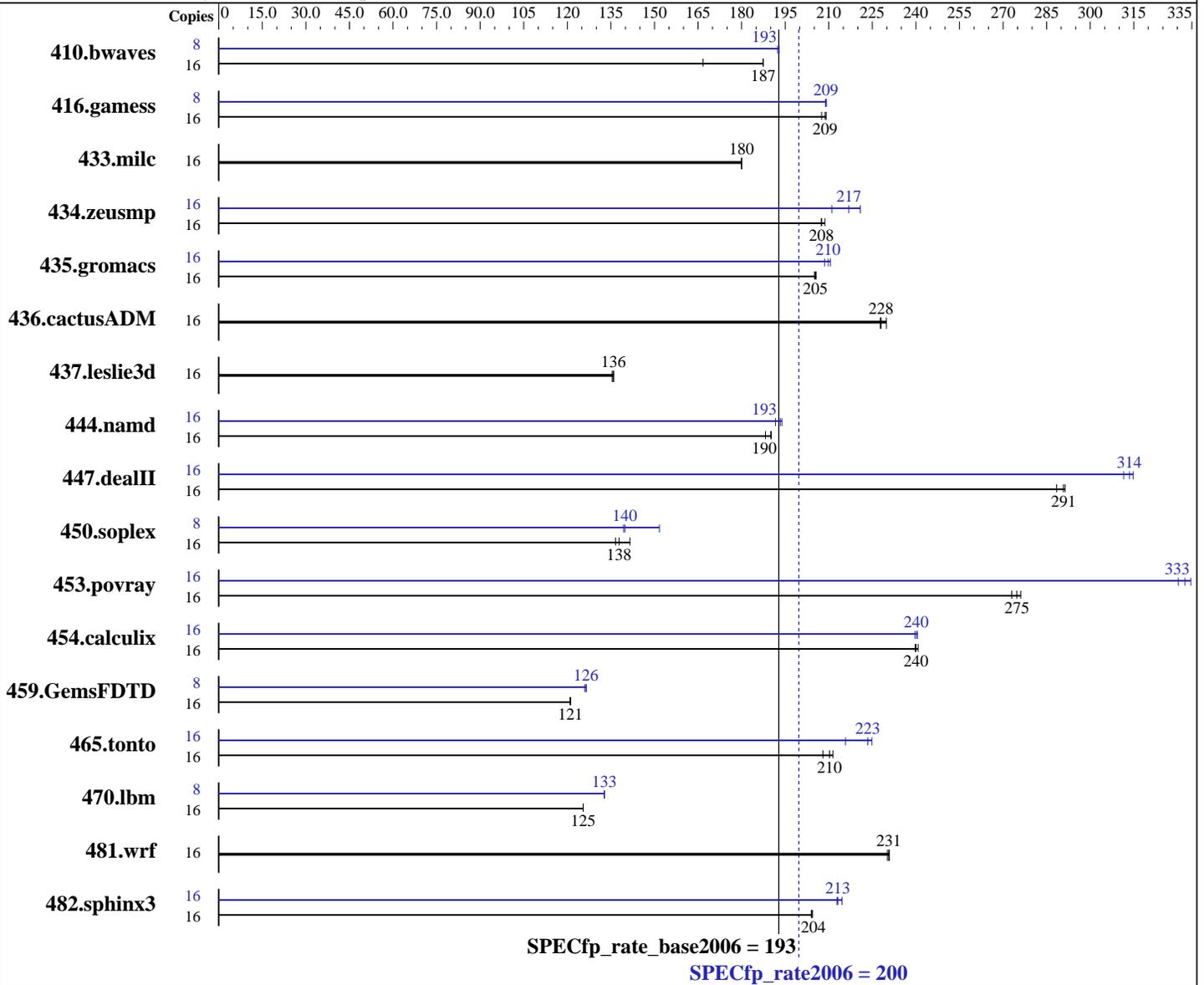
Test sponsor: Fujitsu

Tested by: Fujitsu Siemens Computers

Test date: Feb-2009

Hardware Availability: Apr-2009

Software Availability: Feb-2009



Hardware

CPU Name: Intel Xeon X5570
 CPU Characteristics: Intel Turbo Boost Technology up to 3.33 GHz
 CPU MHz: 2933
 FPU: Integrated
 CPU(s) enabled: 8 cores, 2 chips, 4 cores/chip, 2 threads/core
 CPU(s) orderable: 1,2 chips
 Primary Cache: 32 KB I + 32 KB D on chip per core
 Secondary Cache: 256 KB I+D on chip per core

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Software

Operating System: SUSE Linux Enterprise Server 10 (x86_64) SP2, Kernel 2.6.16.60-0.21-smpp
 Compiler: Intel C++ and Fortran Compiler 11.0 for Linux Build 20090131 Package ID: l_cproc_p_11.0.080, l_cprof_p_11.0.080
 Auto Parallel: No
 File System: ext3
 System State: Multi-User Run Level 3
 Base Pointers: 64-bit

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L3 Cache: 8 MB I+D on chip per chip
Other Cache: None
Memory: 24 GB (6x4 GB PC3-10600R, 2 rank, CL9-9-9, ECC)
Disk Subsystem: 1 x SATA, 250 GB, 7200 RPM
Other Hardware: None

Peak Pointers: 32/64-bit
Other Software: Binutils 2.18.50.0.7.20080502

Results Table

| Benchmark | Base | | | | | | | Peak | | | | | | |
|---------------|--------|--------------------|-------------------|--------------------|-------------------|--------------------|-------------------|--------|--------------------|-------------------|-------------------|-------------------|-------------------|-------------------|
| | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio | Copies | Seconds | Ratio | Seconds | Ratio | Seconds | Ratio |
| 410.bwaves | 16 | 1304 | 167 | <u>1160</u> | <u>187</u> | 1160 | 187 | 8 | 565 | 192 | 564 | 193 | <u>564</u> | <u>193</u> |
| 416.gamess | 16 | <u>1501</u> | <u>209</u> | 1510 | 208 | 1498 | 209 | 8 | 750 | 209 | 749 | 209 | <u>750</u> | <u>209</u> |
| 433.milc | 16 | 816 | 180 | <u>816</u> | <u>180</u> | 817 | 180 | 16 | 816 | 180 | <u>816</u> | <u>180</u> | 817 | 180 |
| 434.zeusmp | 16 | 698 | 209 | <u>702</u> | <u>208</u> | 702 | 207 | 16 | 659 | 221 | 690 | 211 | <u>671</u> | <u>217</u> |
| 435.gromacs | 16 | <u>556</u> | <u>205</u> | 557 | 205 | 556 | 206 | 16 | 542 | 211 | <u>544</u> | <u>210</u> | 548 | 209 |
| 436.cactusADM | 16 | 840 | 228 | <u>839</u> | <u>228</u> | 832 | 230 | 16 | 840 | 228 | <u>839</u> | <u>228</u> | 832 | 230 |
| 437.leslie3d | 16 | <u>1107</u> | <u>136</u> | 1111 | 135 | 1106 | 136 | 16 | <u>1107</u> | <u>136</u> | 1111 | 135 | 1106 | 136 |
| 444.namd | 16 | 682 | 188 | <u>675</u> | <u>190</u> | 674 | 190 | 16 | 662 | 194 | <u>664</u> | <u>193</u> | 670 | 192 |
| 447.dealII | 16 | 628 | 291 | <u>629</u> | <u>291</u> | 635 | 288 | 16 | 588 | 312 | 582 | 315 | <u>584</u> | <u>314</u> |
| 450.soplex | 16 | 942 | 142 | 977 | 137 | <u>968</u> | <u>138</u> | 8 | 479 | 139 | <u>477</u> | <u>140</u> | 440 | 152 |
| 453.povray | 16 | 312 | 273 | <u>310</u> | <u>275</u> | 308 | 276 | 16 | <u>256</u> | <u>333</u> | 258 | 330 | 254 | 335 |
| 454.calculix | 16 | 548 | 241 | <u>550</u> | <u>240</u> | 551 | 240 | 16 | <u>550</u> | <u>240</u> | 549 | 241 | 551 | 240 |
| 459.GemsFDTD | 16 | 1402 | 121 | 1404 | 121 | <u>1402</u> | <u>121</u> | 8 | 674 | 126 | 671 | 127 | <u>671</u> | <u>126</u> |
| 465.tonto | 16 | 744 | 211 | 757 | 208 | <u>749</u> | <u>210</u> | 16 | 730 | 216 | <u>705</u> | <u>223</u> | 700 | 225 |
| 470.lbm | 16 | 1752 | 125 | <u>1752</u> | <u>125</u> | 1752 | 125 | 8 | <u>828</u> | <u>133</u> | 828 | 133 | 828 | 133 |
| 481.wrf | 16 | <u>775</u> | <u>231</u> | 776 | 230 | 774 | 231 | 16 | <u>775</u> | <u>231</u> | 776 | 230 | 774 | 231 |
| 482.sphinx3 | 16 | <u>1527</u> | <u>204</u> | 1529 | 204 | 1526 | 204 | 16 | <u>1463</u> | <u>213</u> | 1466 | 213 | 1453 | 215 |

Results appear in the order in which they were run. Bold underlined text indicates a median measurement.

Submit Notes

The config file option 'submit' was used.
numactl was used to bind copies to the cores

Operating System Notes

'ulimit -s unlimited' was used to set the stacksize to unlimited prior to run

General Notes

This result was measured on the PRIMERGY TX300 S5. The PRIMERGY TX300 S5 and the PRIMERGY RX300 S5 are electronically equivalent.

For information about Fujitsu please visit: <http://www.fujitsu.com>



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Base Compiler Invocation

C benchmarks:

icc

C++ benchmarks:

icpc

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Base Portability Flags

410.bwaves: -DSPEC_CPU_LP64
416.gamess: -DSPEC_CPU_LP64
433.milc: -DSPEC_CPU_LP64
434.zeusmp: -DSPEC_CPU_LP64
435.gromacs: -DSPEC_CPU_LP64 -noFOR_main
436.cactusADM: -DSPEC_CPU_LP64 -noFOR_main
437.leslie3d: -DSPEC_CPU_LP64
444.namd: -DSPEC_CPU_LP64
447.dealII: -DSPEC_CPU_LP64
450.soplex: -DSPEC_CPU_LP64
453.povray: -DSPEC_CPU_LP64
454.calculix: -DSPEC_CPU_LP64 -noFOR_main
459.GemsFDTD: -DSPEC_CPU_LP64
465.tonto: -DSPEC_CPU_LP64
470.lbm: -DSPEC_CPU_LP64
481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX
482.sphinx3: -DSPEC_CPU_LP64

Base Optimization Flags

C benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

C++ benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Fortran benchmarks:

-xSSE4.2 -ipo -O3 -no-prec-div -static

Benchmarks using both Fortran and C:

-xSSE4.2 -ipo -O3 -no-prec-div -static



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Peak Compiler Invocation

C benchmarks (except as noted below):

icc

482.sphinx3: icc -m32

C++ benchmarks (except as noted below):

icpc

450.soplex: icpc -m32

Fortran benchmarks:

ifort

Benchmarks using both Fortran and C:

icc ifort

Peak Portability Flags

410.bwaves: -DSPEC_CPU_LP64
 416.gamess: -DSPEC_CPU_LP64
 433.milc: -DSPEC_CPU_LP64
 434.zeusmp: -DSPEC_CPU_LP64
 435.gromacs: -DSPEC_CPU_LP64 -nofor_main
 436.cactusADM: -DSPEC_CPU_LP64 -nofor_main
 437.leslie3d: -DSPEC_CPU_LP64
 444.namd: -DSPEC_CPU_LP64
 447.dealII: -DSPEC_CPU_LP64
 453.povray: -DSPEC_CPU_LP64
 454.calculix: -DSPEC_CPU_LP64 -nofor_main
 459.GemsFDTD: -DSPEC_CPU_LP64
 465.tonto: -DSPEC_CPU_LP64
 470.lbm: -DSPEC_CPU_LP64
 481.wrf: -DSPEC_CPU_LP64 -DSPEC_CPU_CASE_FLAG -DSPEC_CPU_LINUX

Peak Optimization Flags

C benchmarks:

433.milc: basepeak = yes

470.lbm: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch
-auto-ilp32

482.sphinx3: -xSSE4.2 -ipo -O3 -no-prec-div -static -unroll2

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Peak Optimization Flags (Continued)

C++ benchmarks:

444.namd: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-fno-alias -auto-ilp32

447.dealIII: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -ansi-alias -scalar-rep-

450.soplex: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-malloc-options=3

453.povray: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -ansi-alias

Fortran benchmarks:

410.bwaves: -xSSE4.2 -ipo -O3 -no-prec-div -static -opt-prefetch

416.gamess: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -ansi-alias -scalar-rep-

434.zeusmp: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)

437.leslie3d: basepeak = yes

459.GemsFDTD: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll2 -Ob0 -opt-prefetch

465.tonto: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-unroll4 -auto

Benchmarks using both Fortran and C:

435.gromacs: -xSSE4.2(pass 2) -prof-gen(pass 1) -ipo(pass 2) -O3(pass 2)
-no-prec-div(pass 2) -static(pass 2) -prof-use(pass 2)
-opt-prefetch -auto-ilp32

436.cactusADM: basepeak = yes

454.calculix: -xSSE4.2 -ipo -O3 -no-prec-div -static -auto-ilp32

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Peak Optimization Flags (Continued)

481.wrf: basepeak = yes

The flags file that was used to format this result can be browsed at

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.html>

You can also download the XML flags source by saving the following link:

<http://www.spec.org/cpu2006/flags/Intel-ic11.0-fp-linux64-revA.20090710.04.xml>

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